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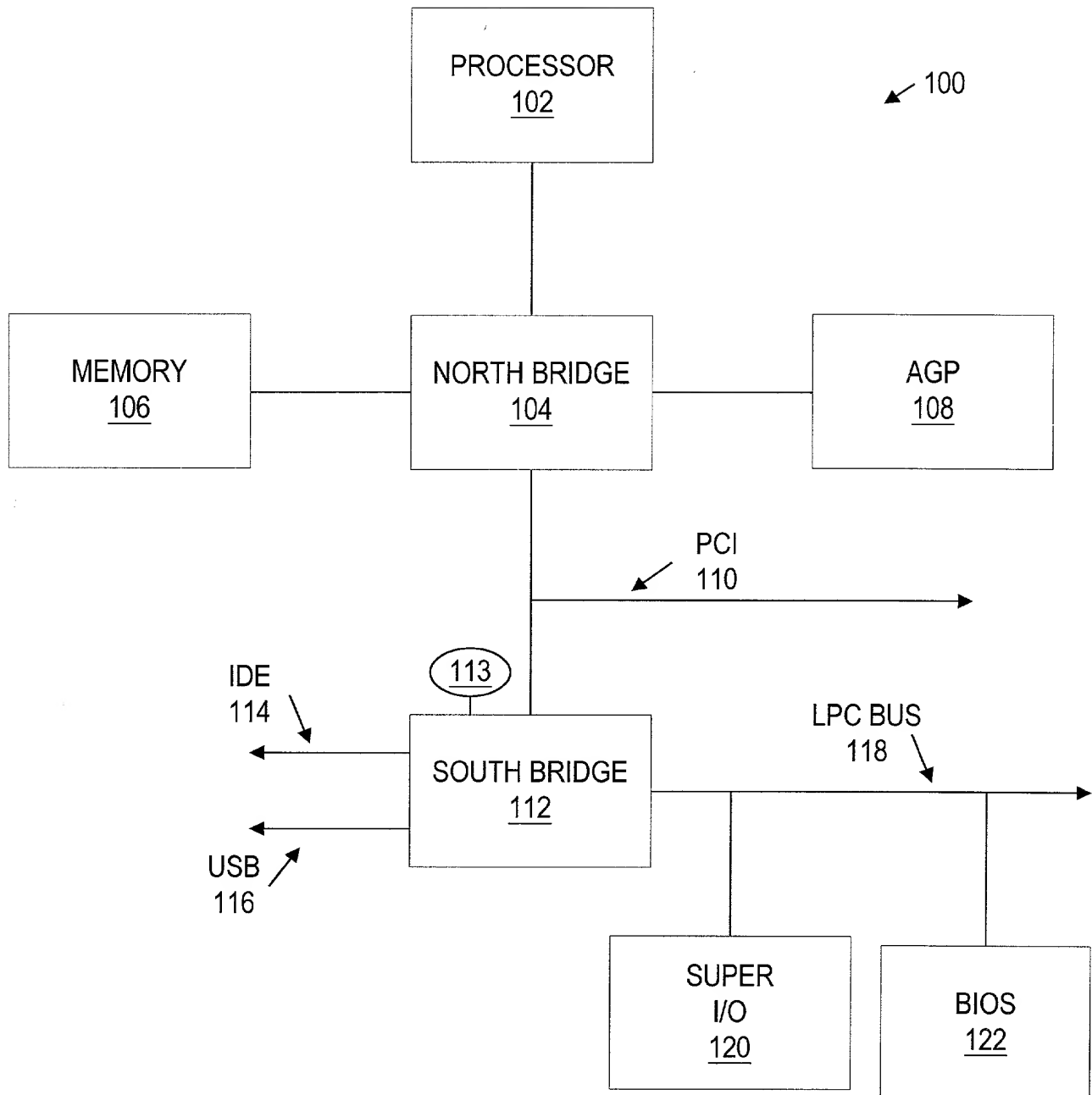


Fig. 1A
(Prior Art)

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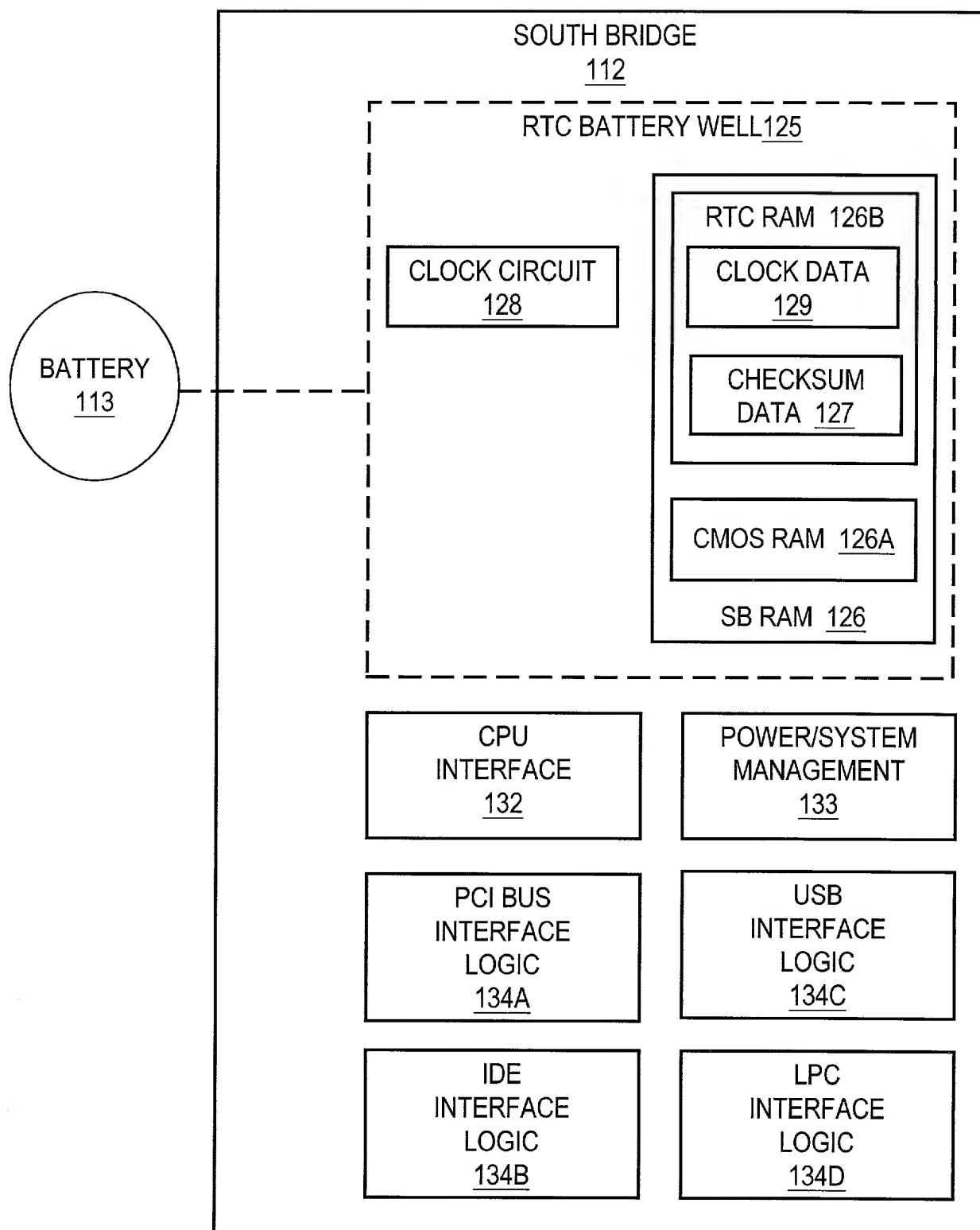


Fig. 1B
(Prior Art)

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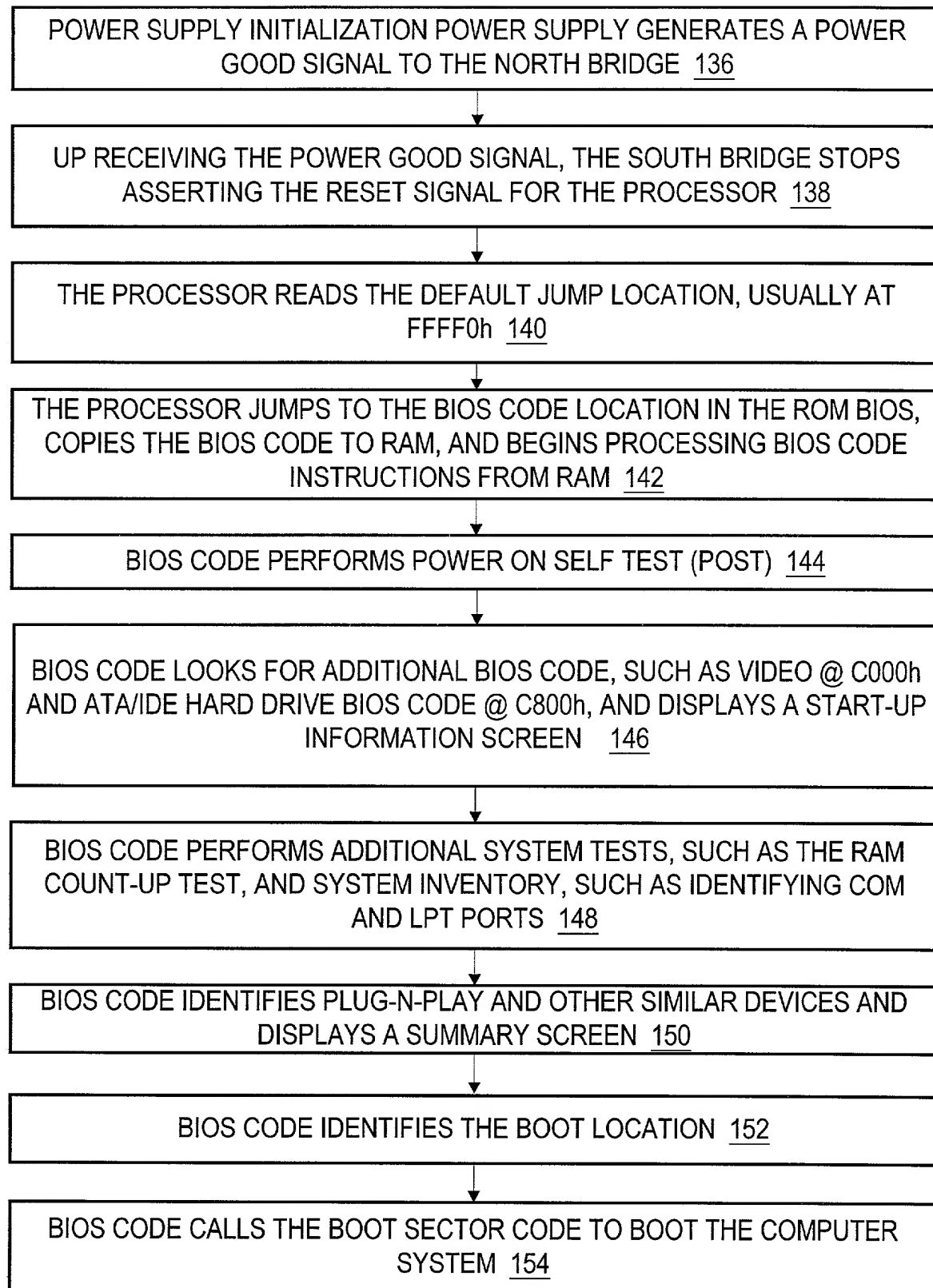


Fig. 2A
(Prior Art)

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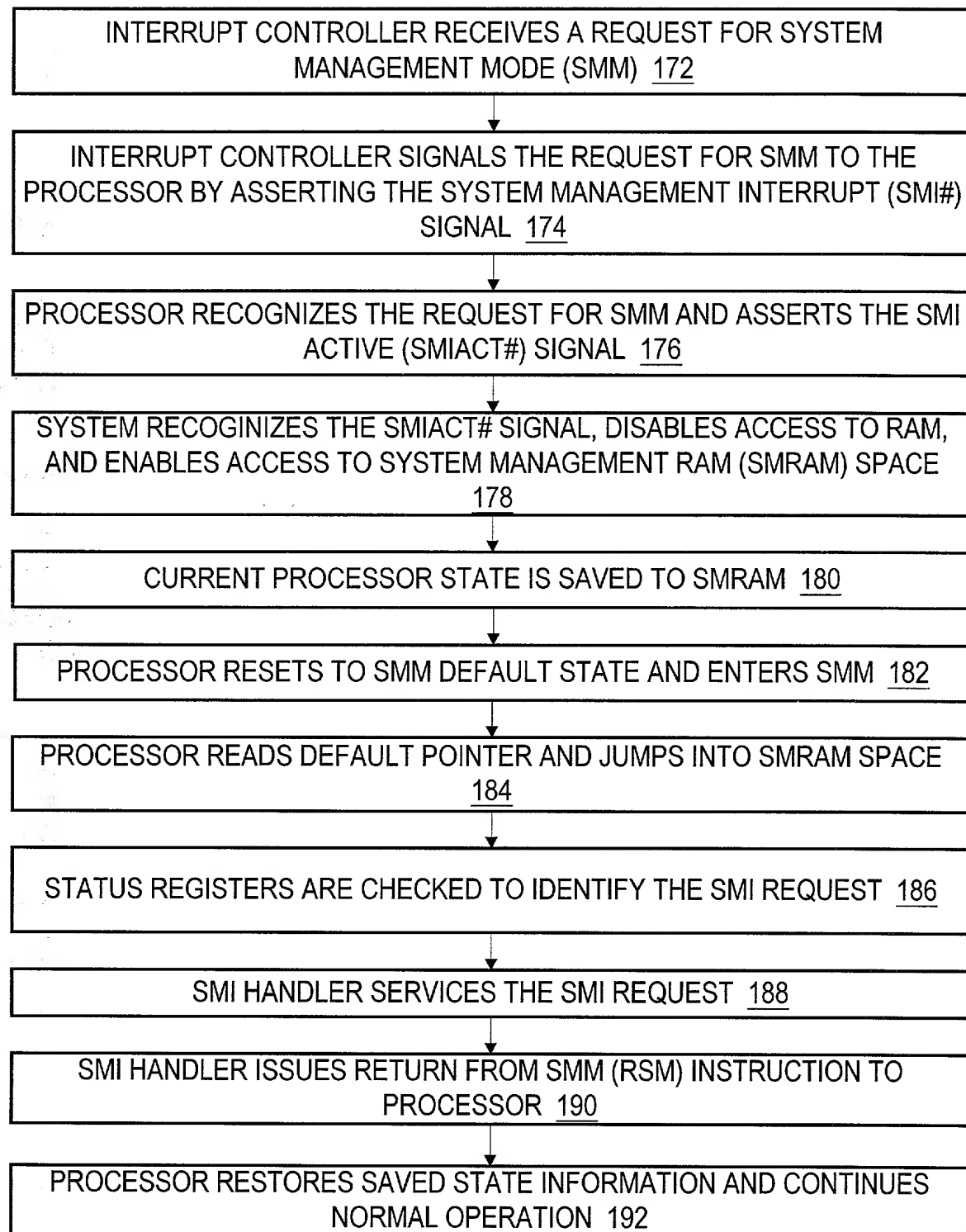


Fig. 2B
(Prior Art)

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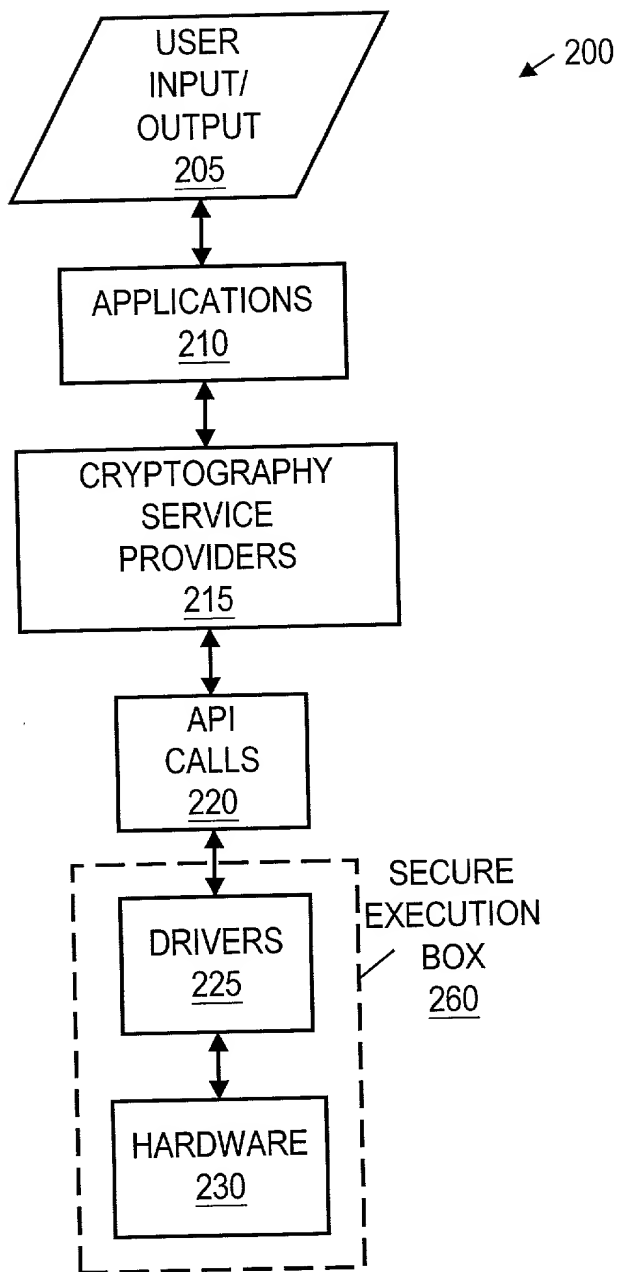


Fig. 3

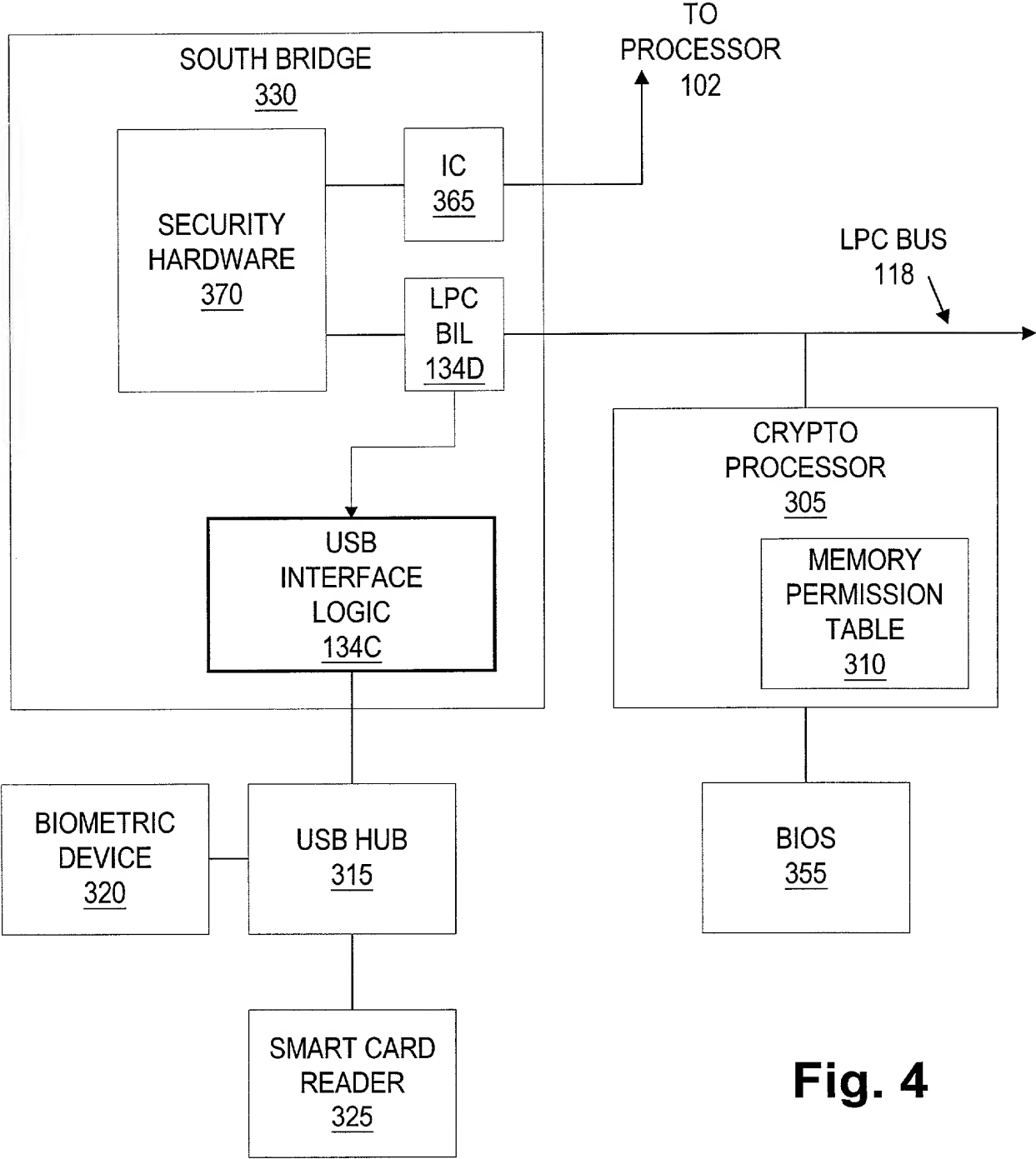


Fig. 4

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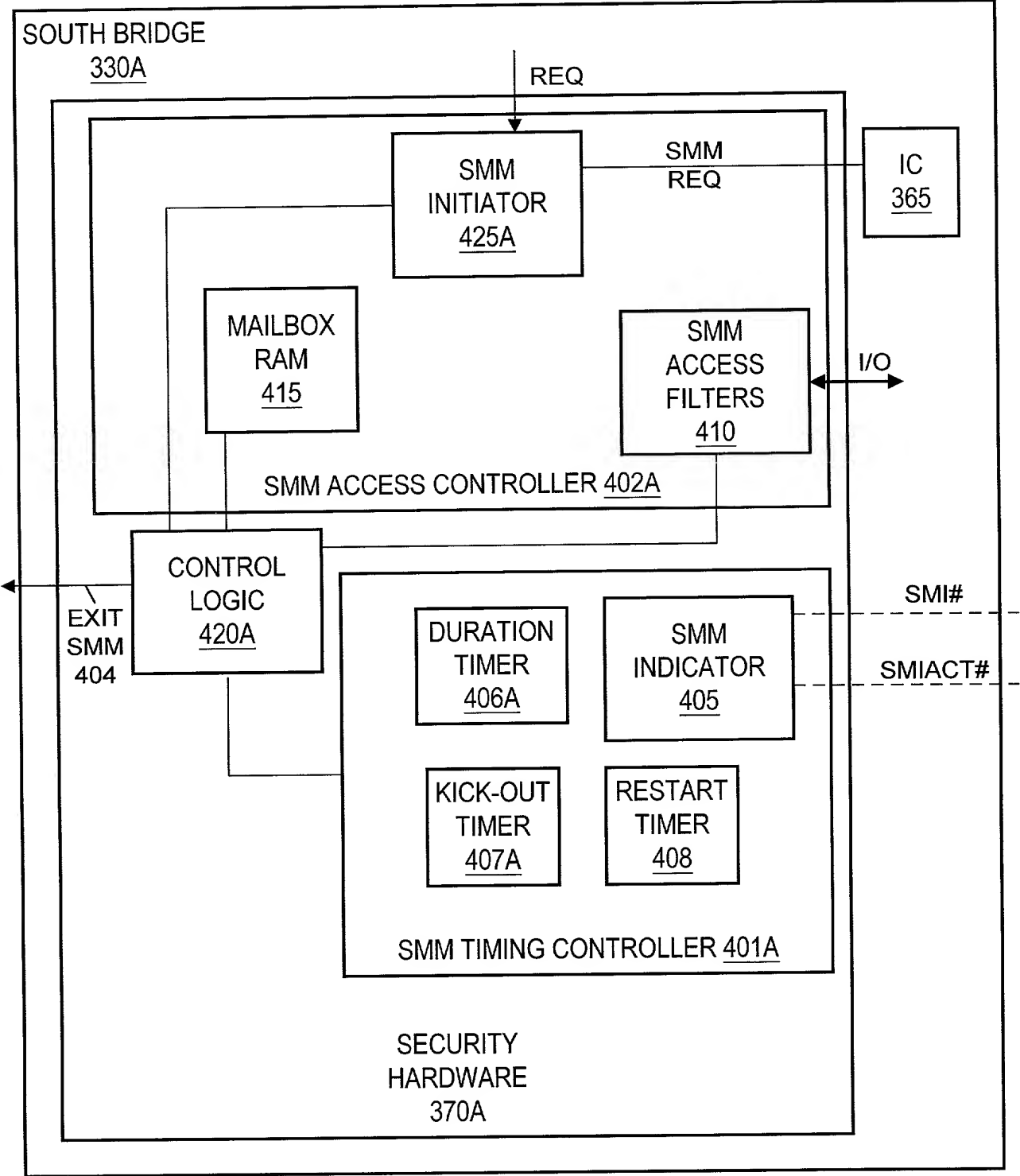


Fig. 5A

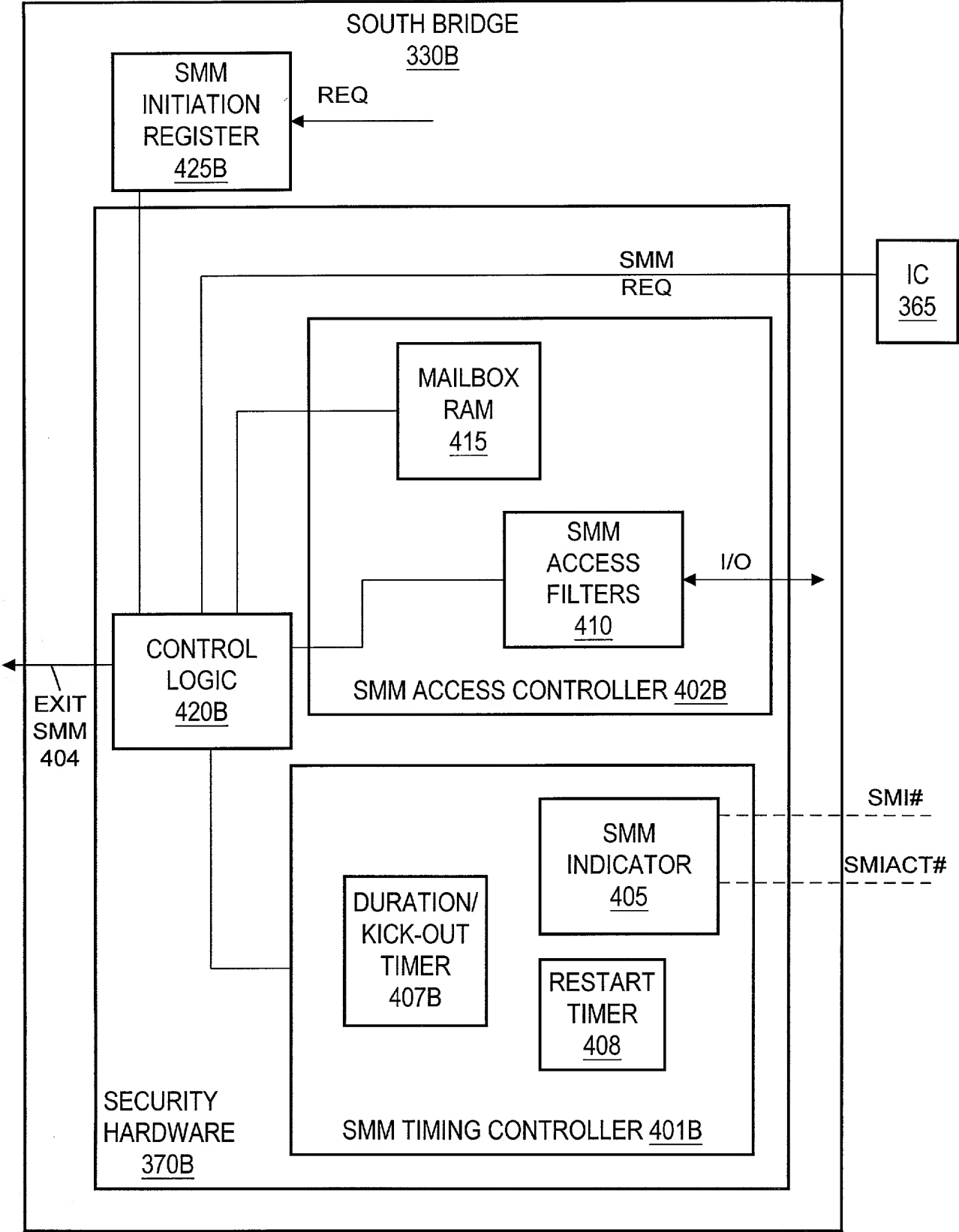


Fig. 5B

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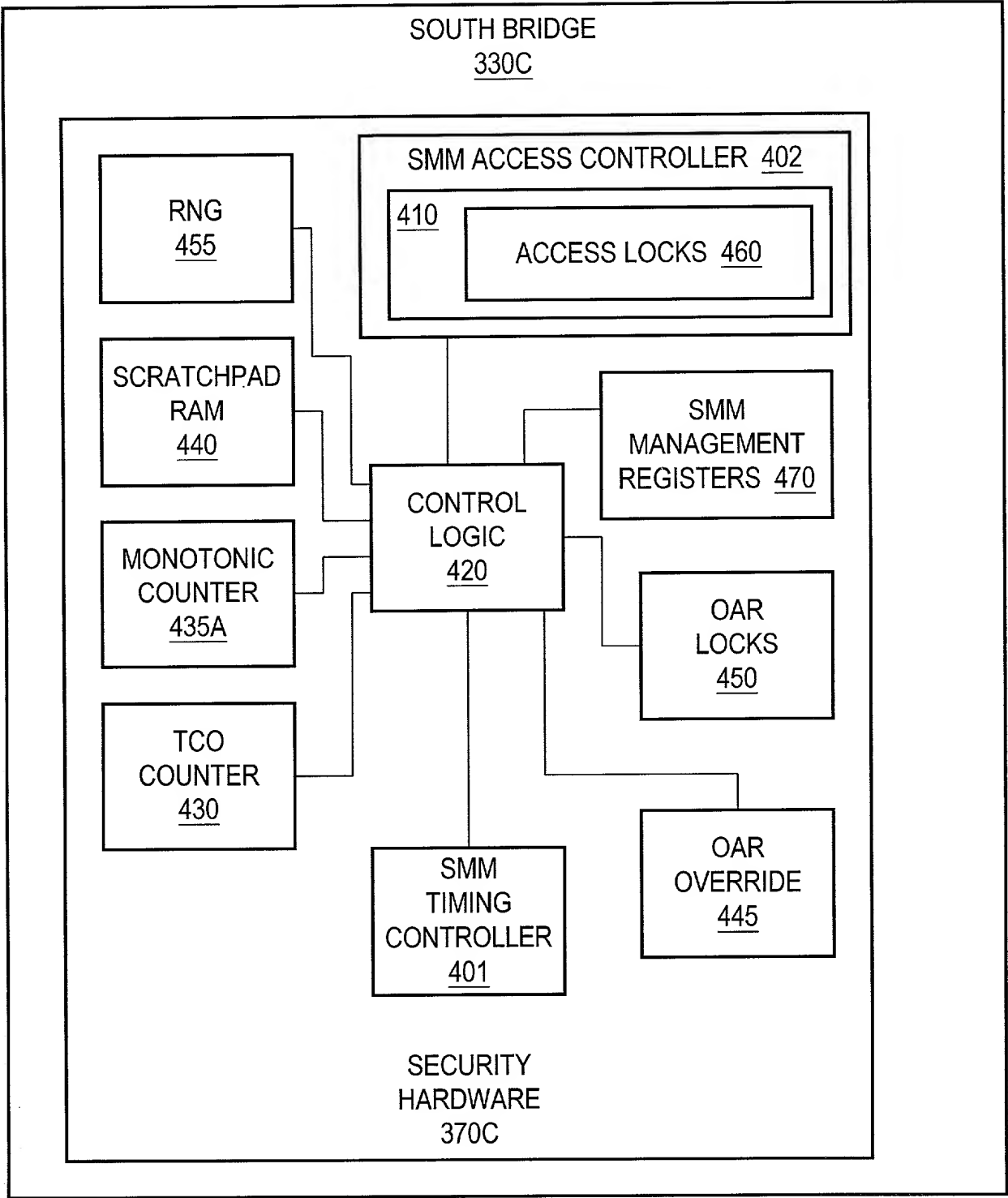


Fig. 6

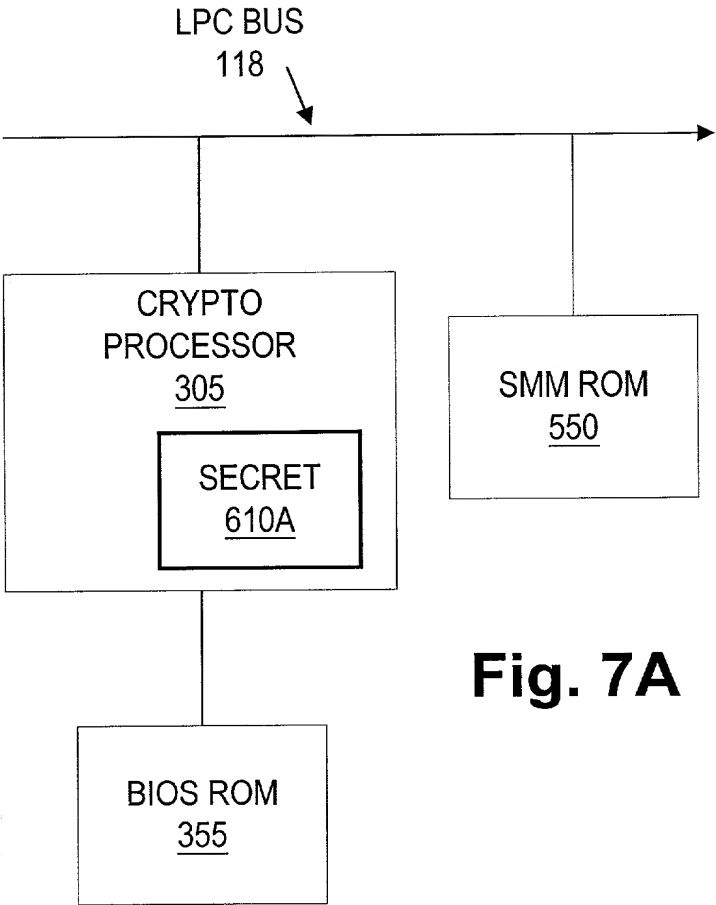


Fig. 7A

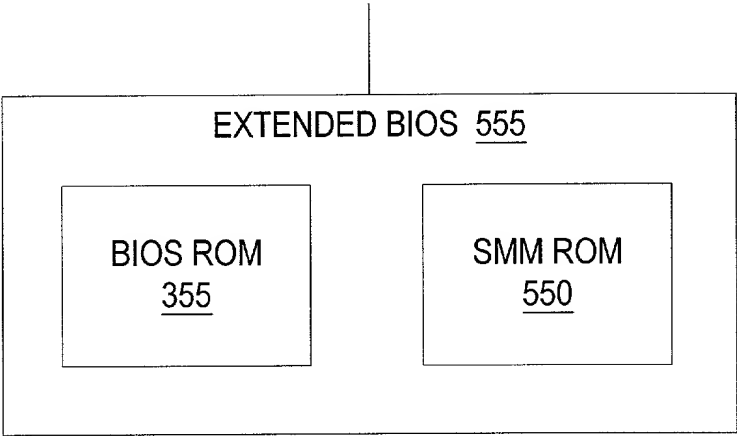
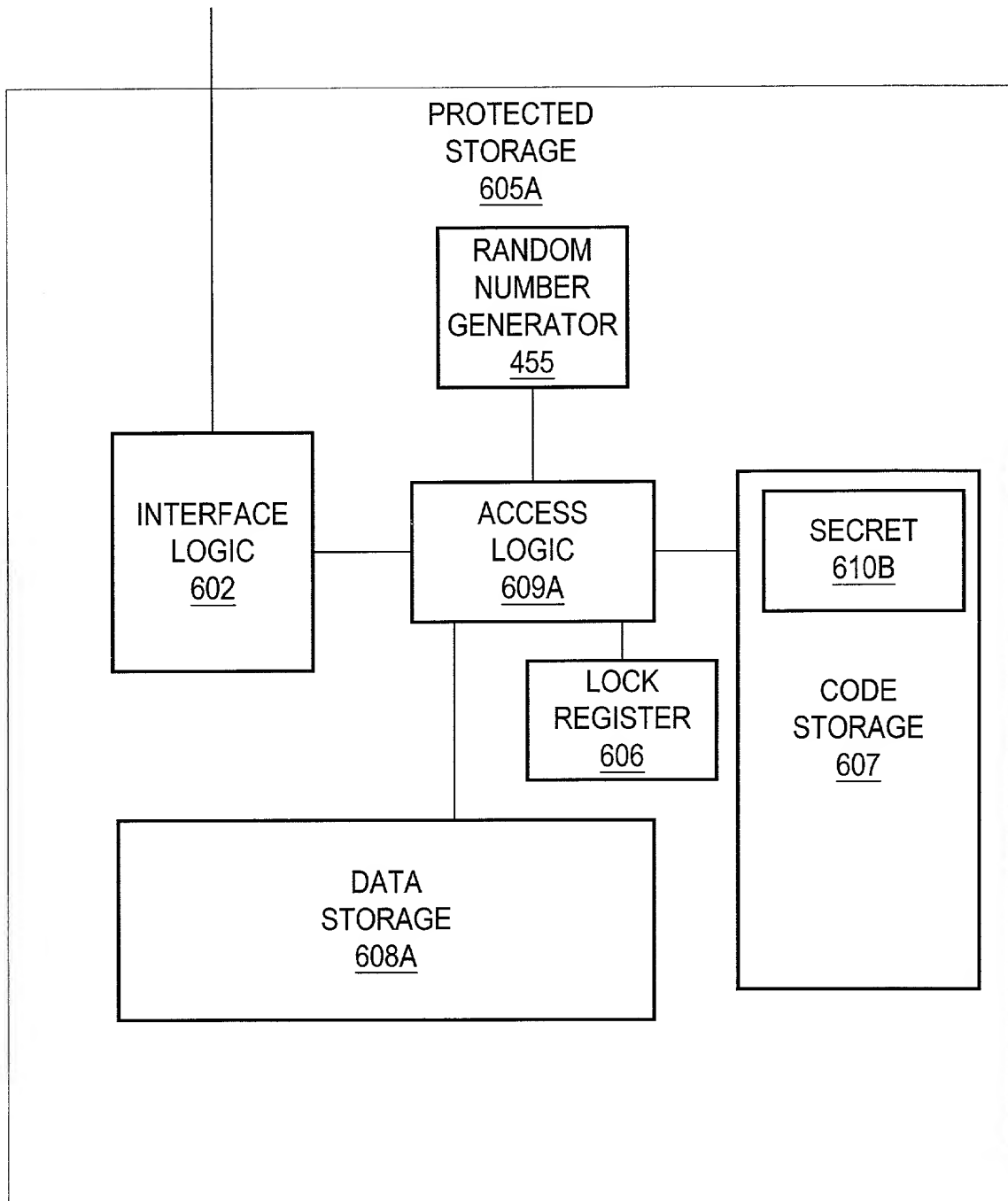


Fig. 7B

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**Fig. 7C**

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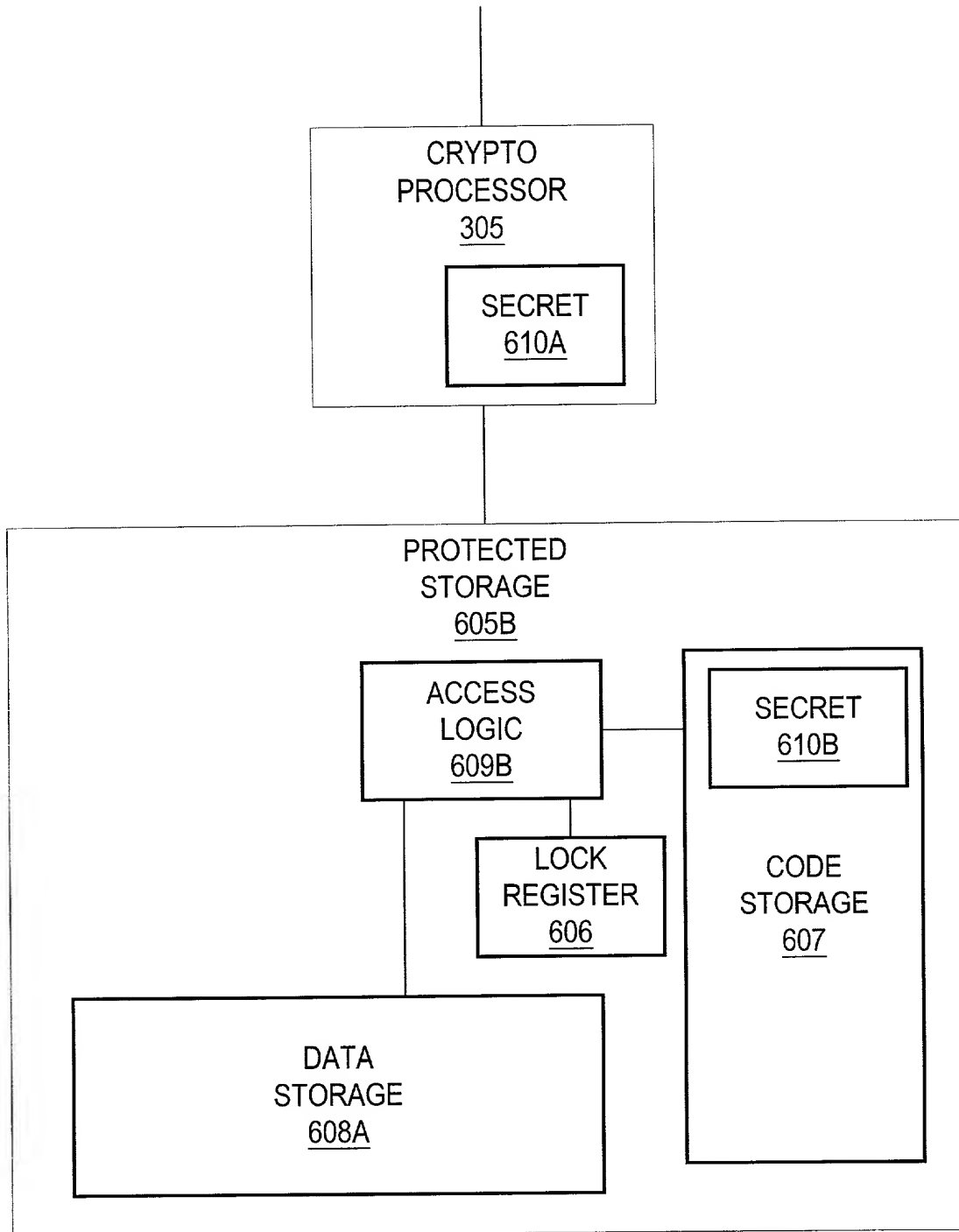


Fig. 7D

For Secret 606

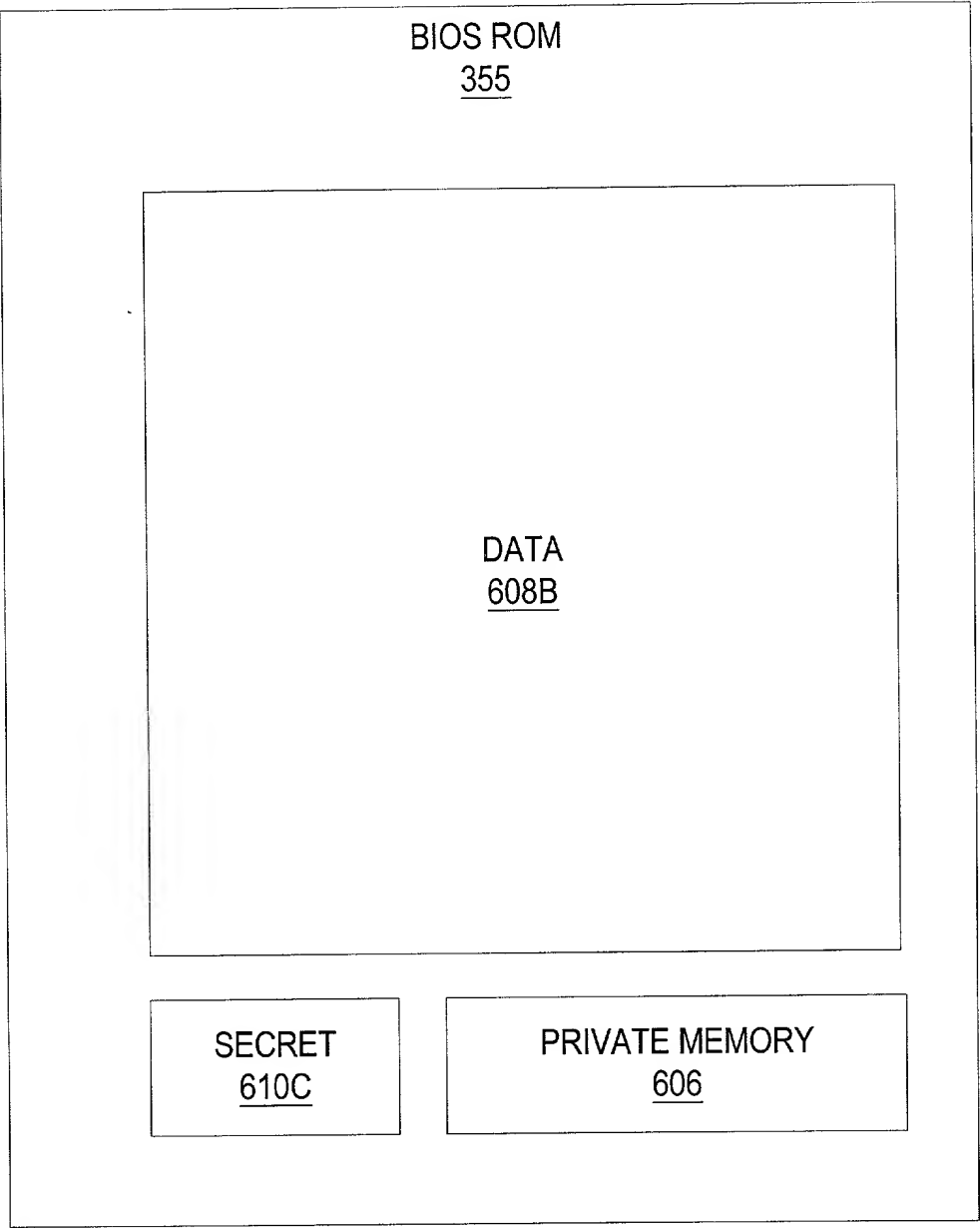


Fig. 8A

0983225 051101

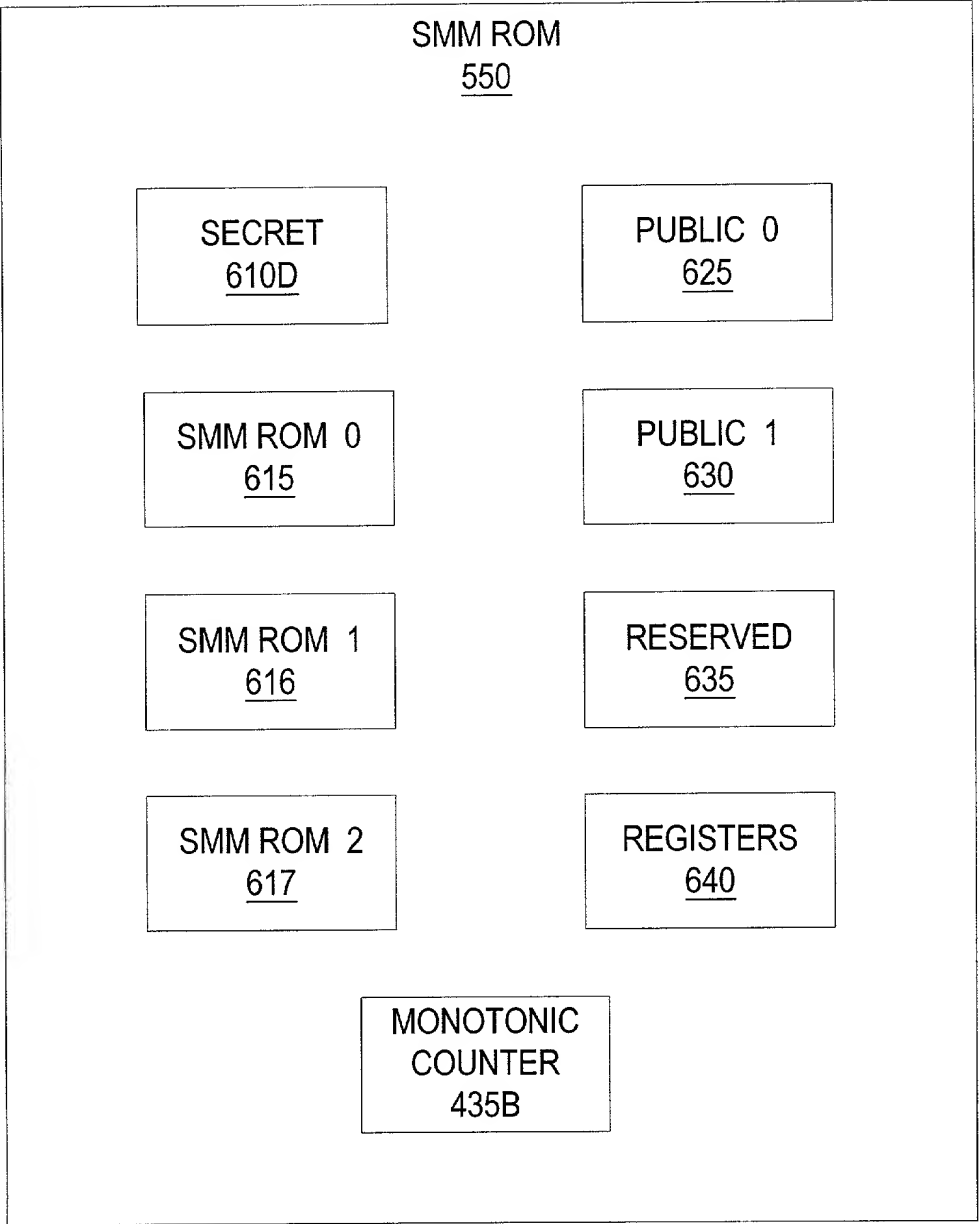


Fig. 8B

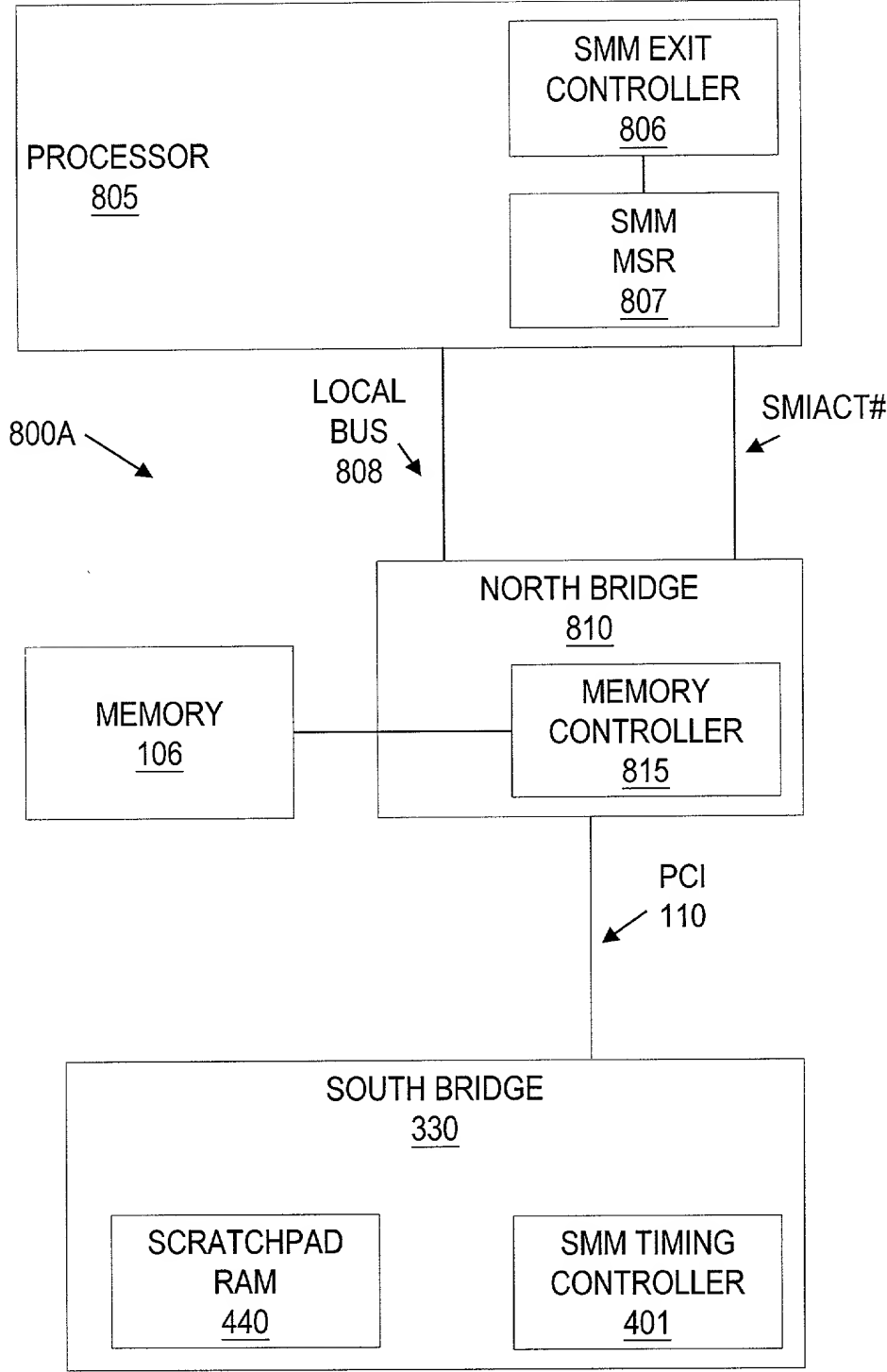


Fig. 9A

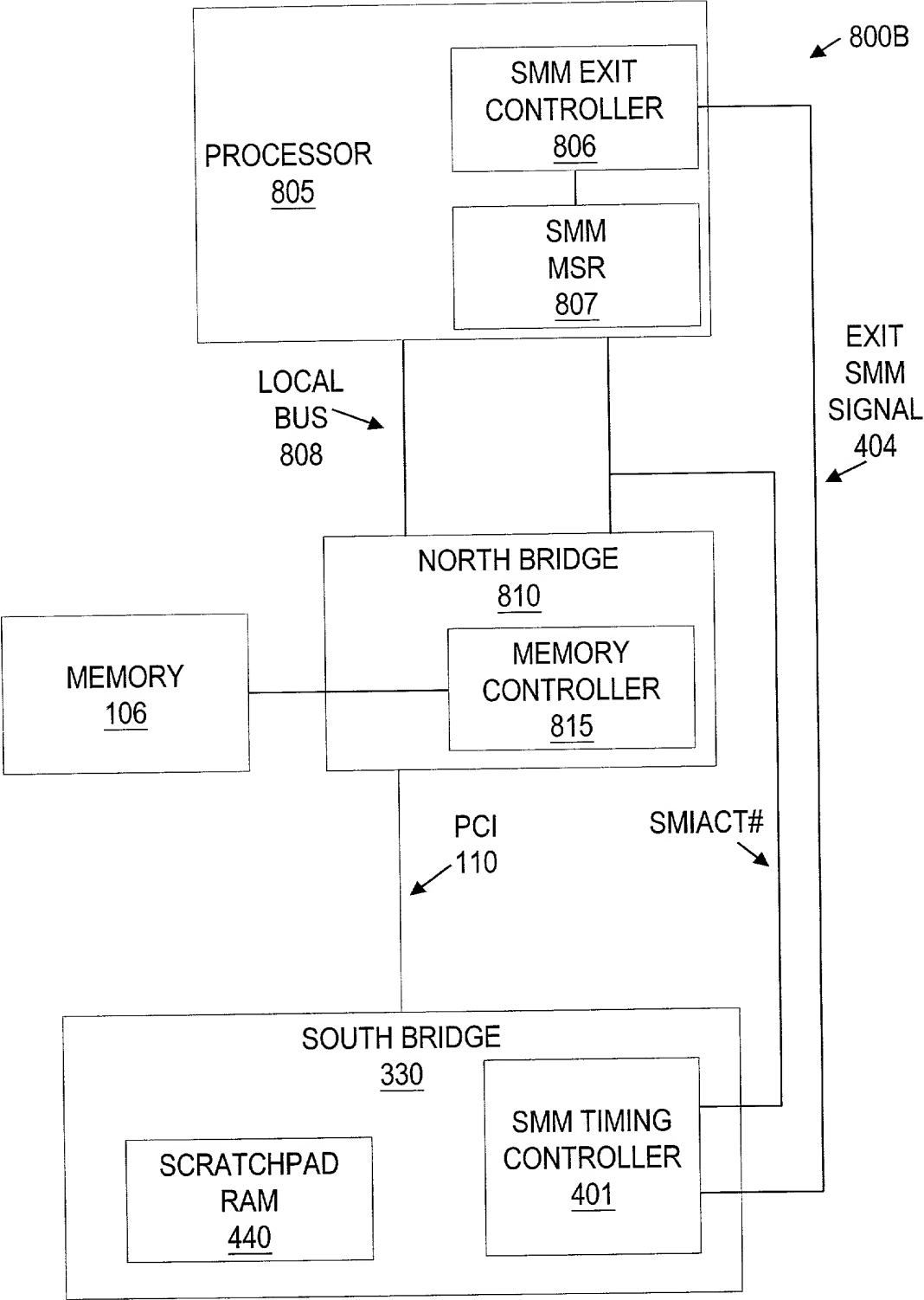


Fig. 9B

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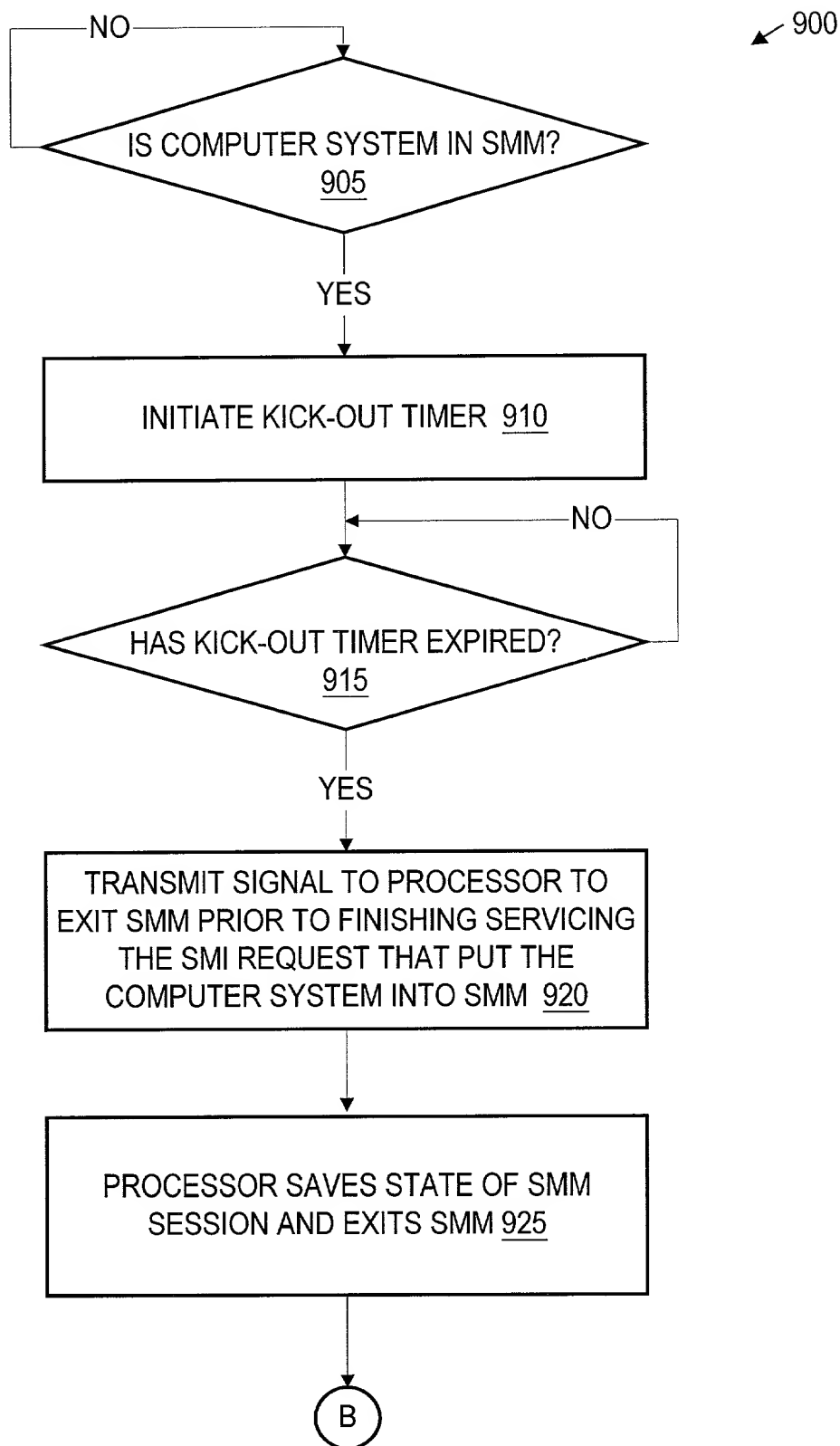


Fig. 10A

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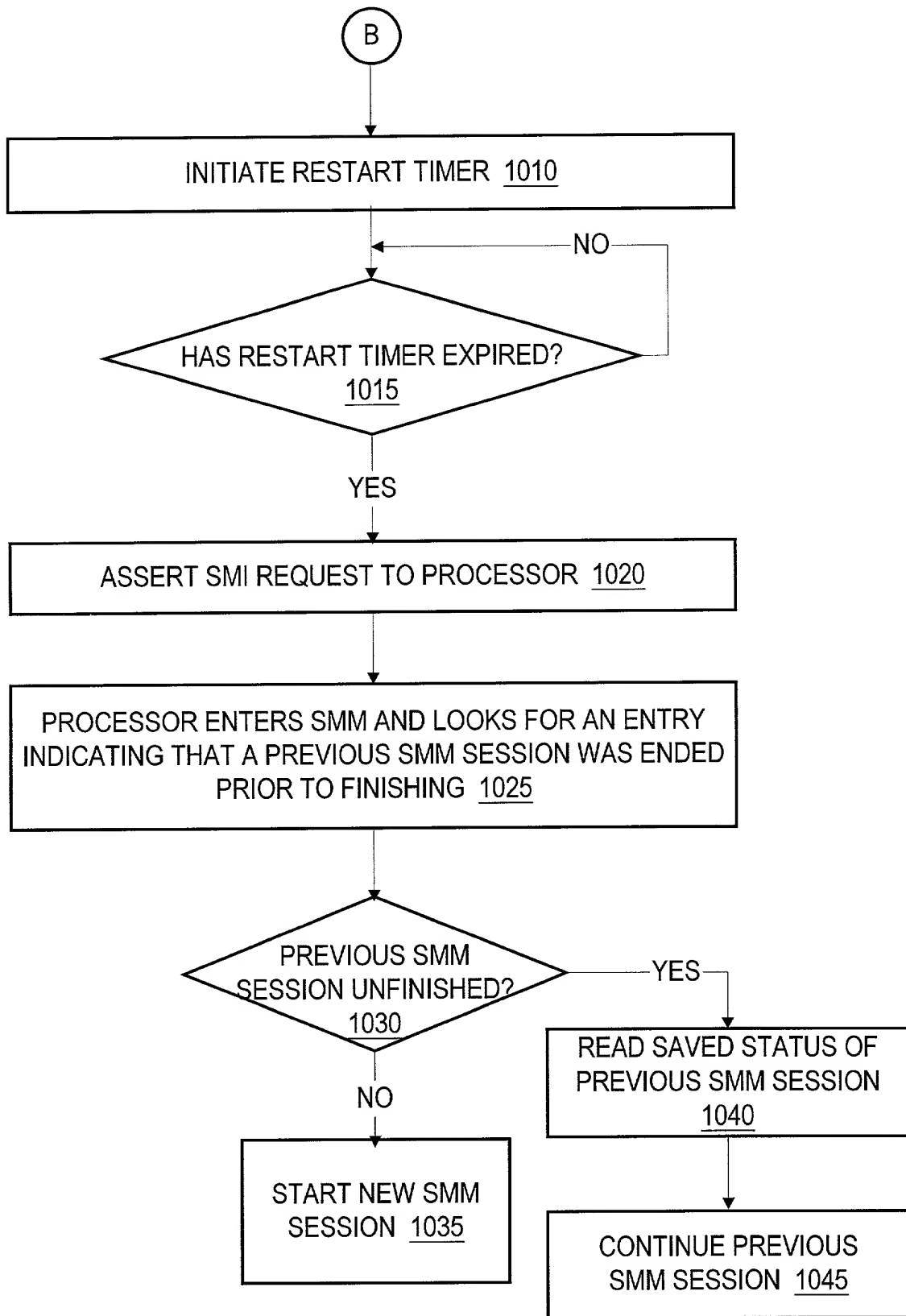


Fig. 10B

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1100A

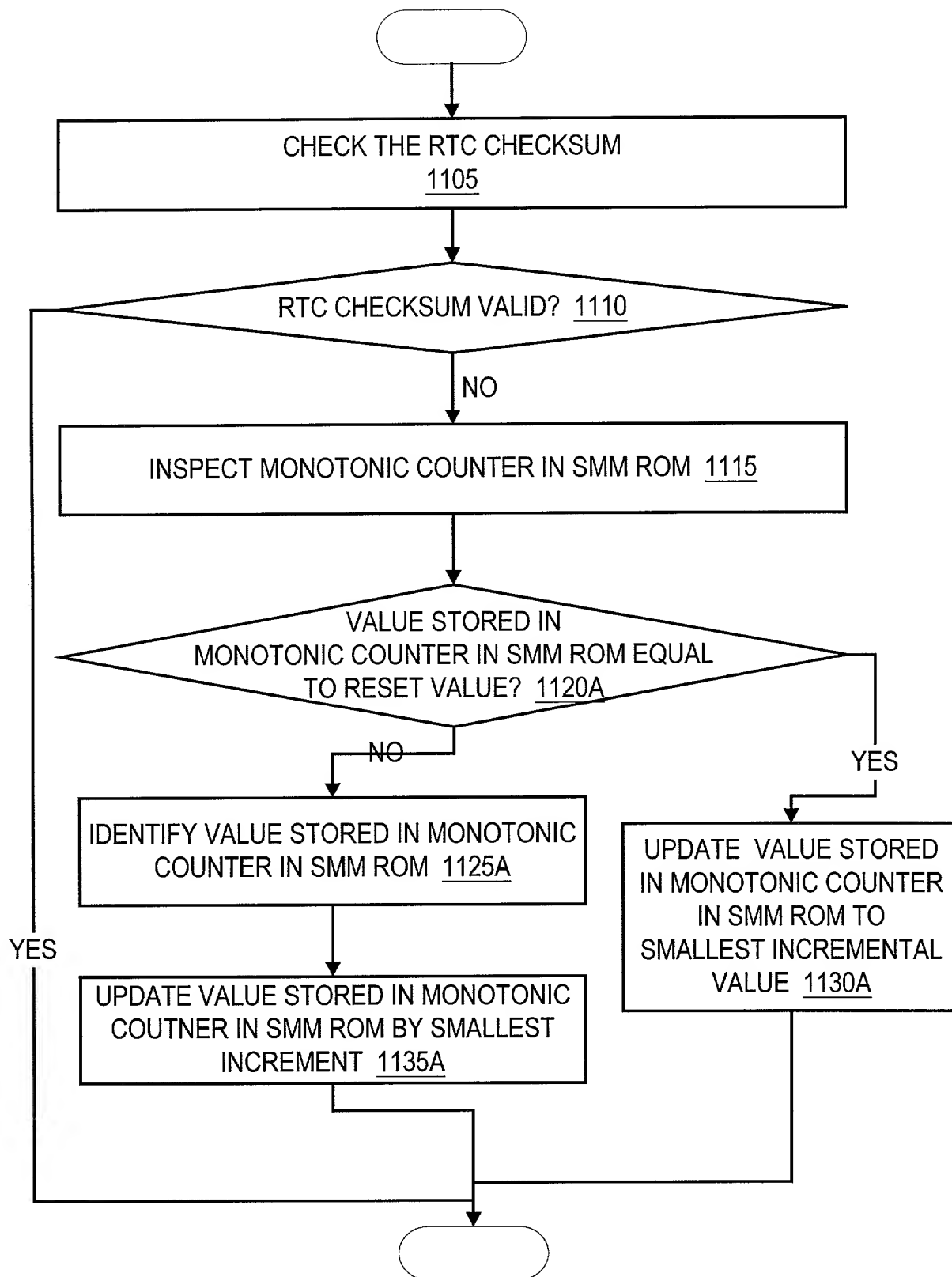


Fig. 11A

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1100B

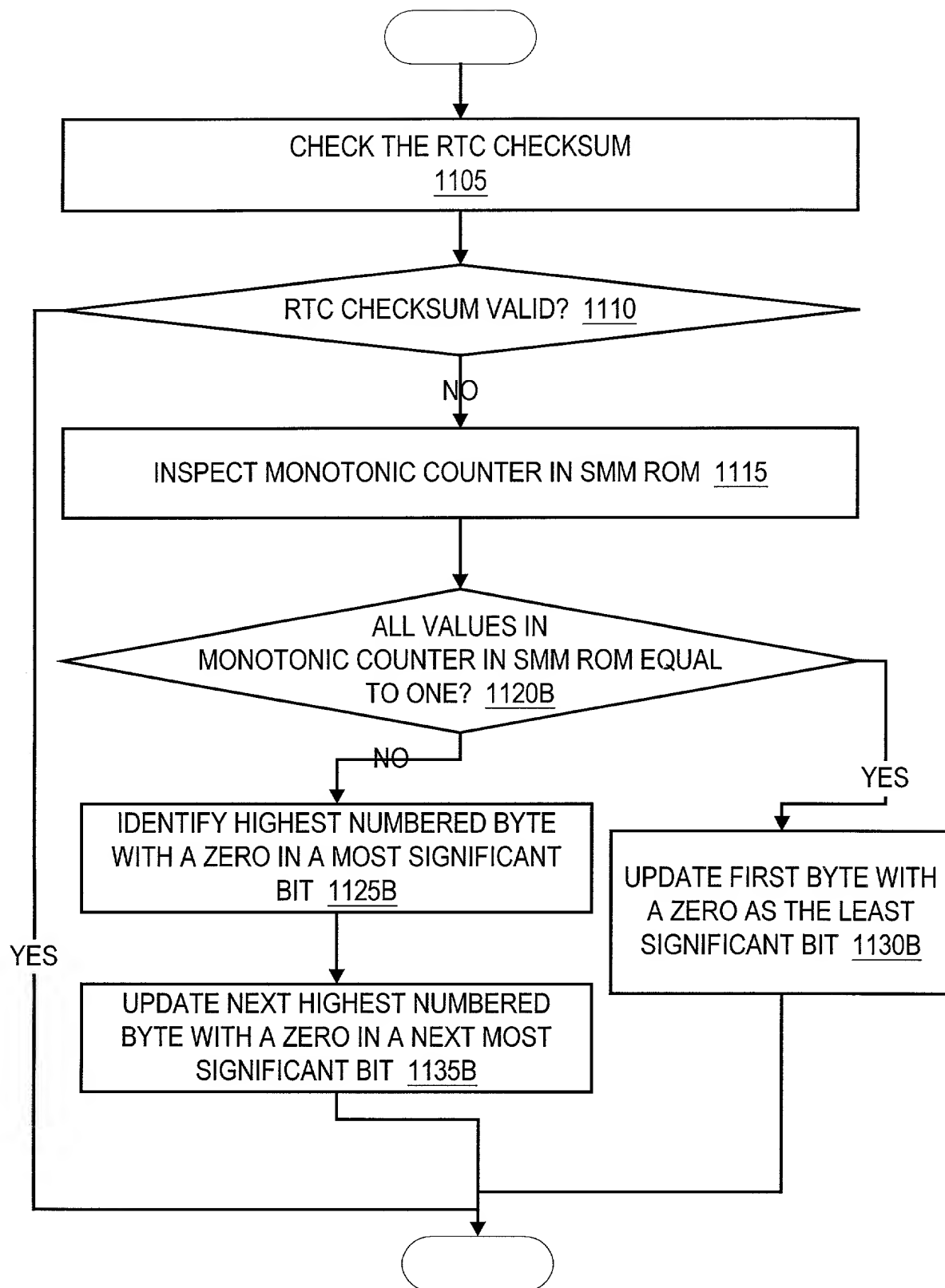


Fig. 11B

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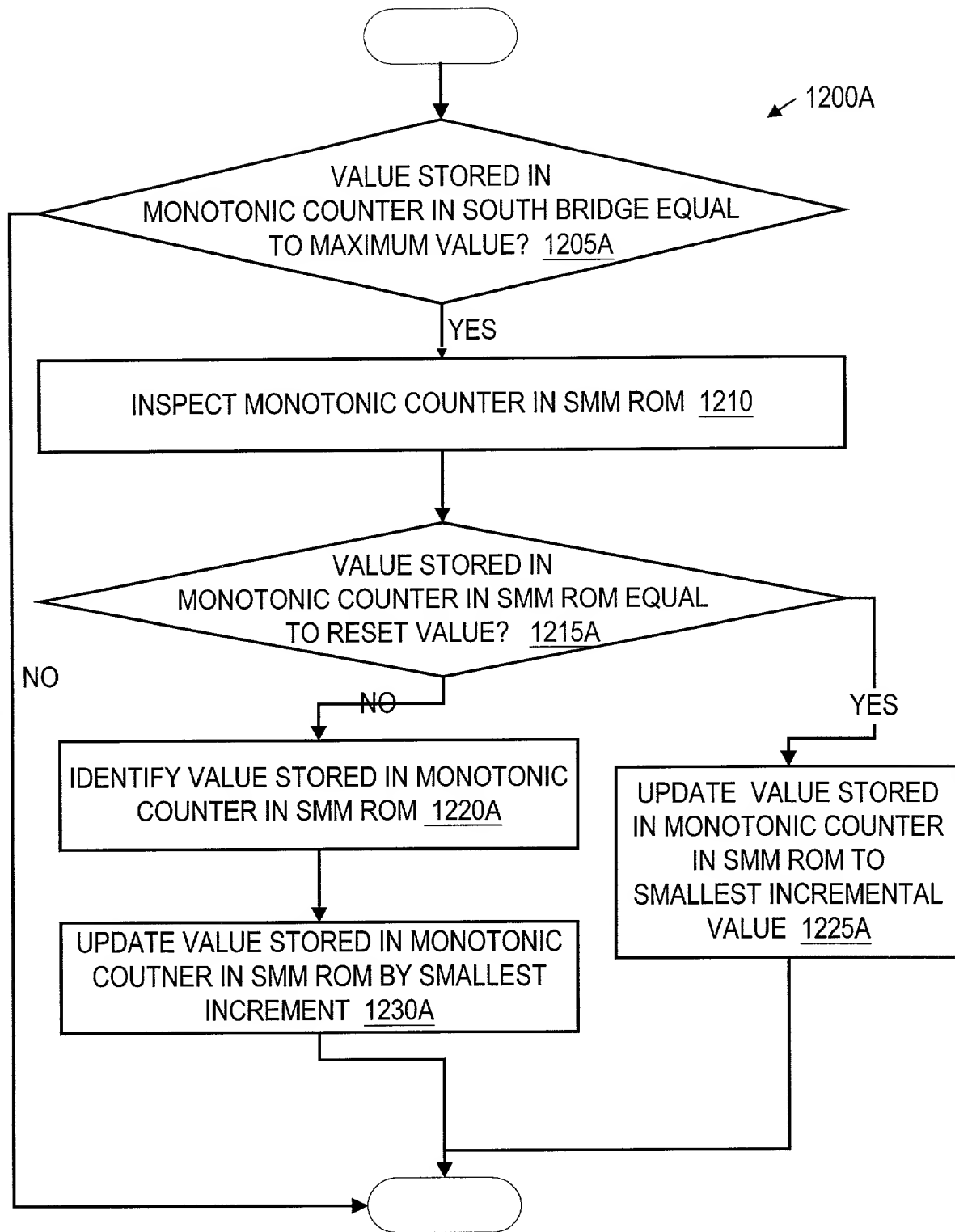


Fig. 12A

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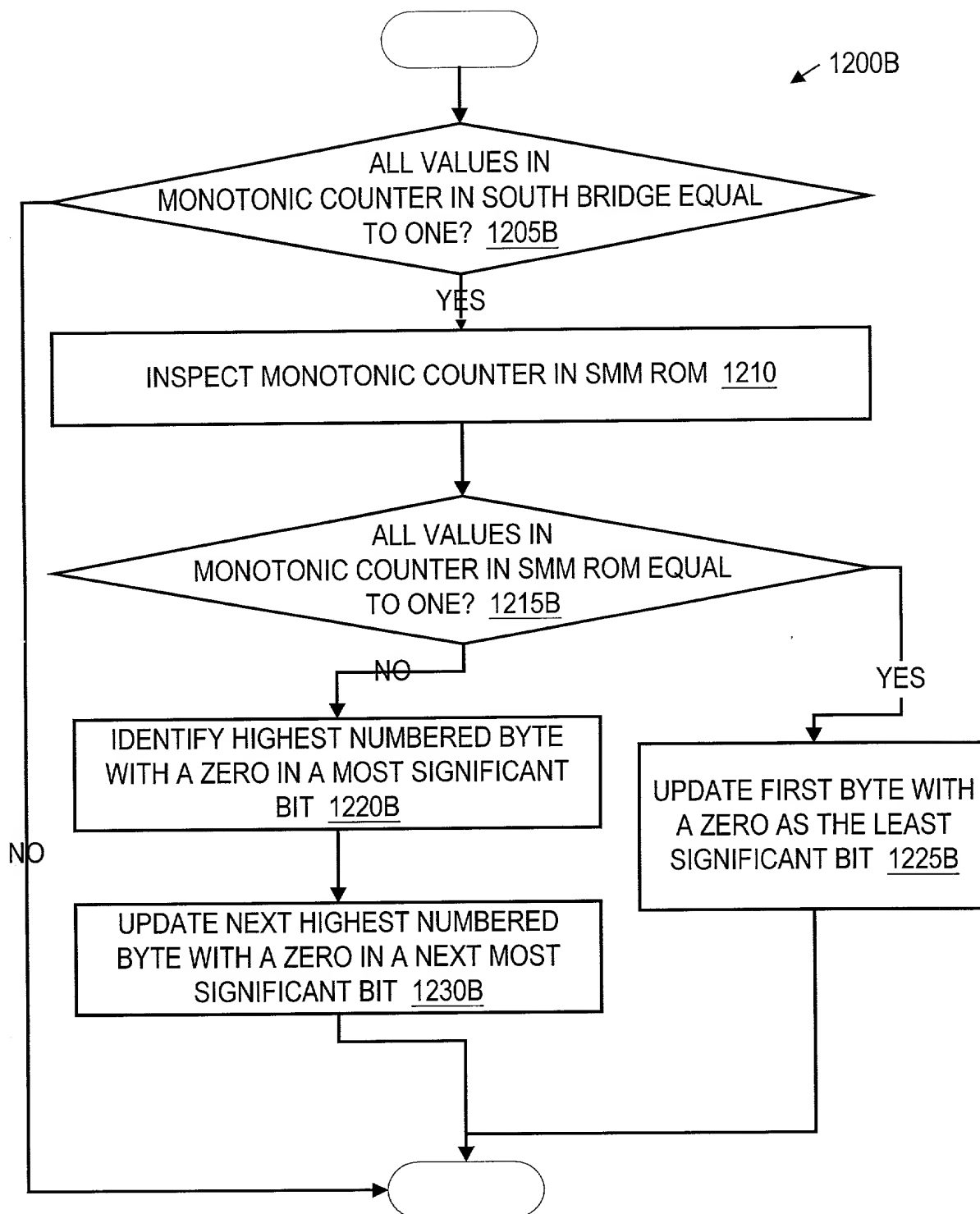


Fig. 12B

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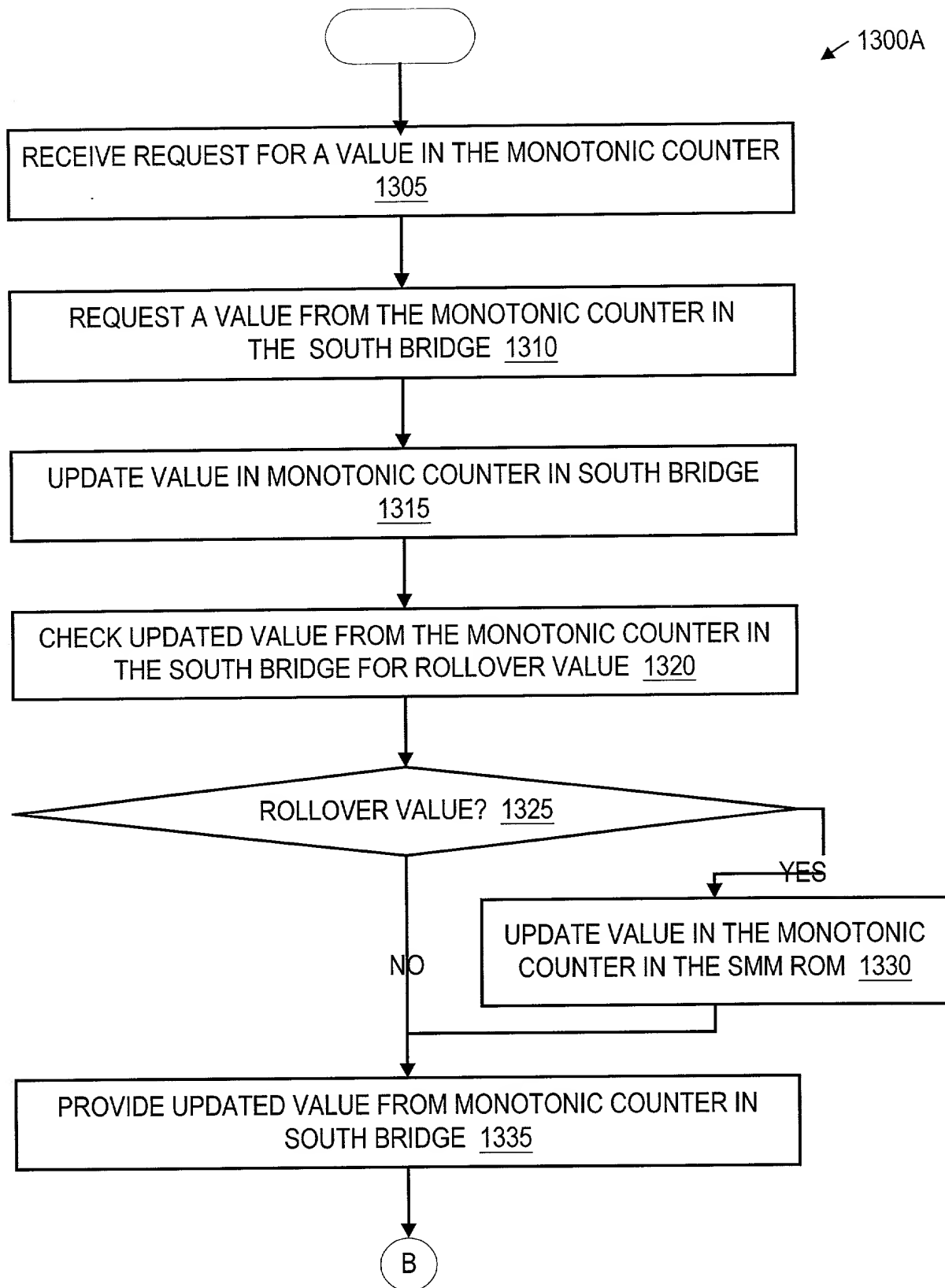


Fig. 13A

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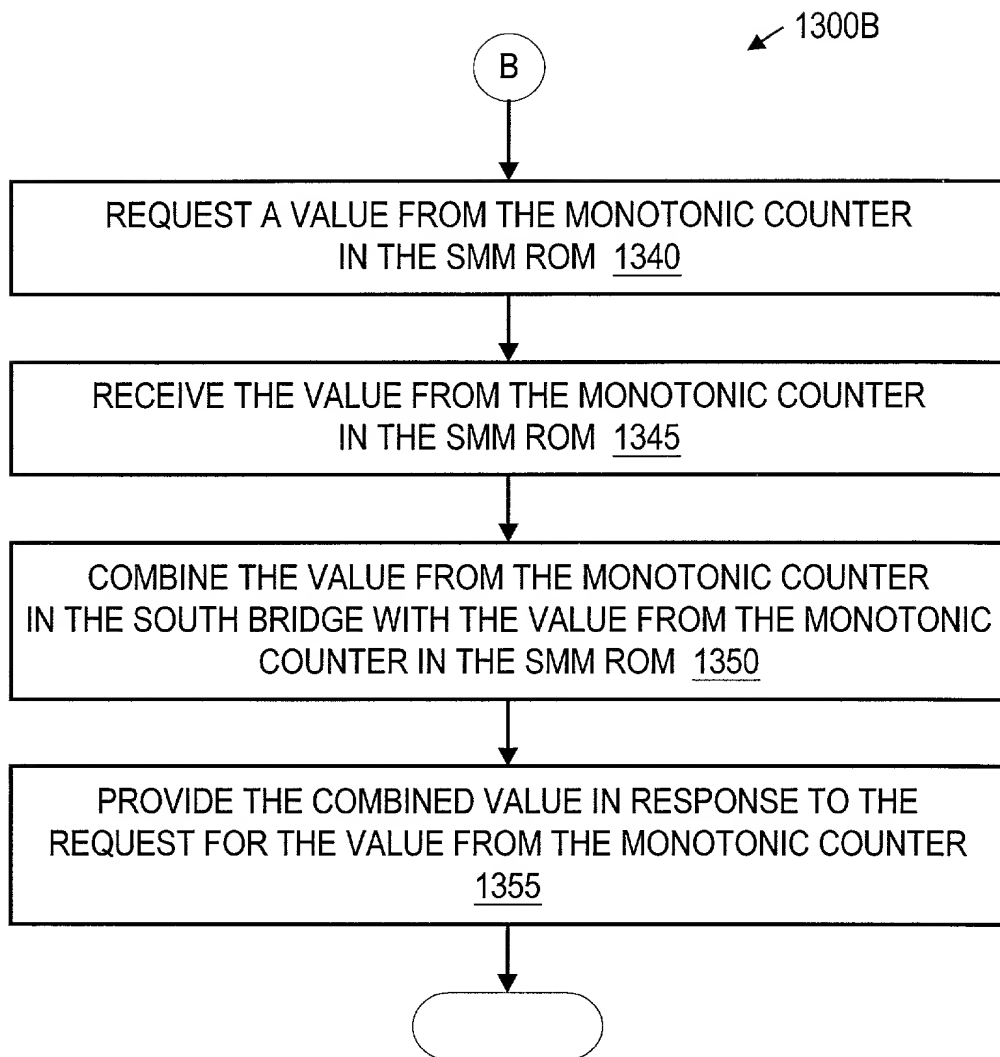


Fig. 13B

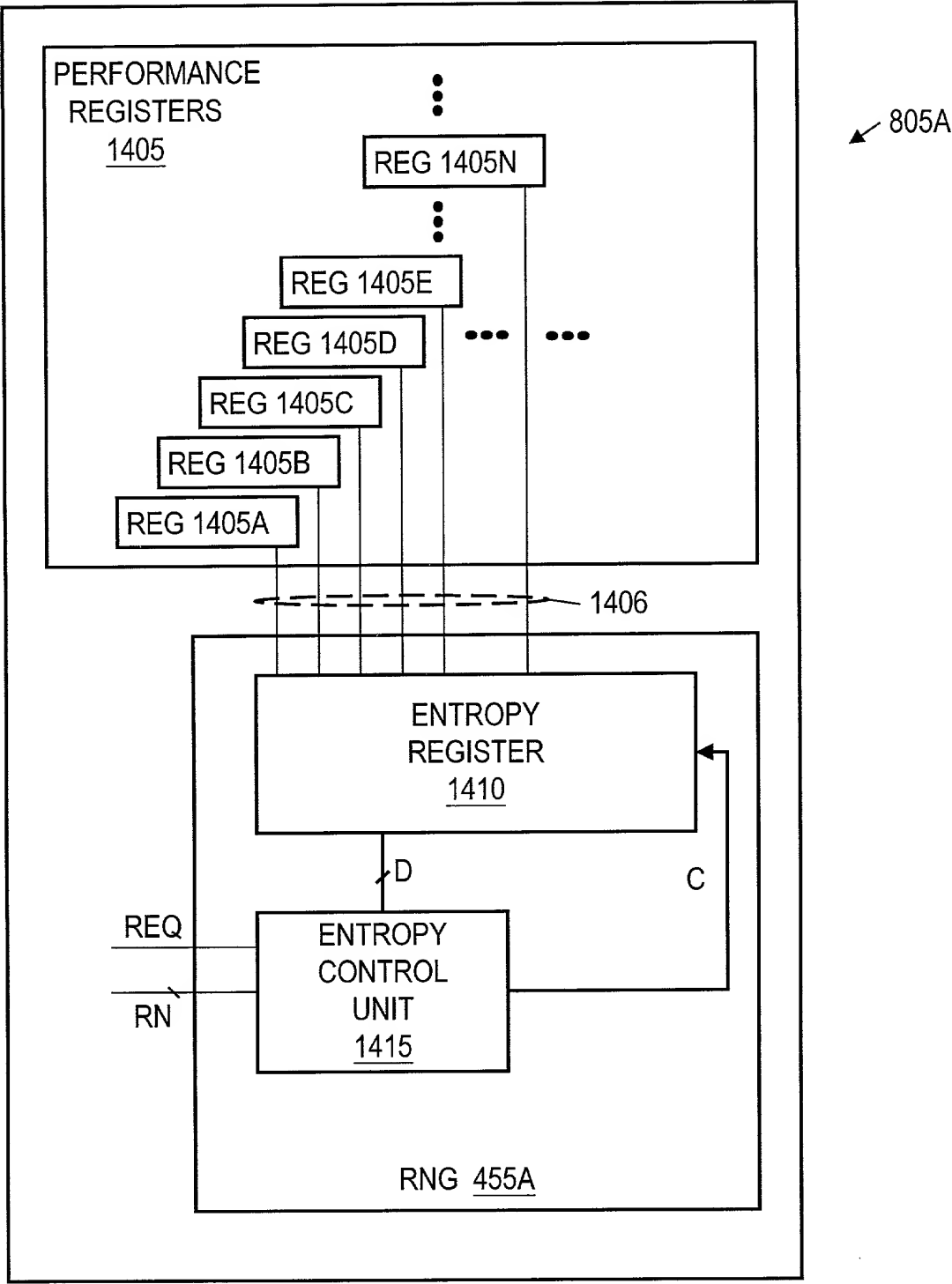


Fig. 14A

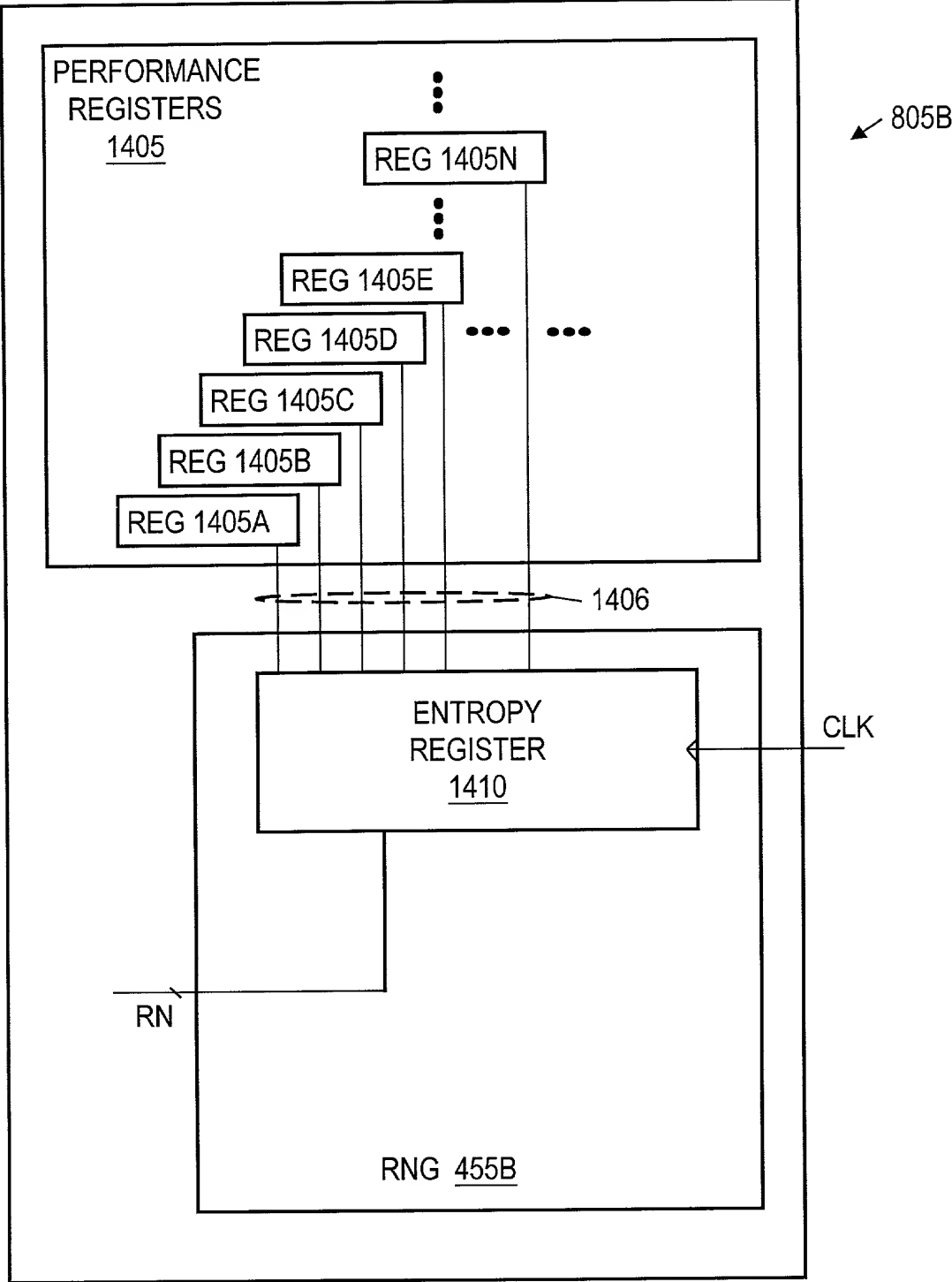


Fig. 14B

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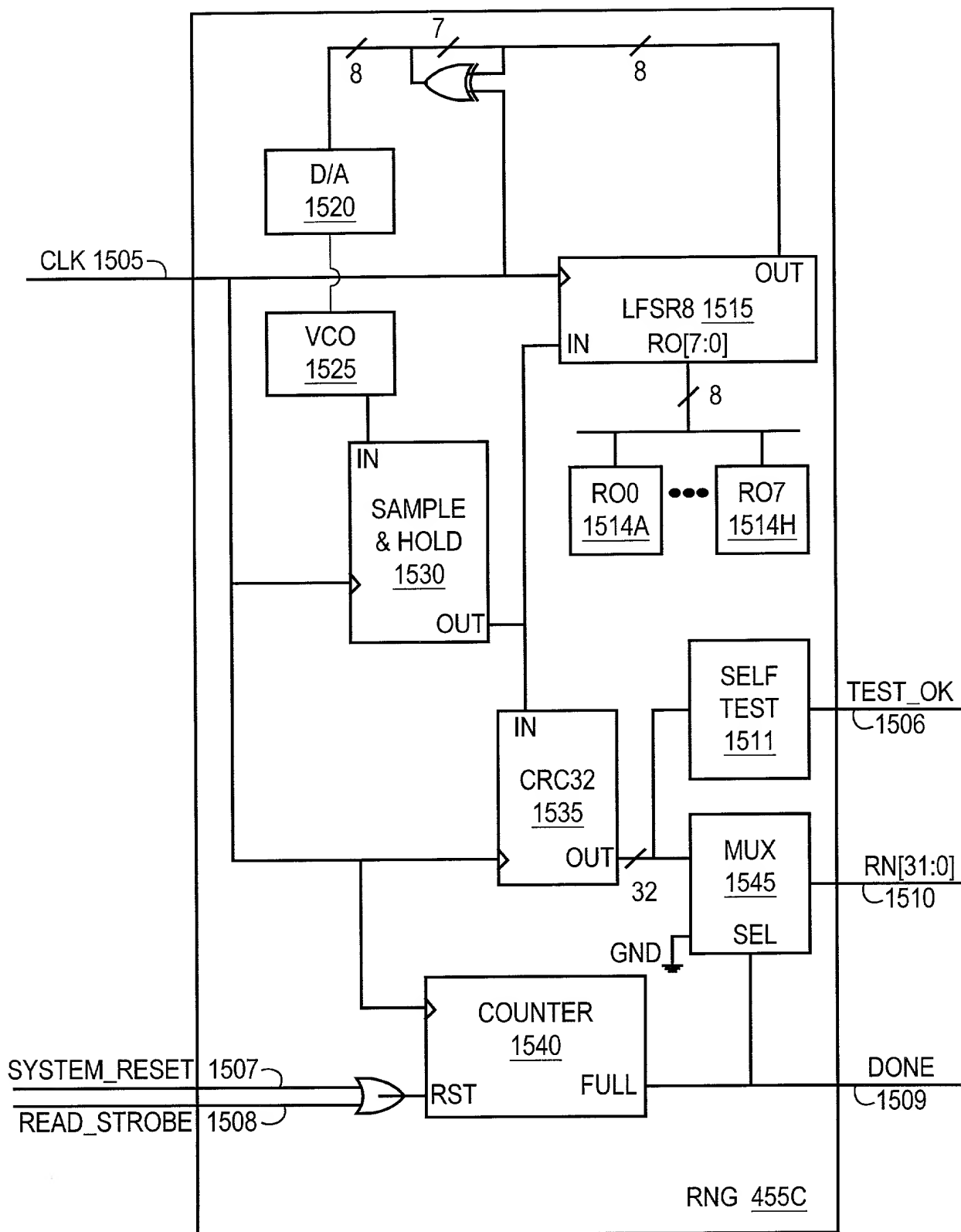


Fig. 15

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1600A

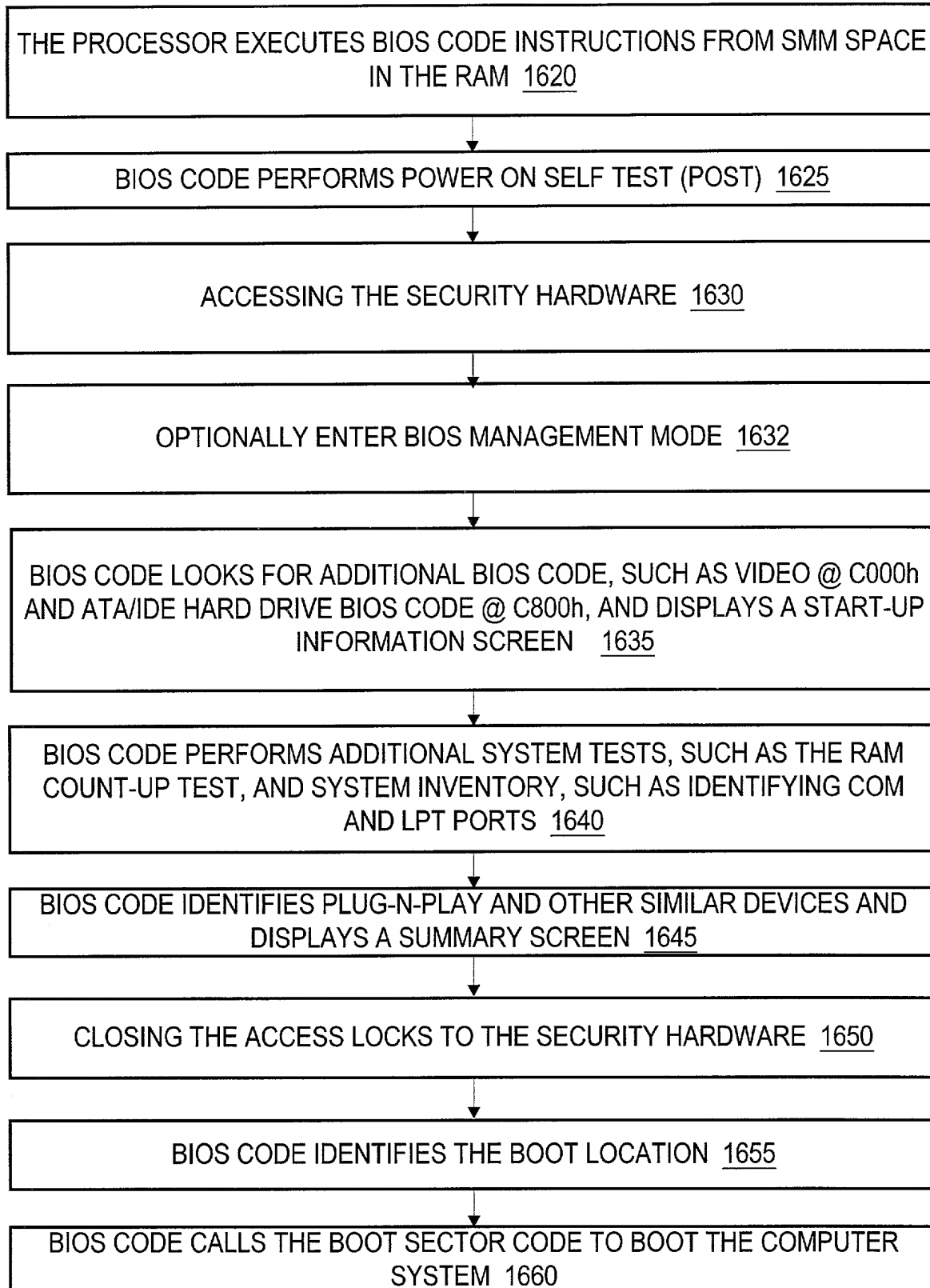


Fig. 16A

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1600B

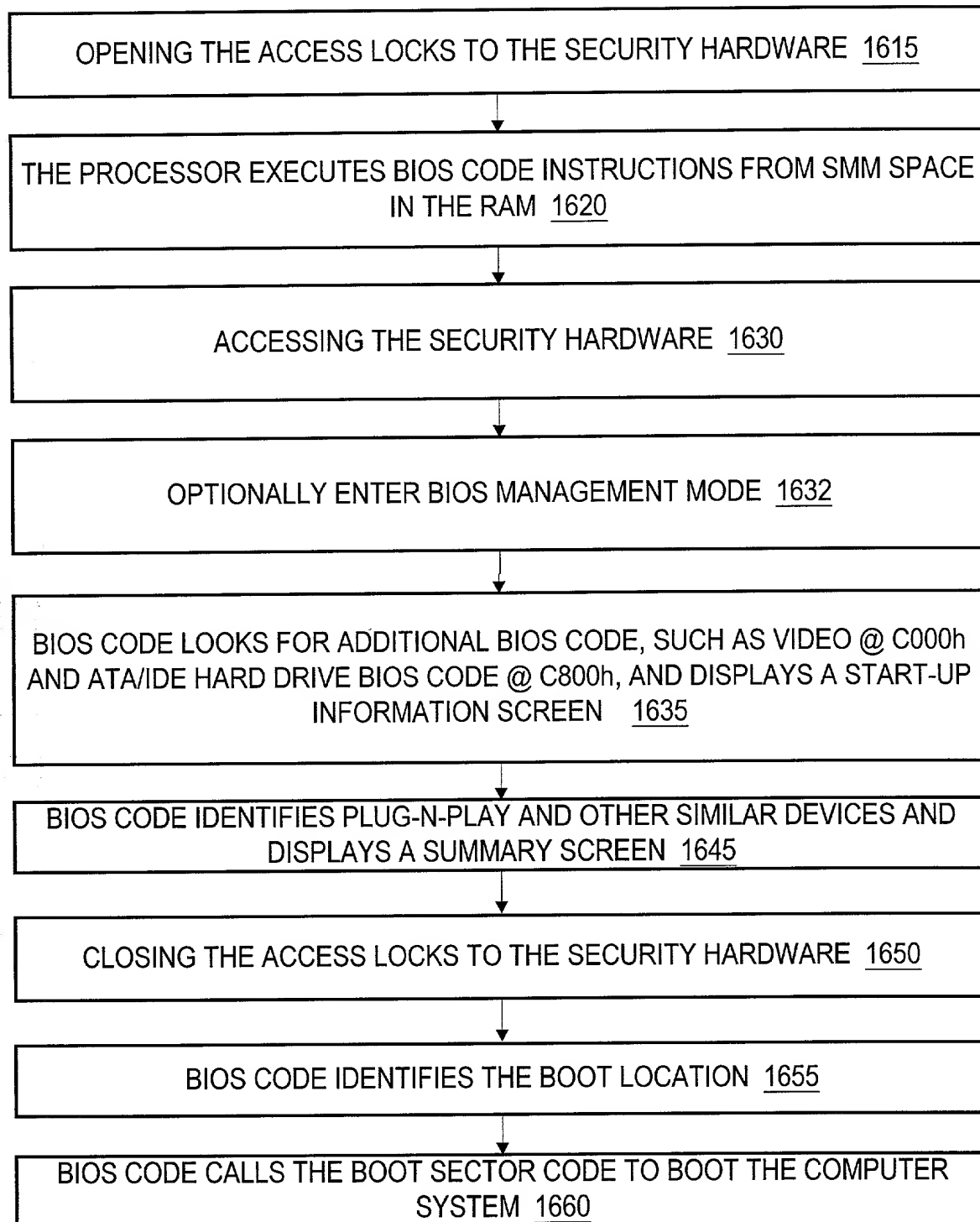


Fig. 16B

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1600C

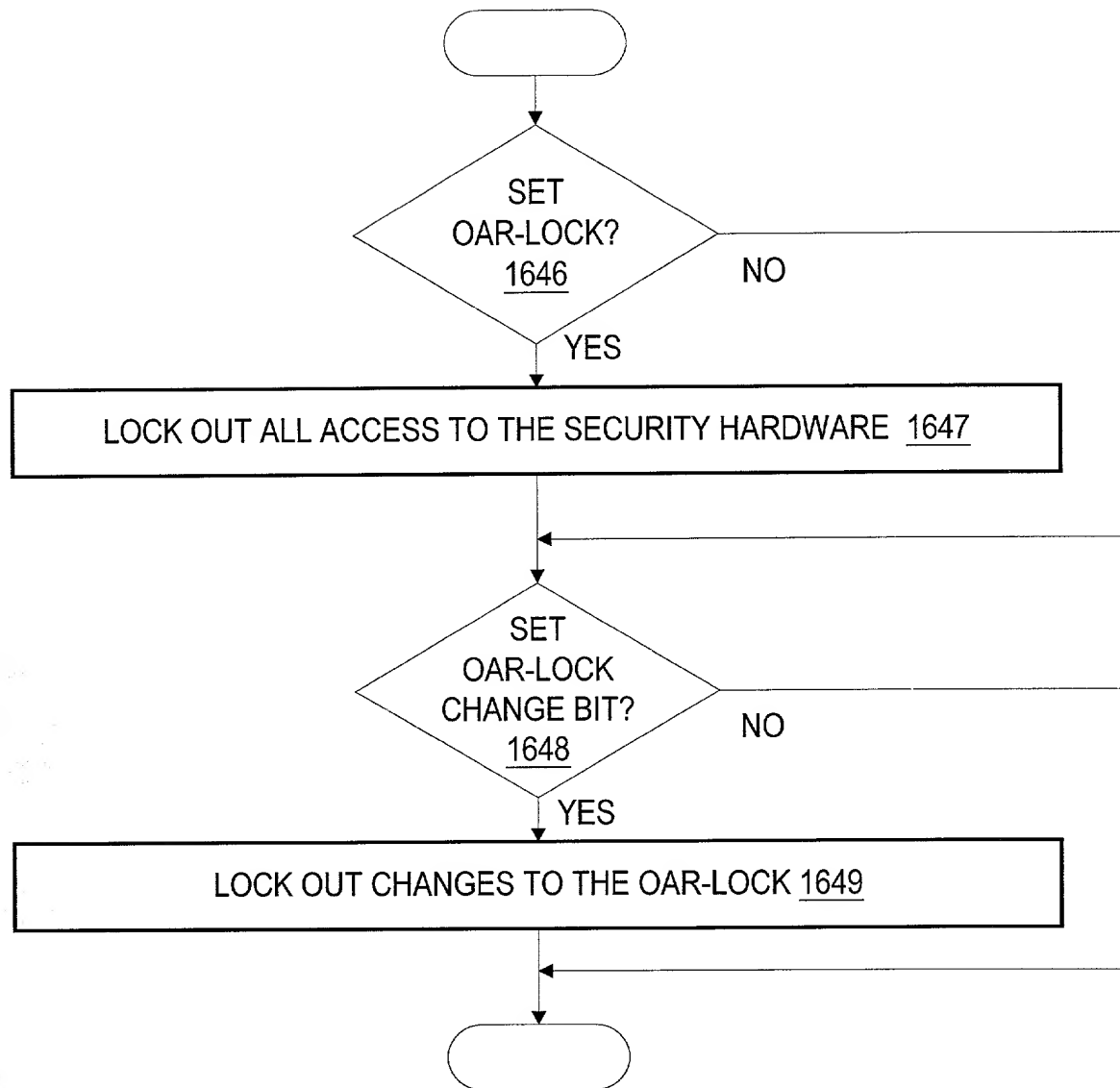


Fig. 16C

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1600D

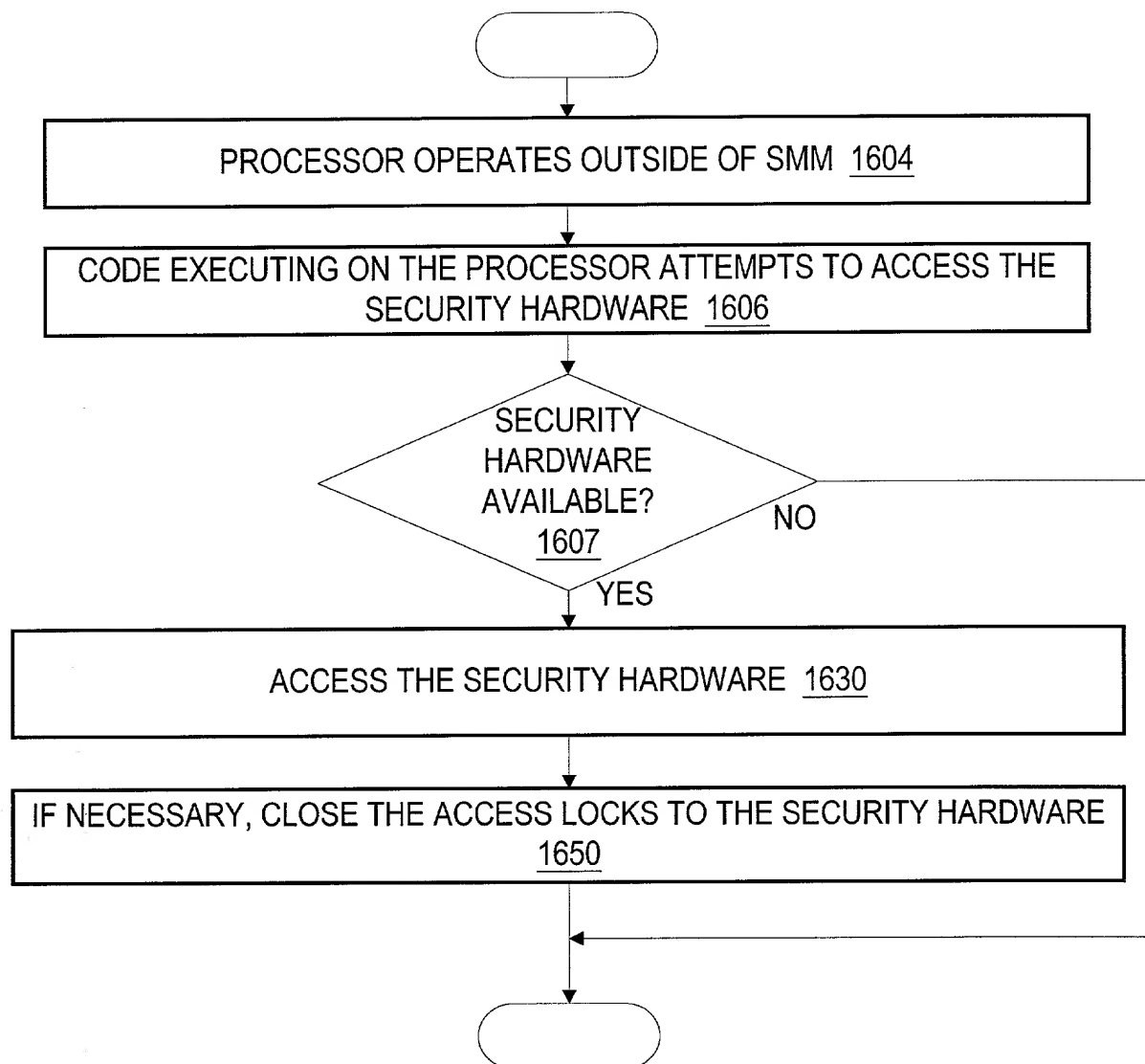


Fig. 16D

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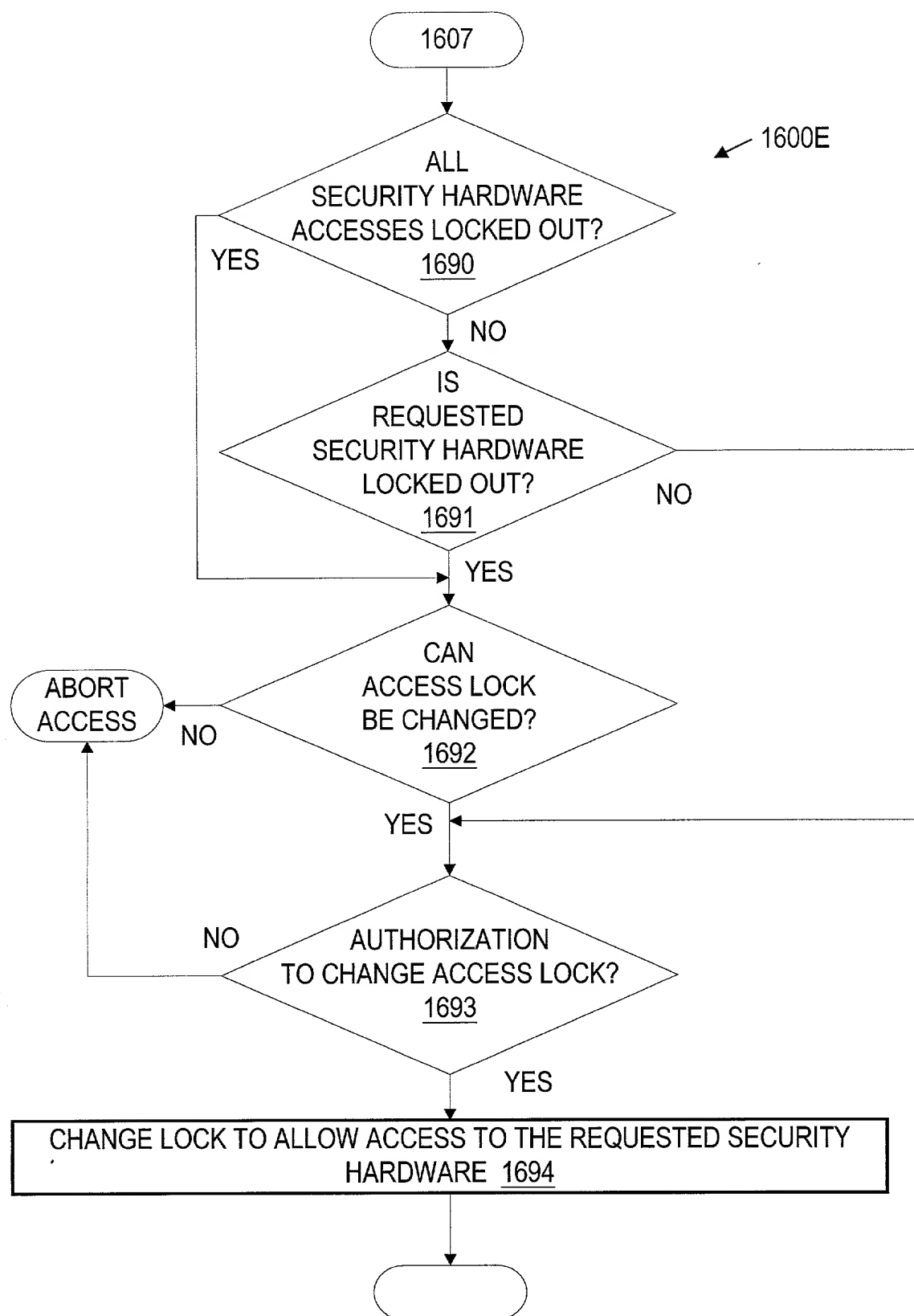


Fig. 16E

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1600F

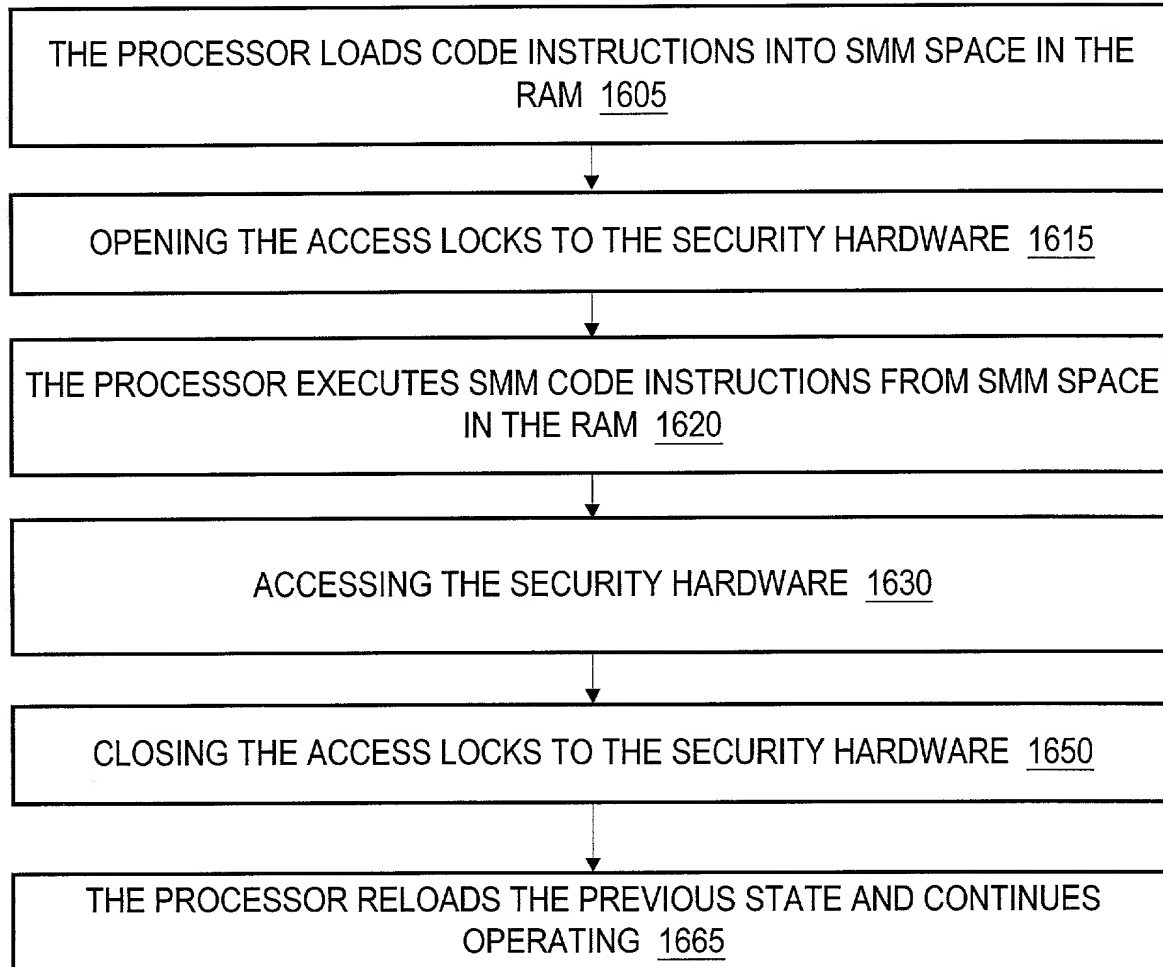


Fig. 16F

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1600G

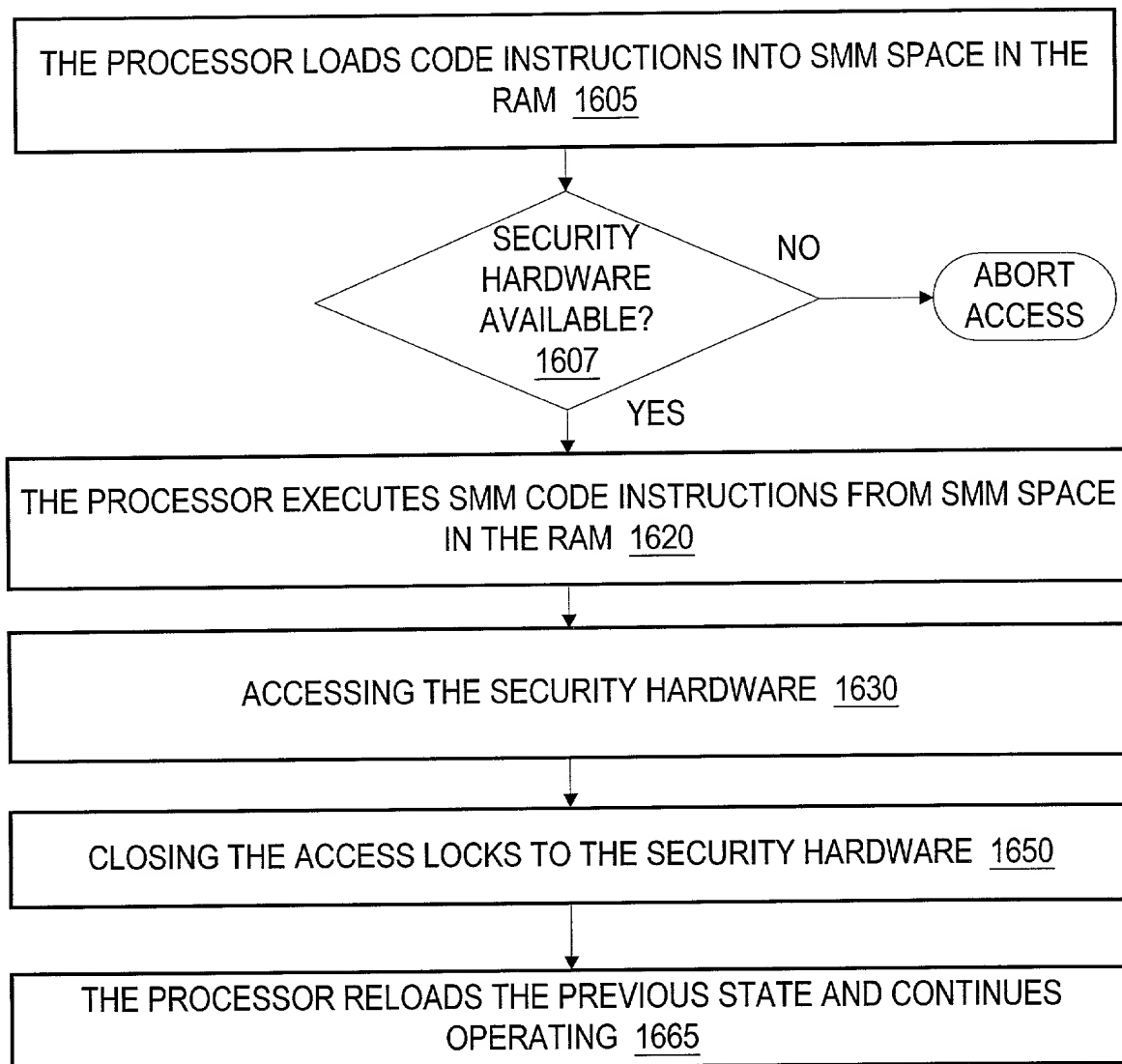


Fig. 16G

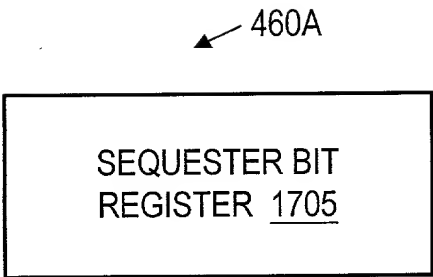


Fig. 17A

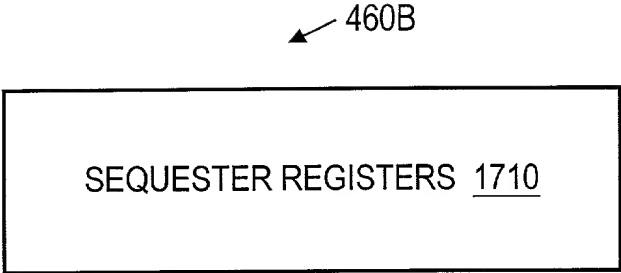


Fig. 17B

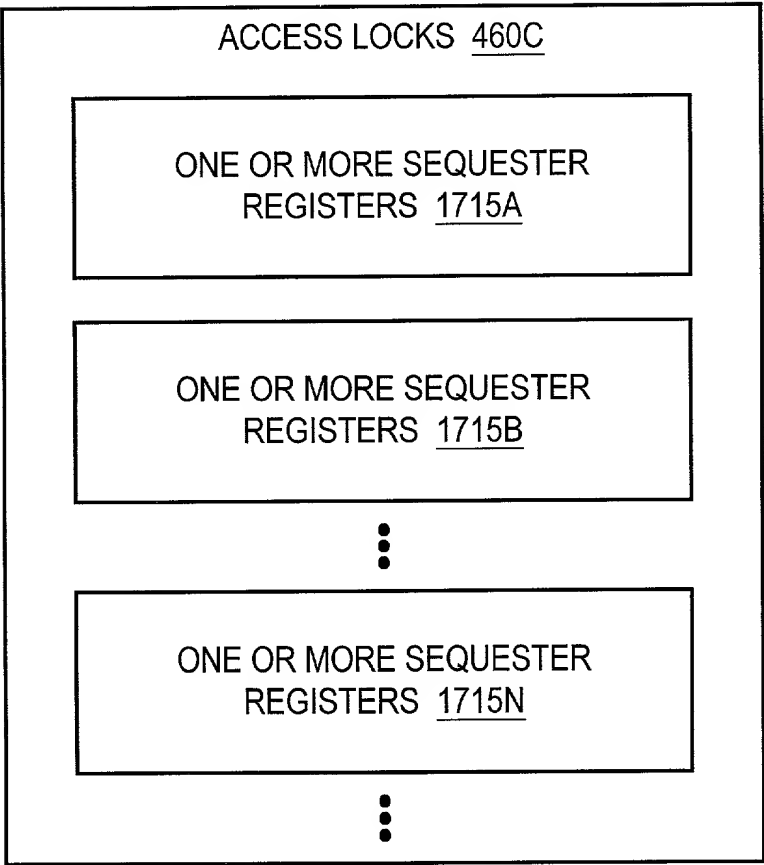


Fig. 17C

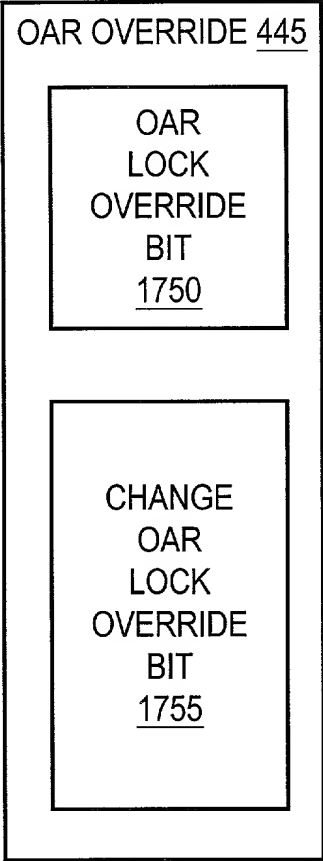


Fig. 17D

FIG. 17A, 17B, 17C, 17D

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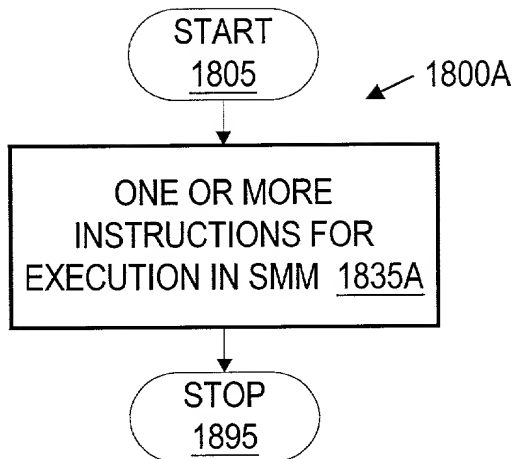


Fig. 18A
PRIOR ART

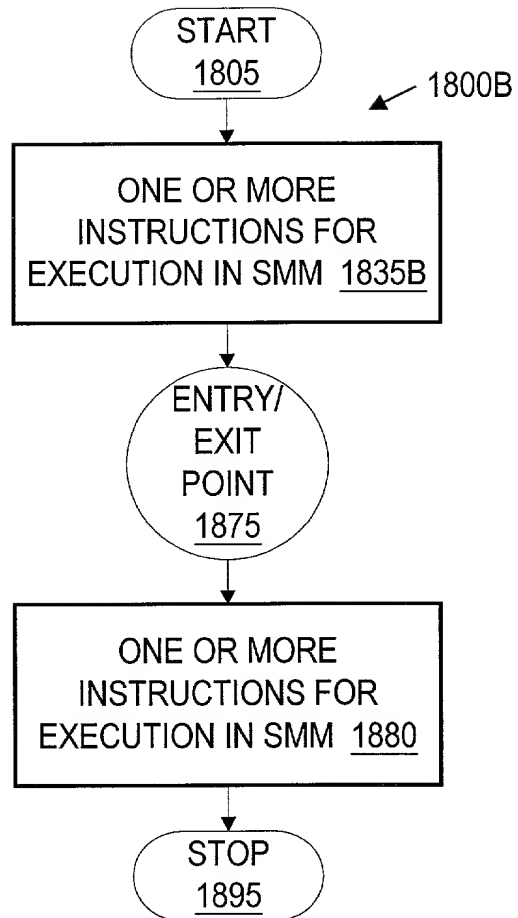


Fig. 18B

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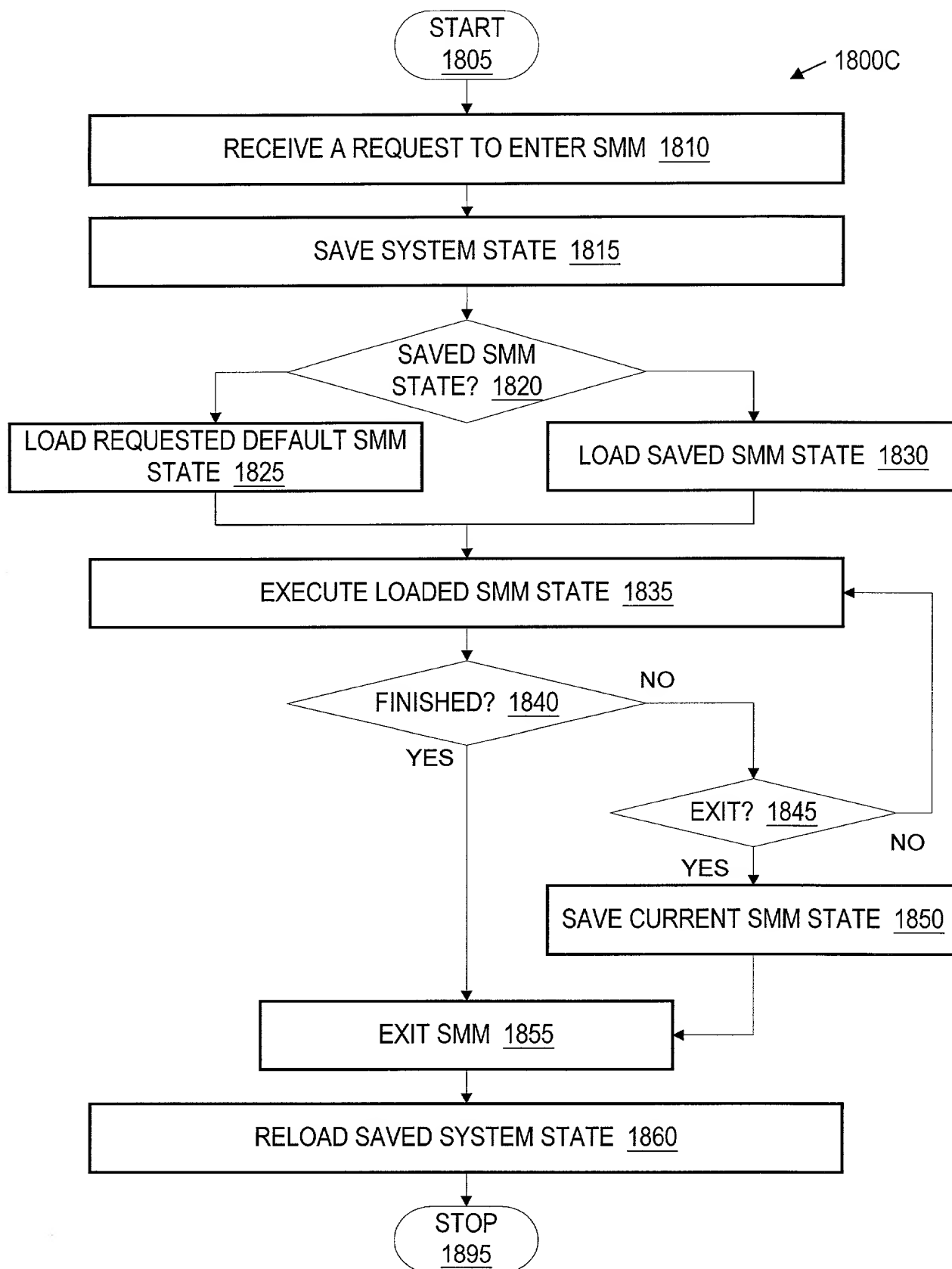


Fig. 18C

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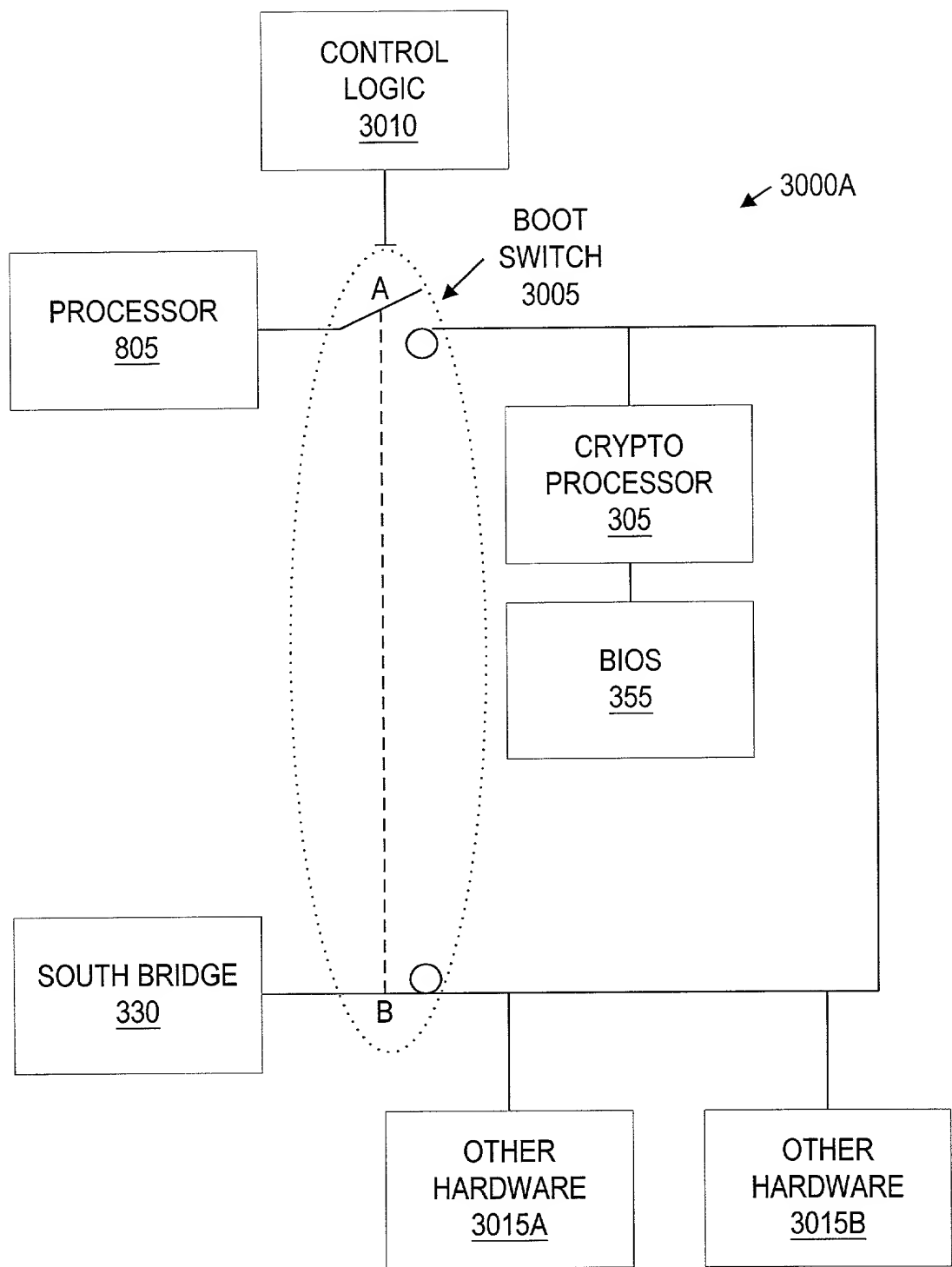


Fig. 19A

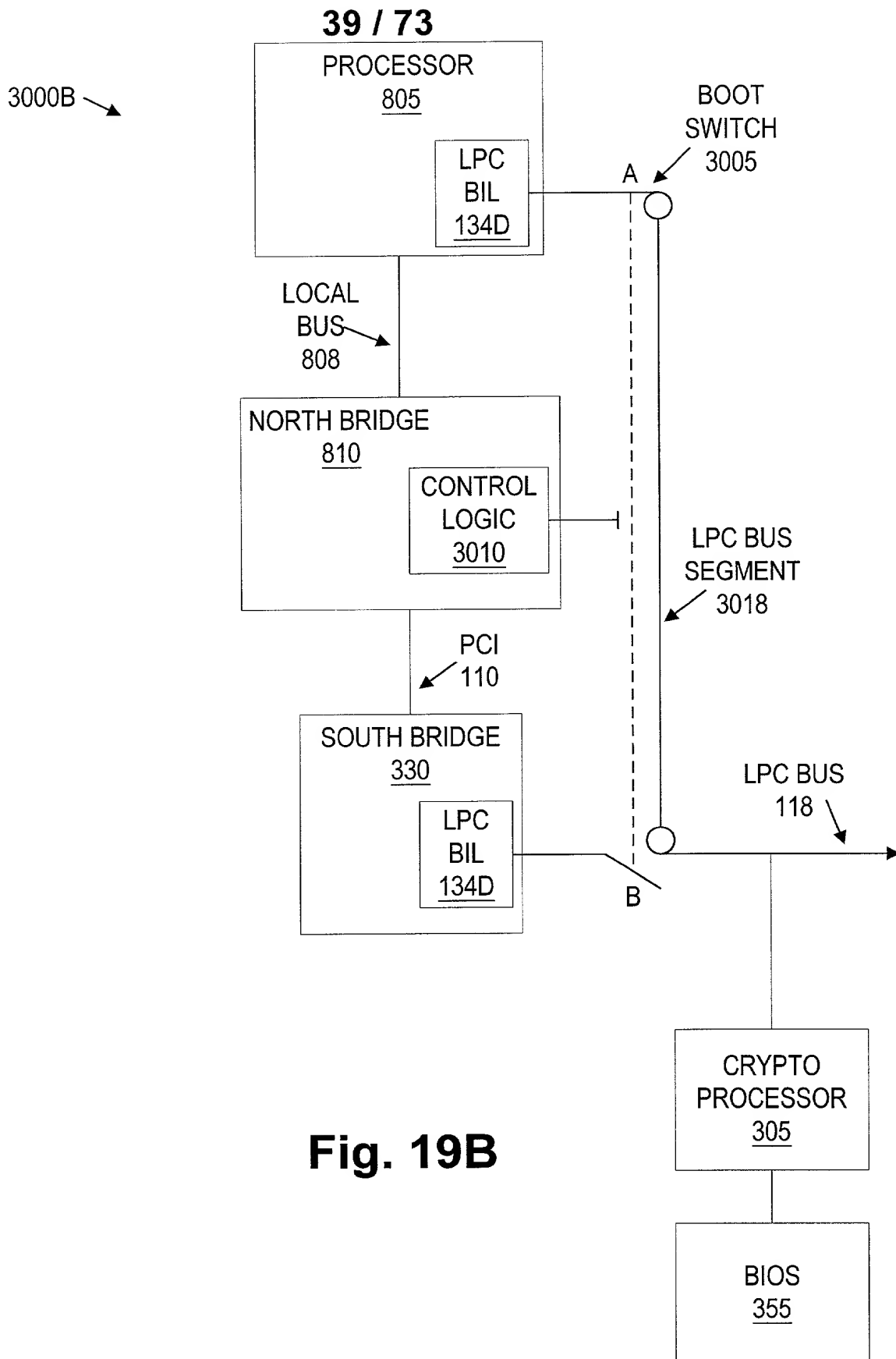


Fig. 19B

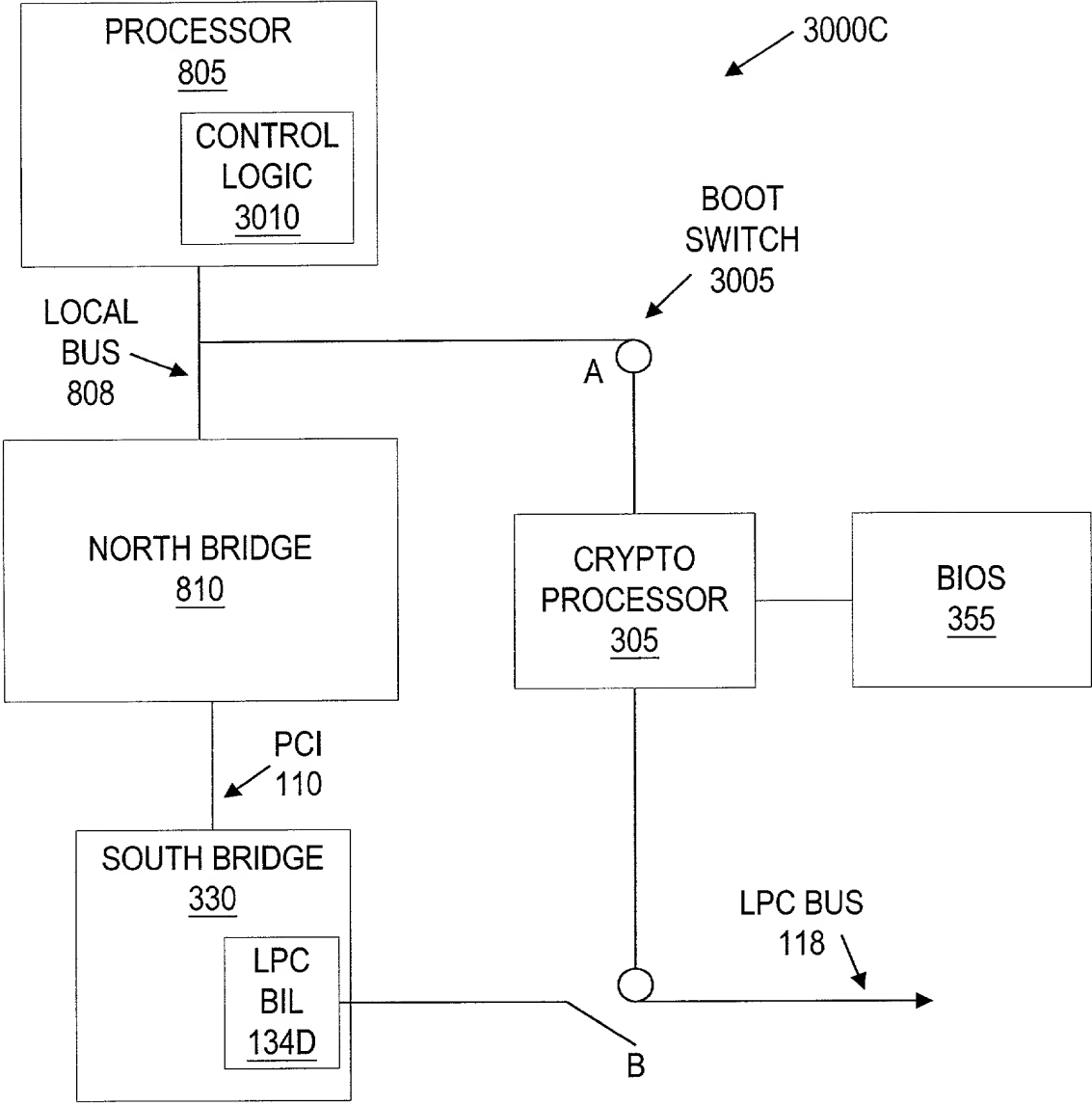
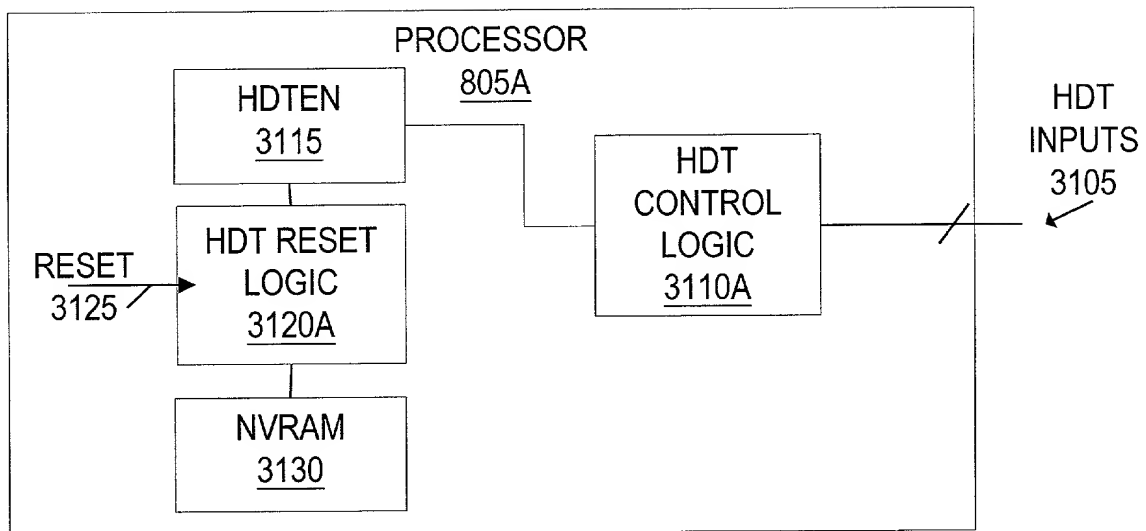
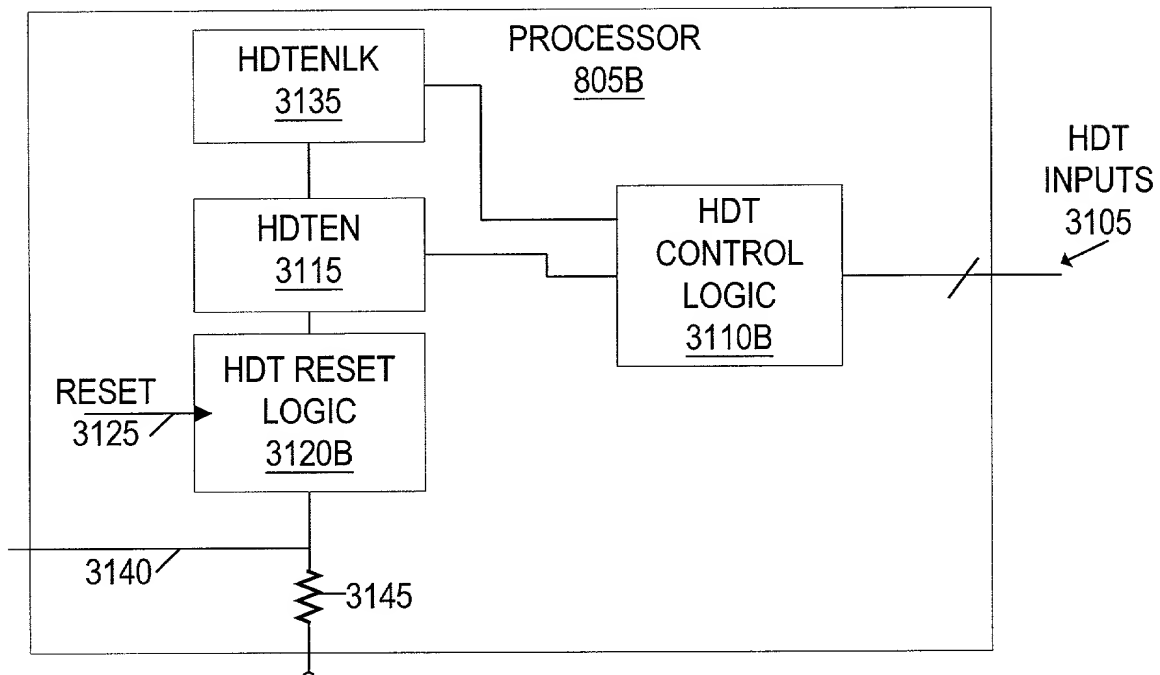
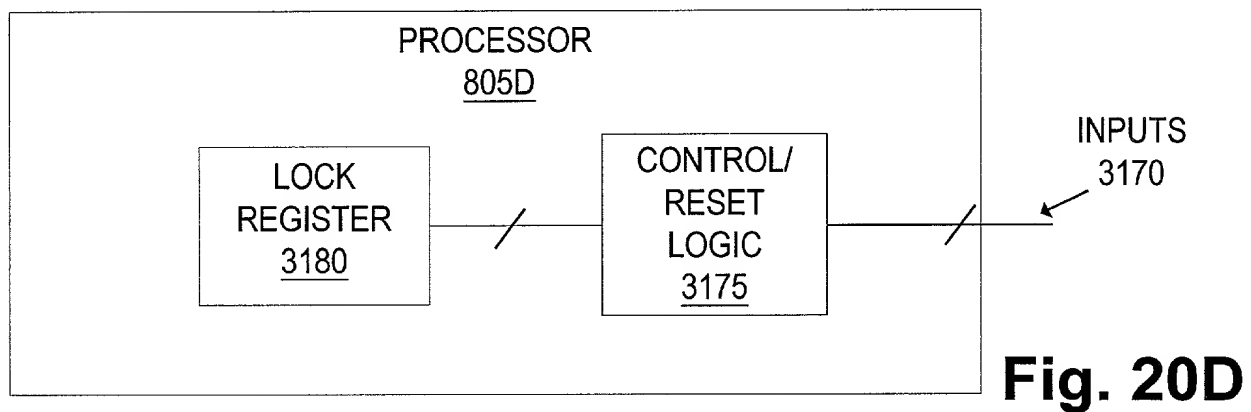
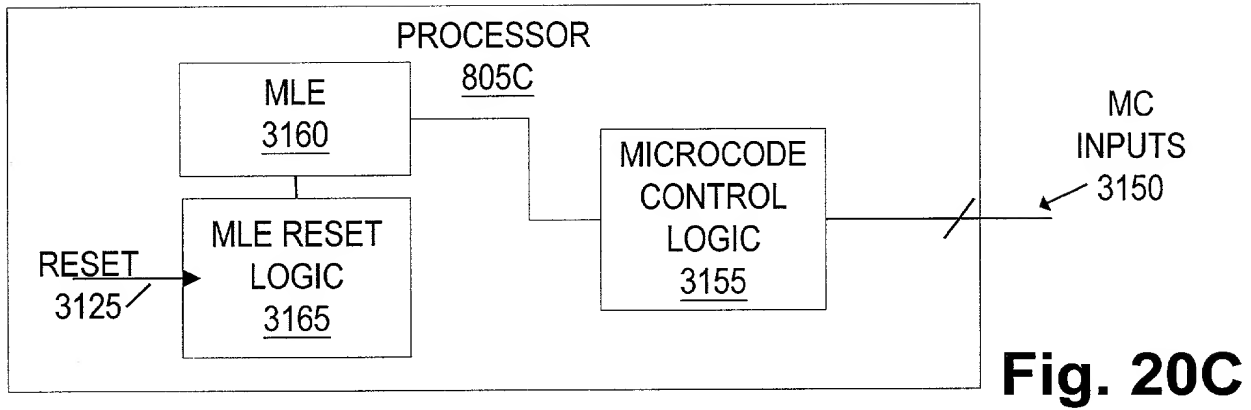


Fig. 19C

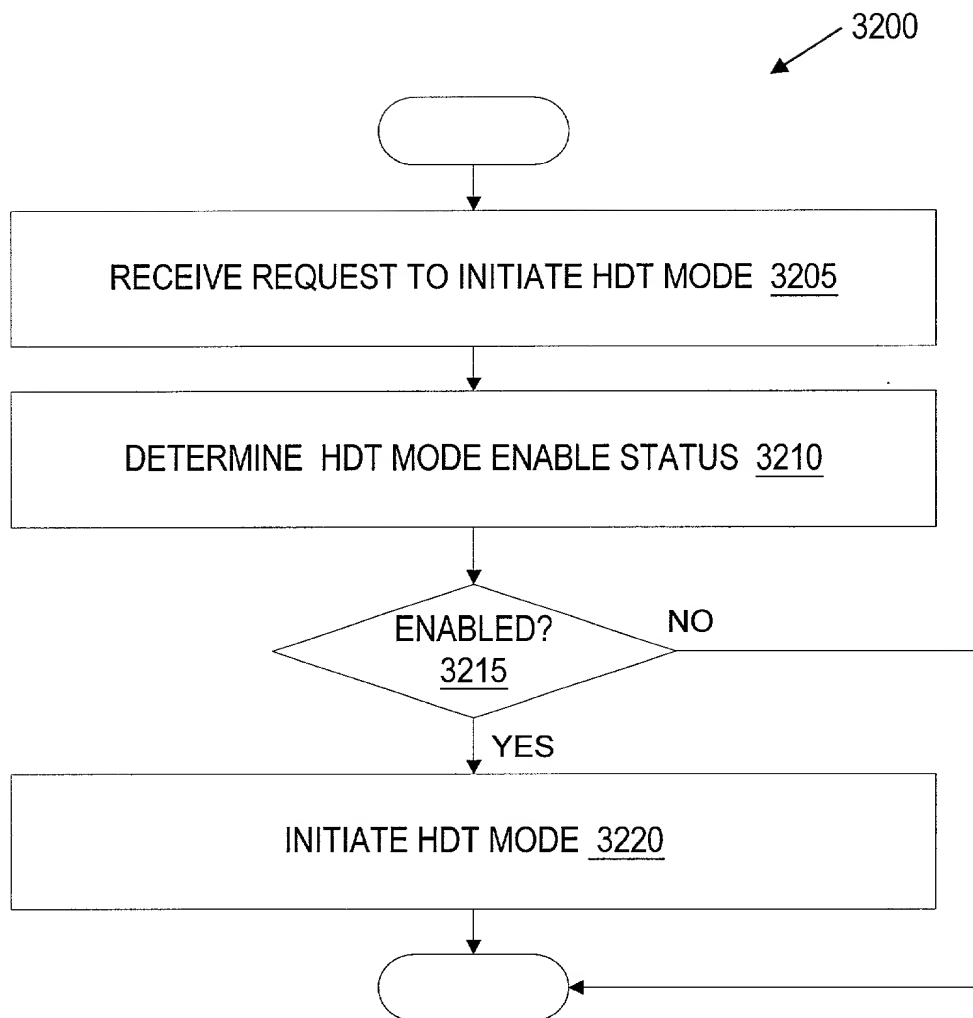
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**Fig. 20A****Fig. 20B**

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**Fig. 21**

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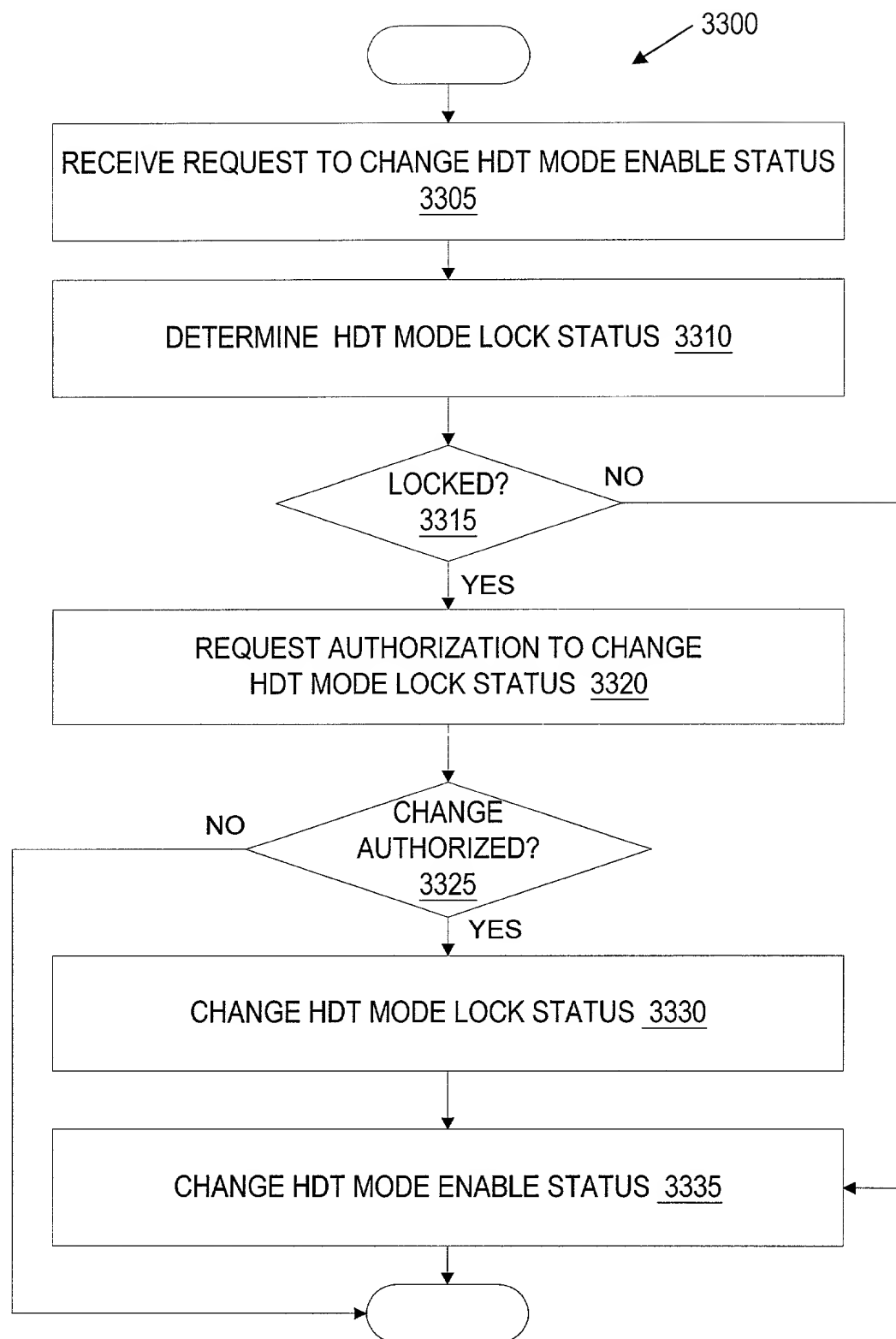
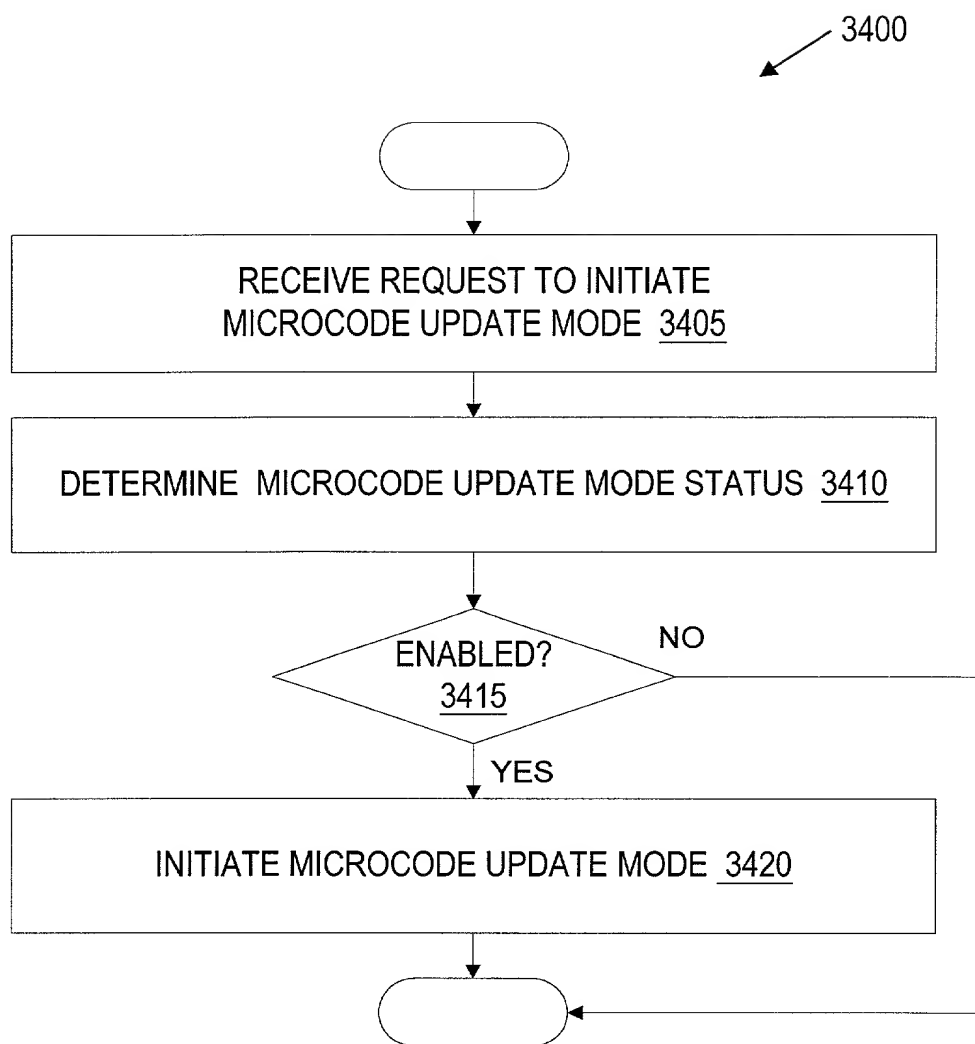


Fig. 22

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**Fig. 23**

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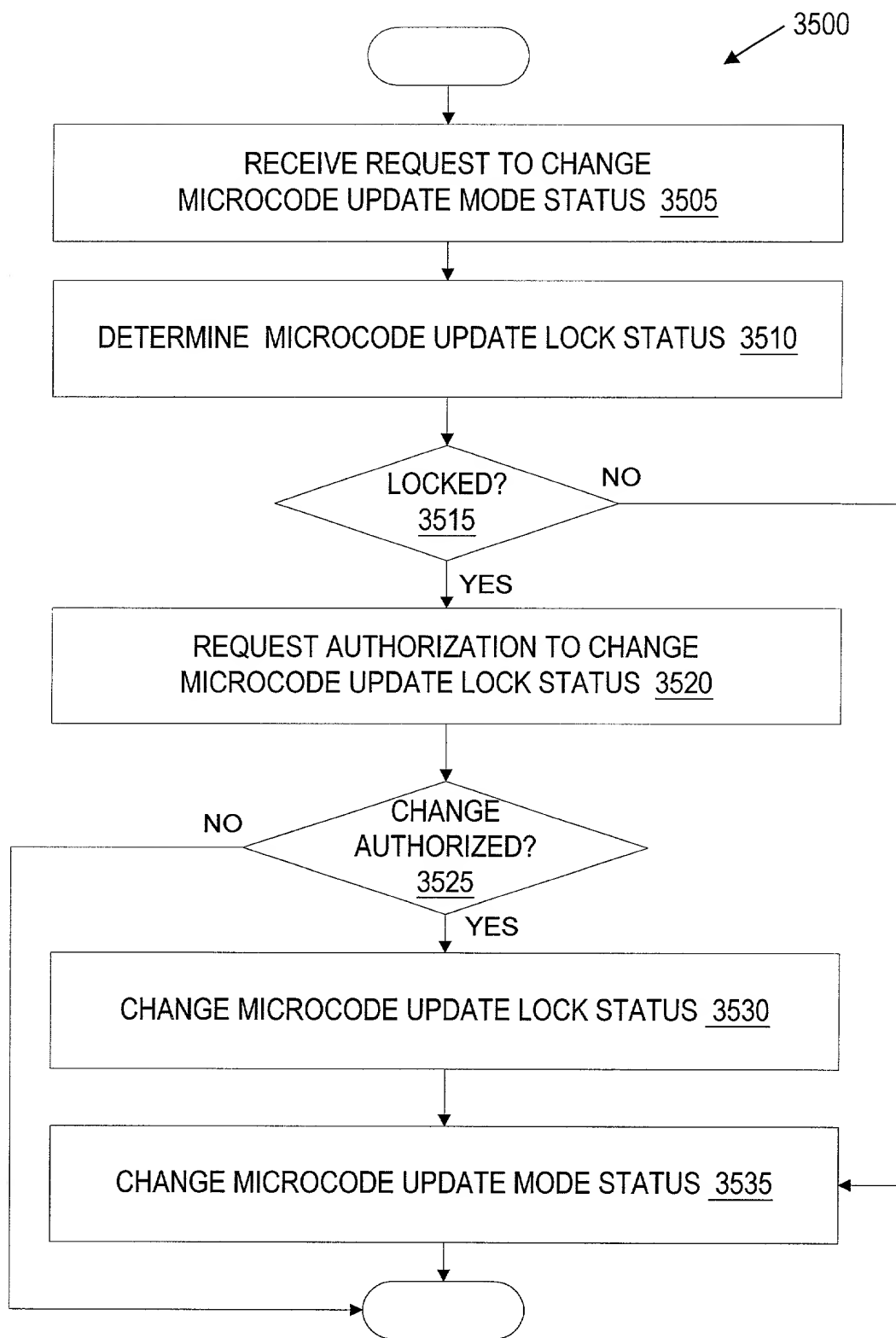


Fig. 24

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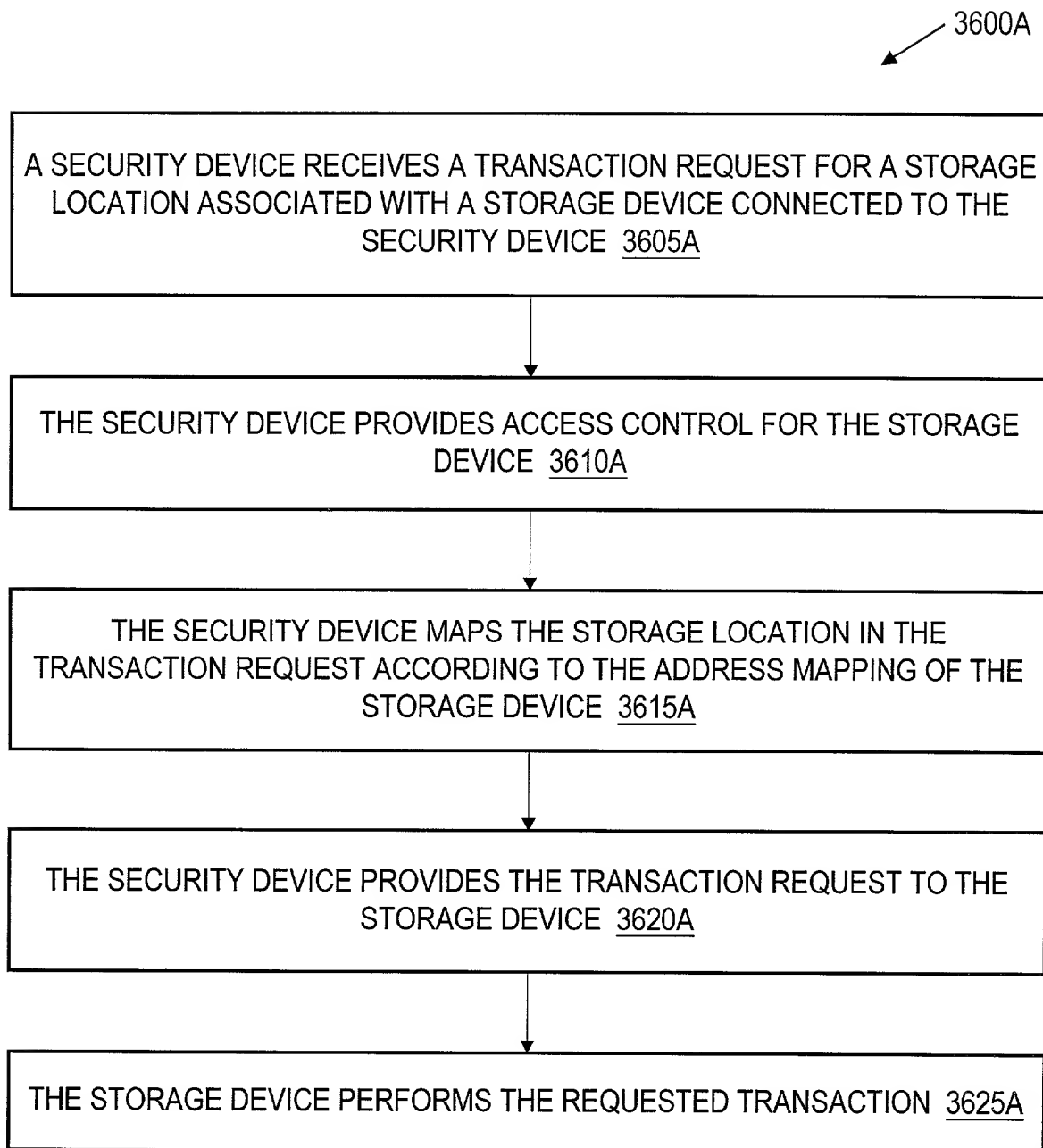


Fig. 25A

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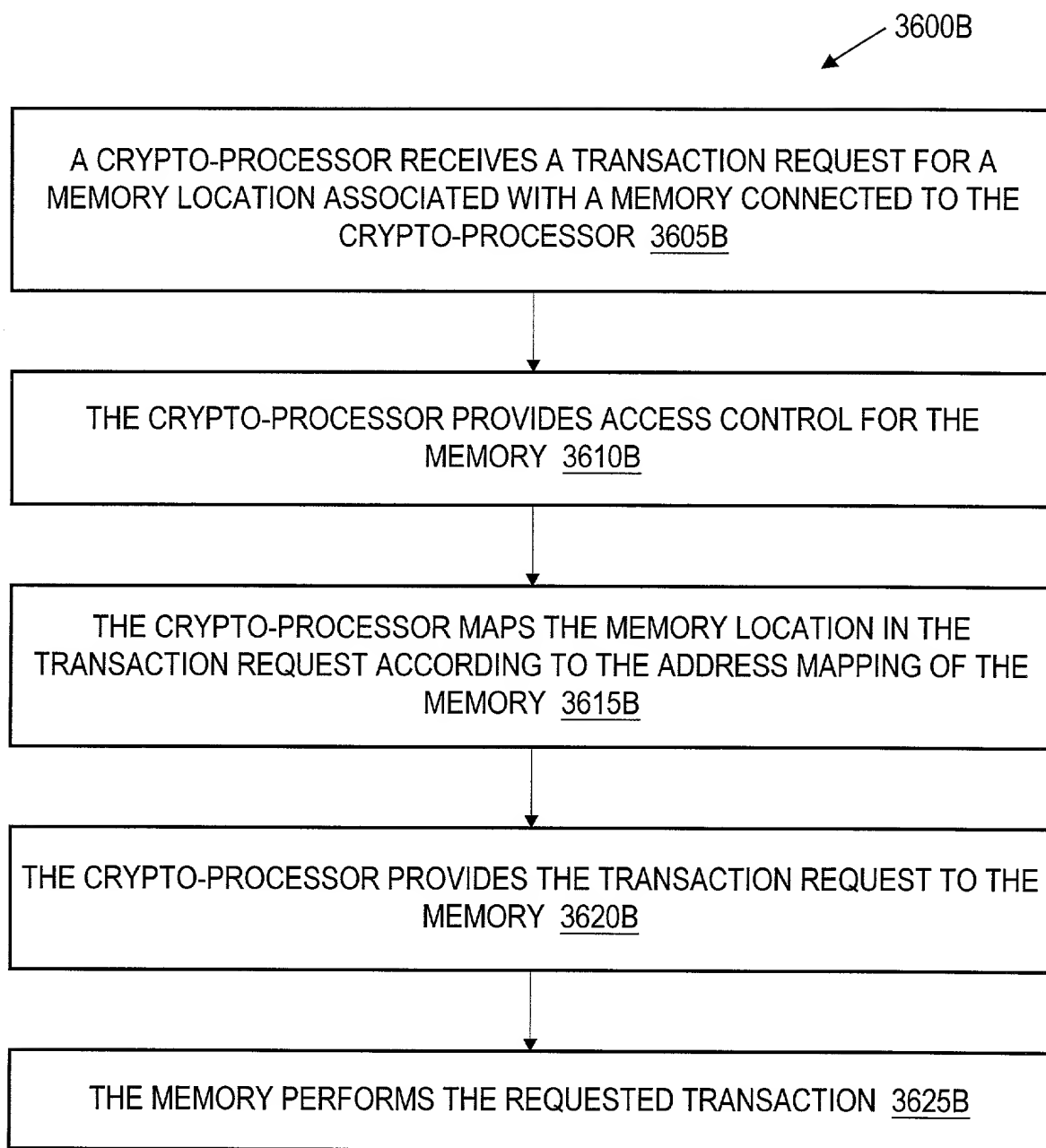


Fig. 25B

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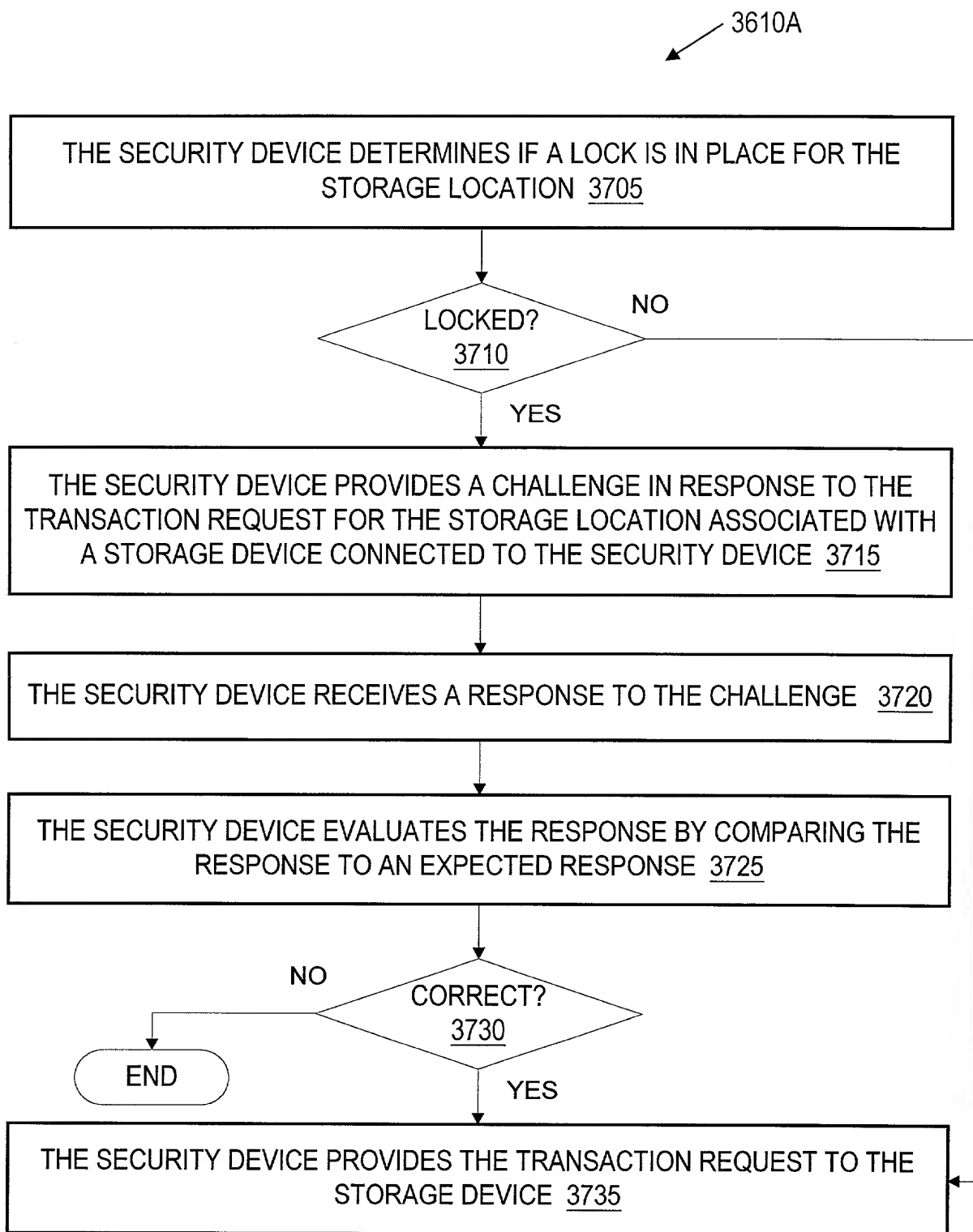
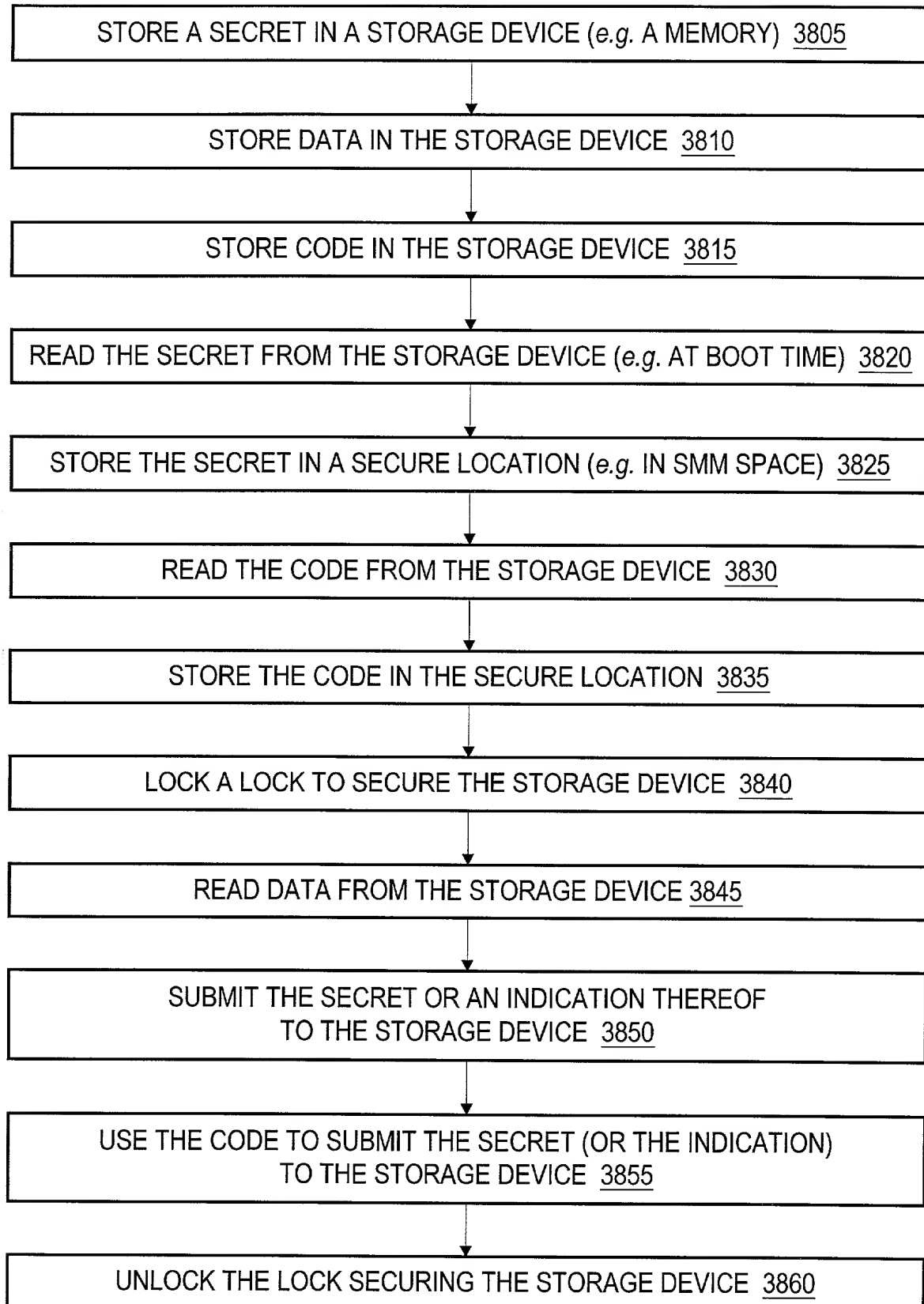


Fig. 26

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3620

**Fig. 27**

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3900

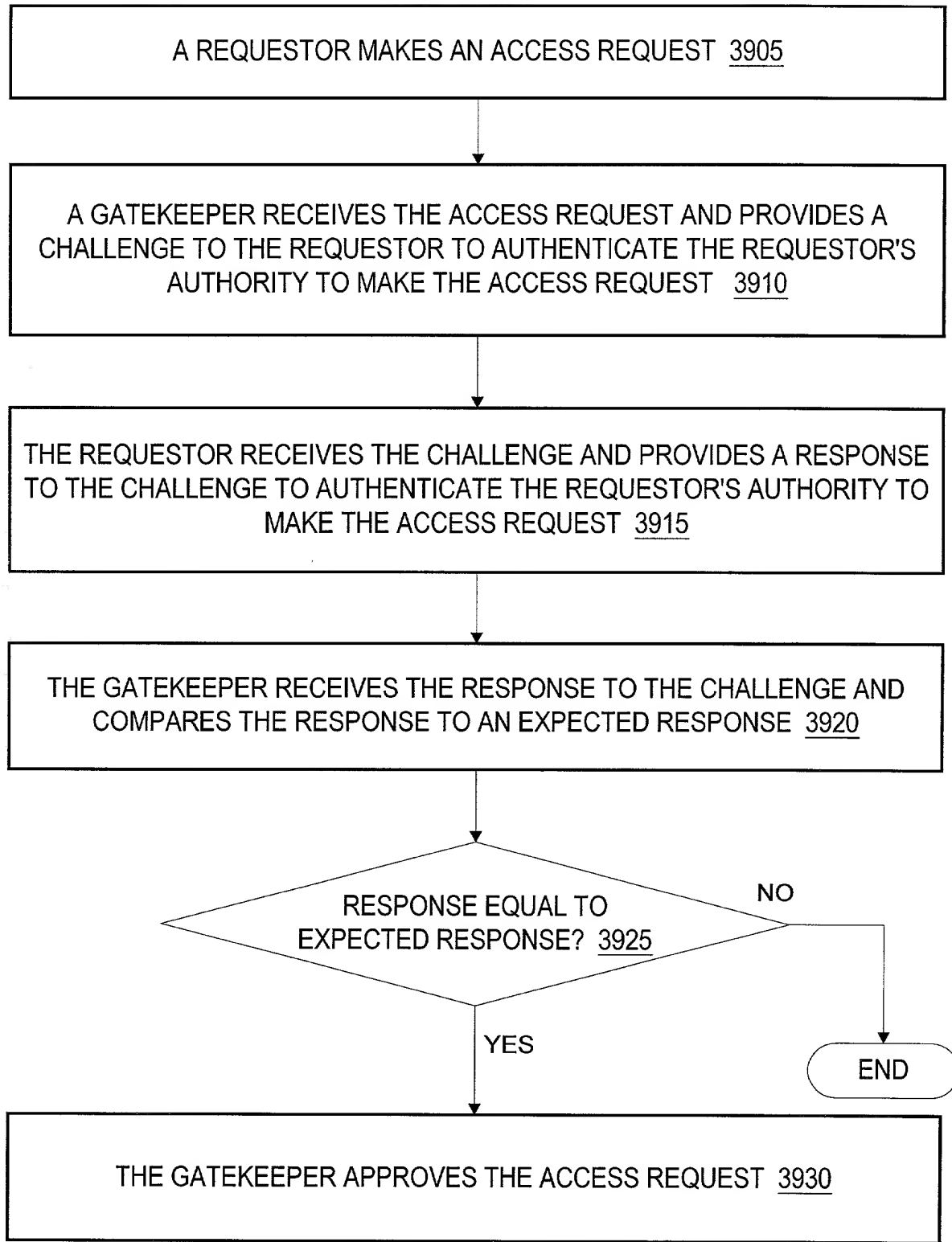
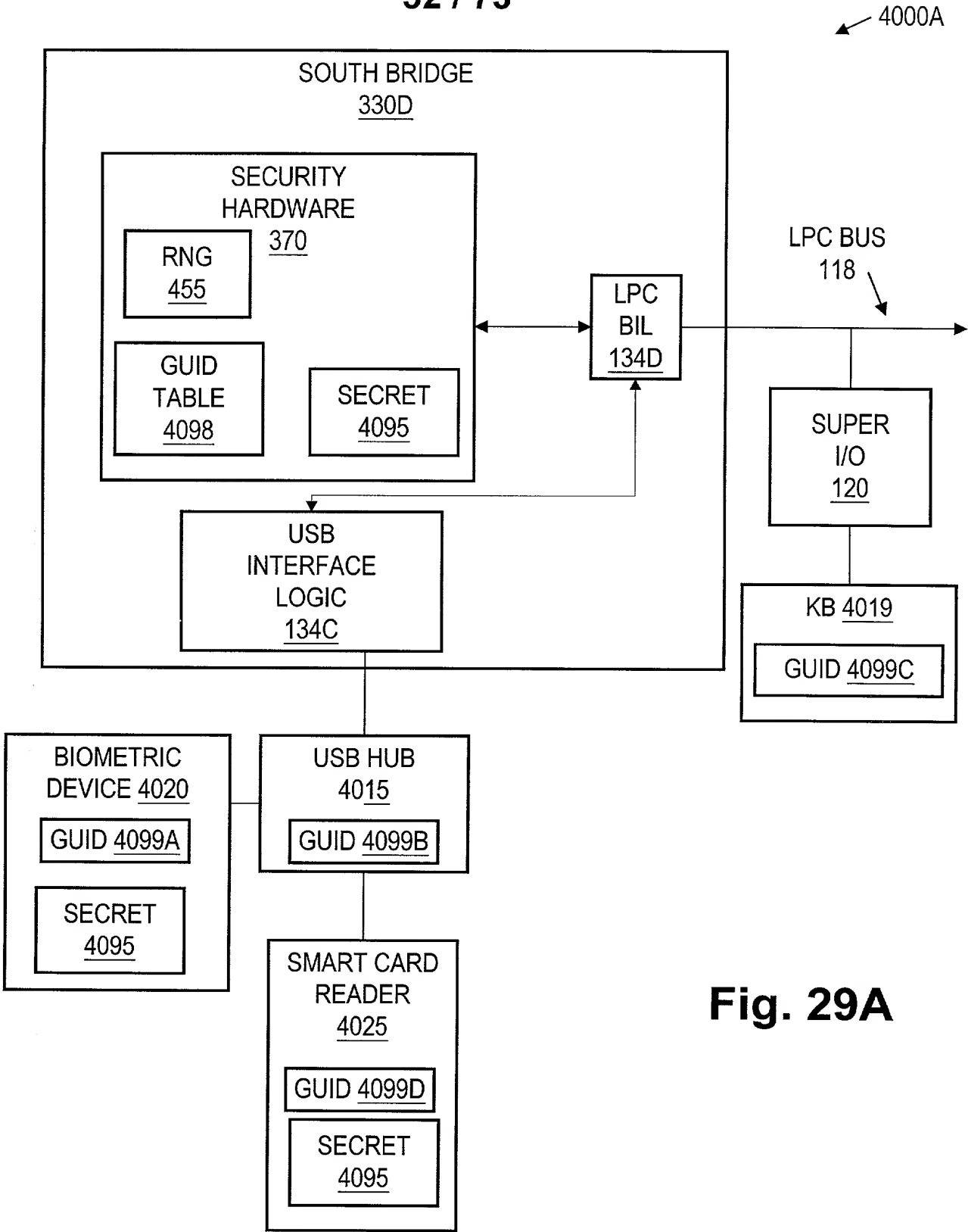


Fig. 28
(Prior Art)



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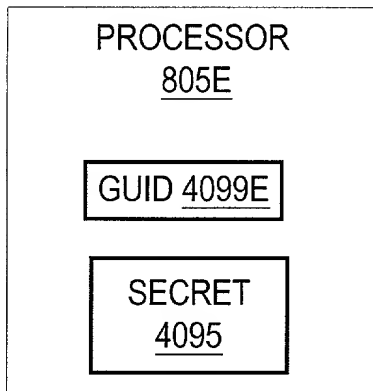


Fig. 29B

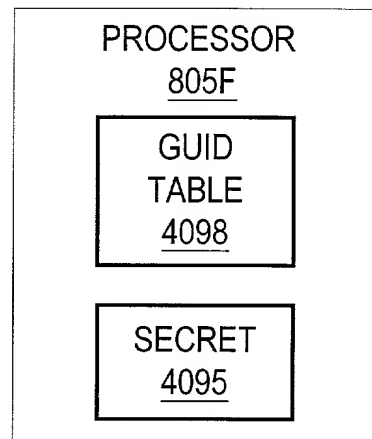


Fig. 29C

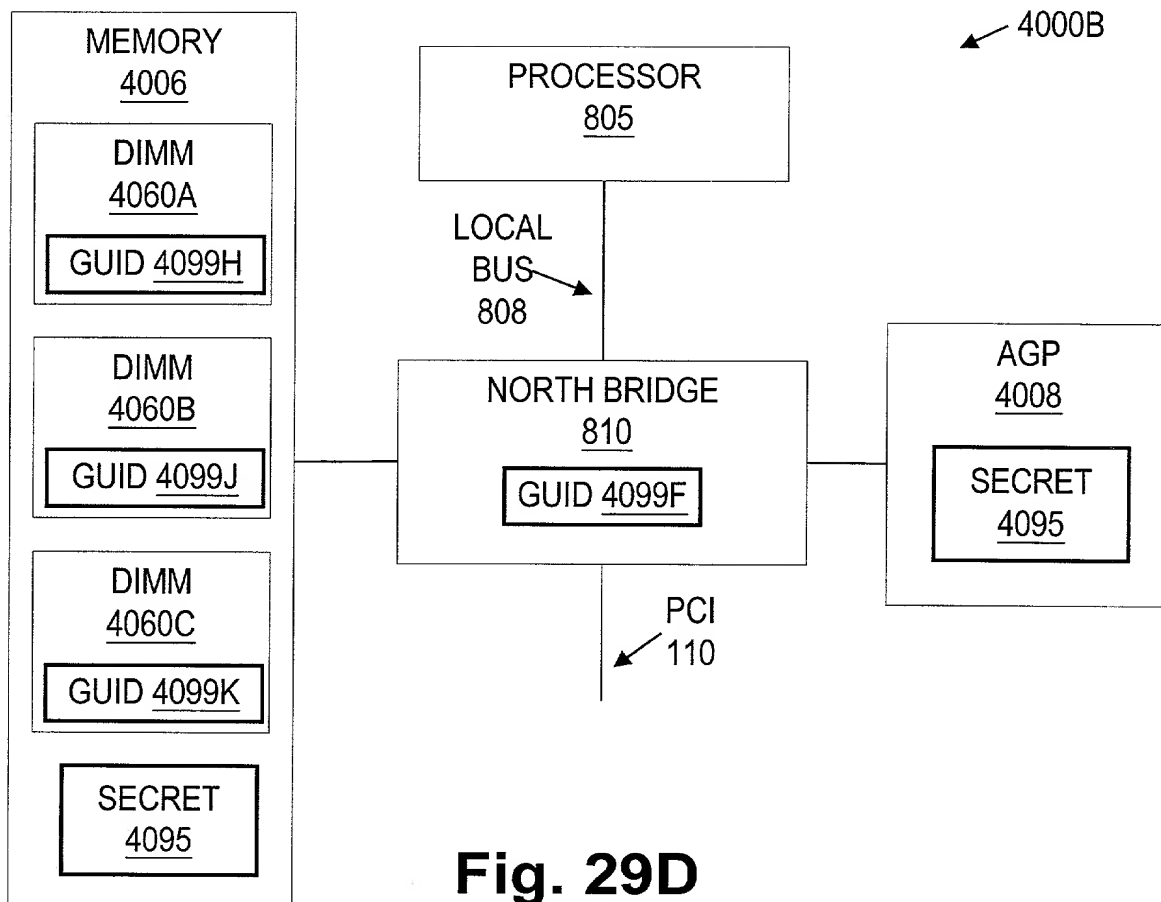


Fig. 29D

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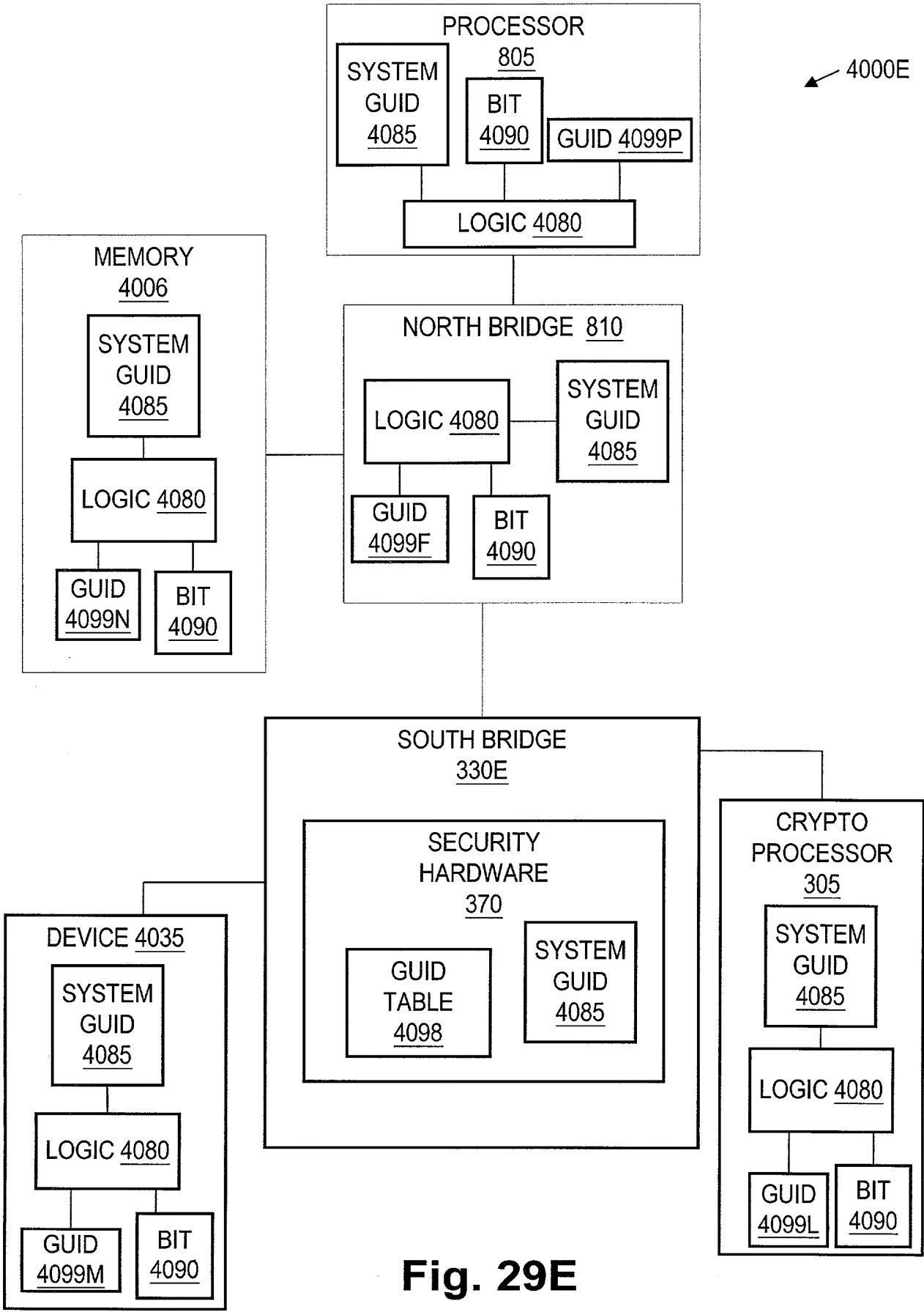


Fig. 29E

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4100A

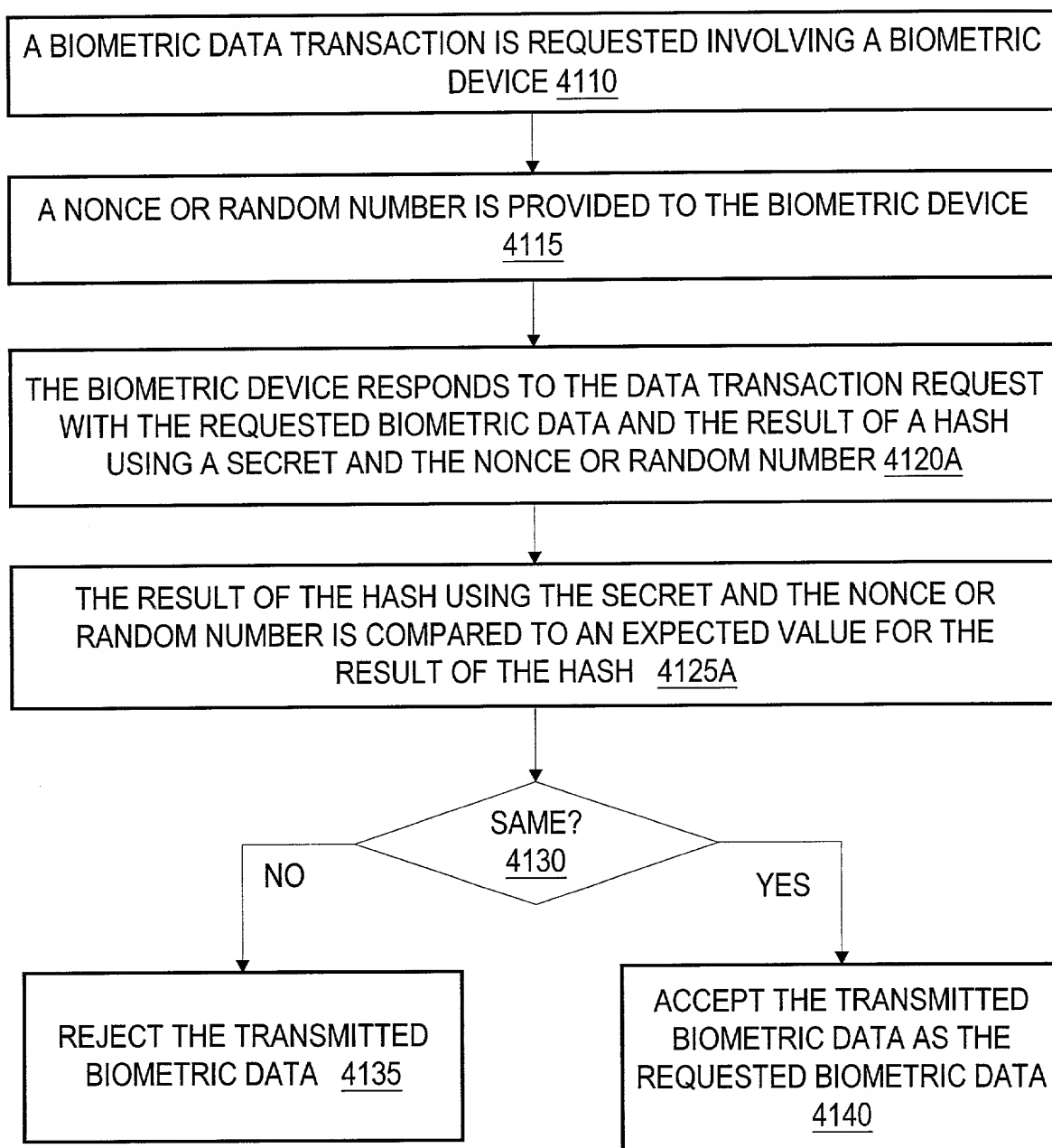


Fig. 30A

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4100B

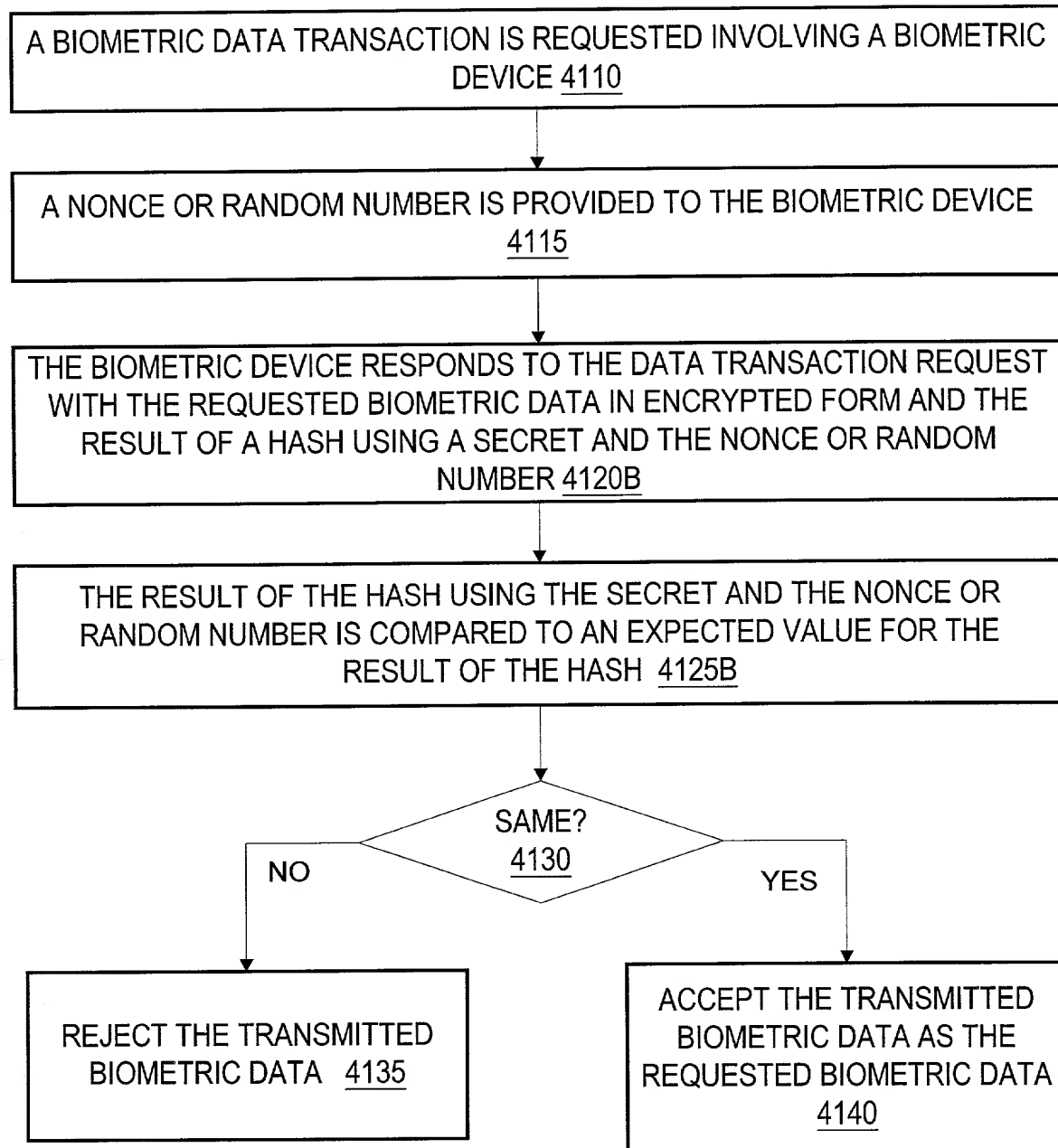


Fig. 30B

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4200A

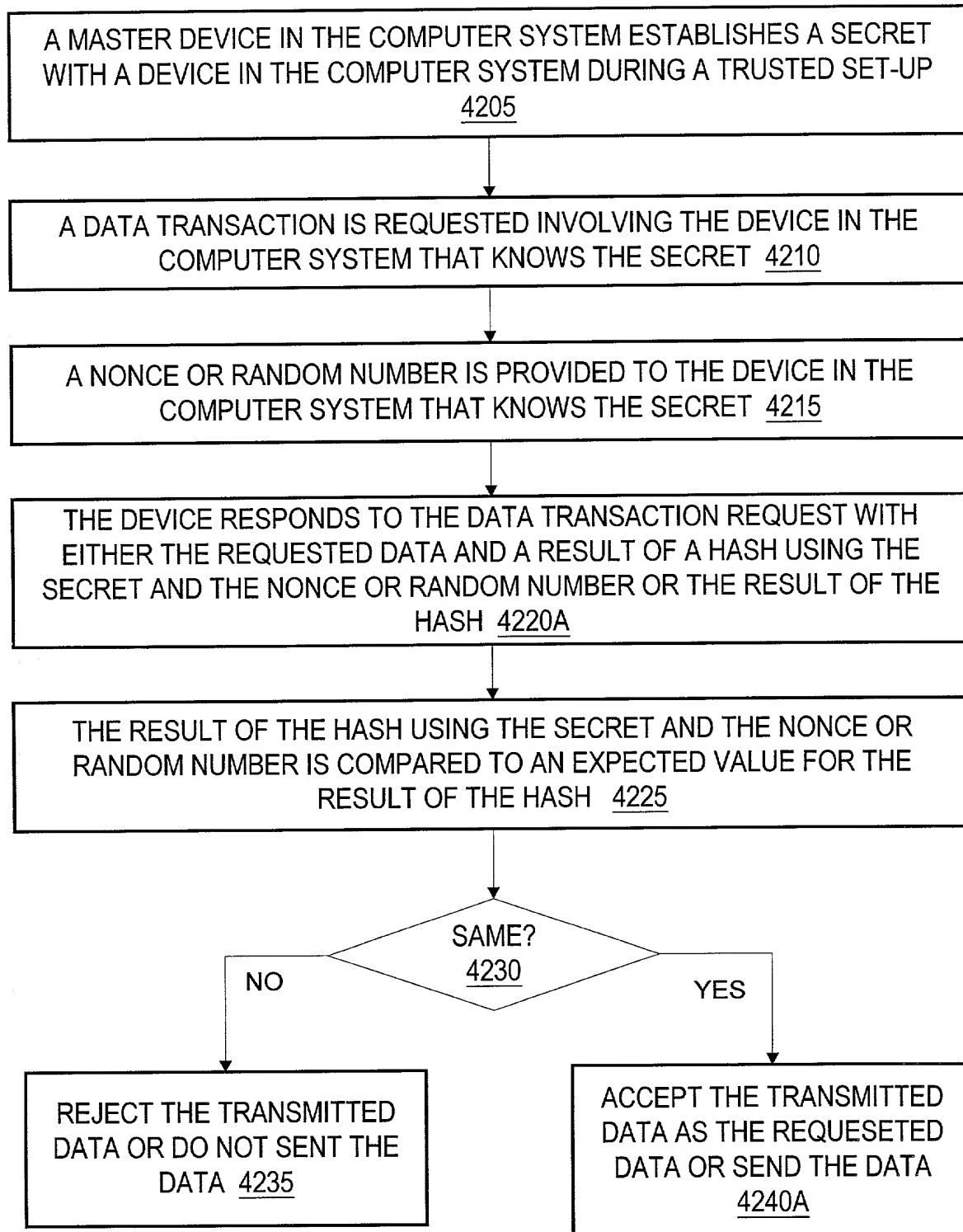


Fig. 31A

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4200B

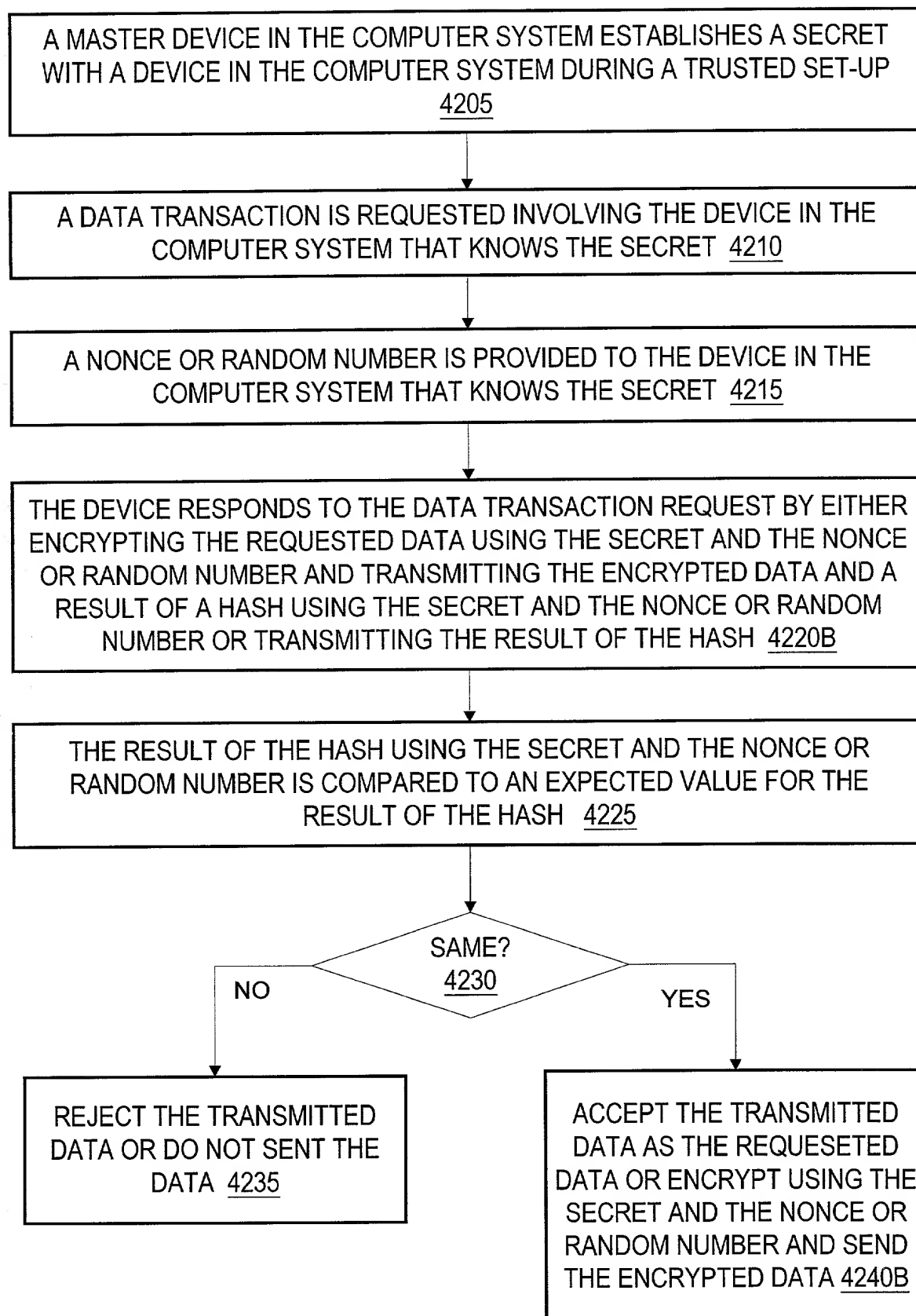


Fig. 31B

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4300A

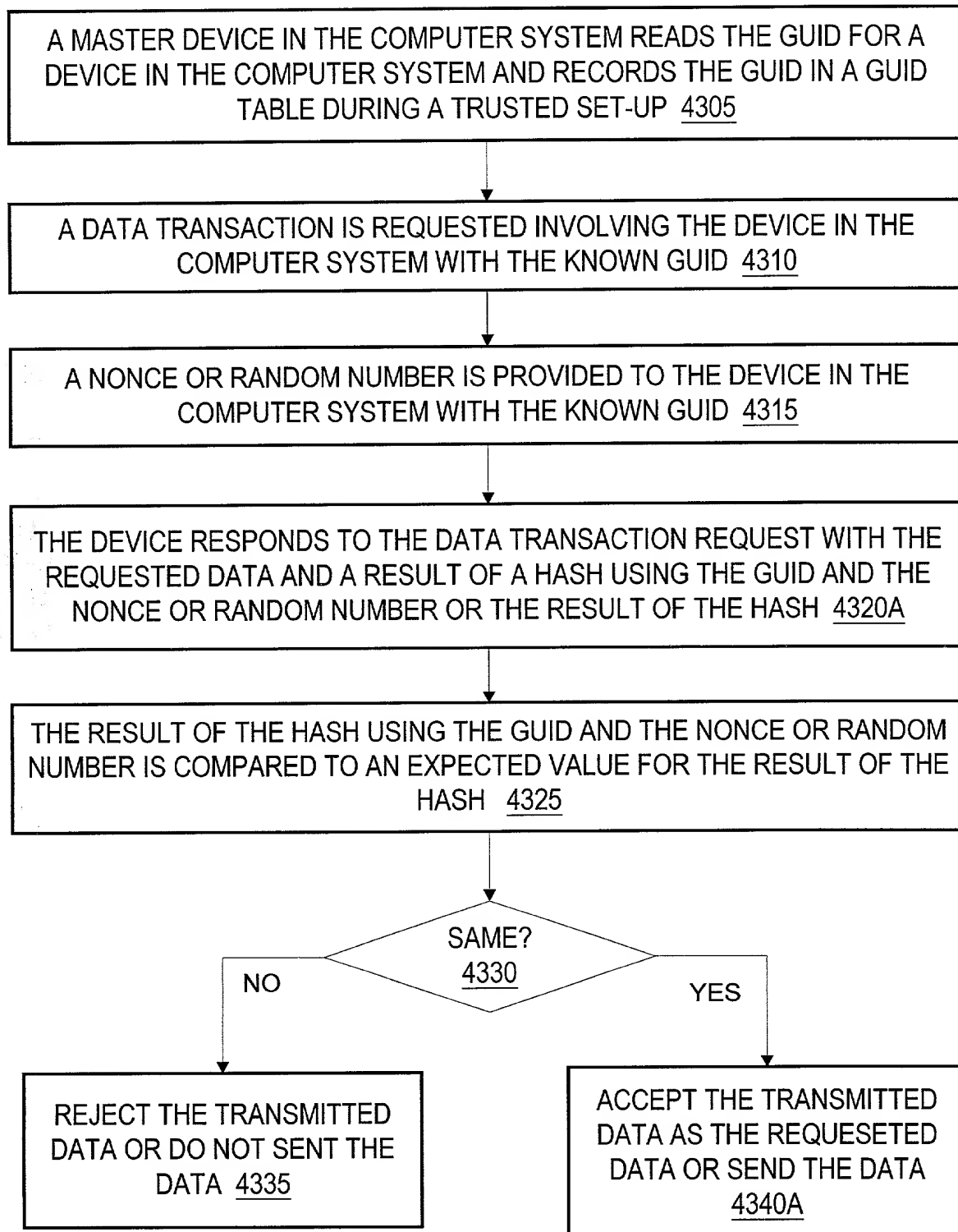


Fig. 32A

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4300B

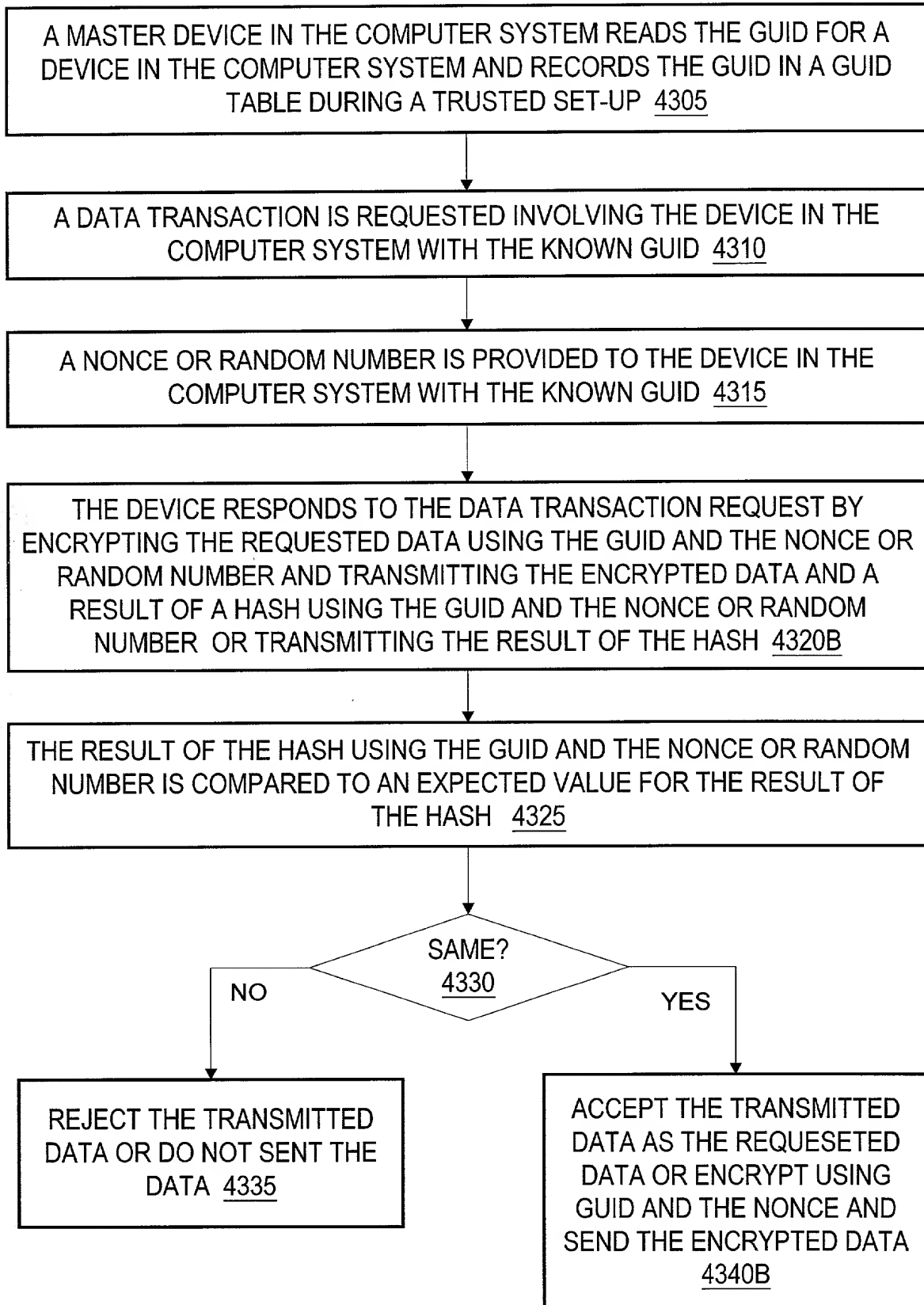


Fig. 32B

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4300C

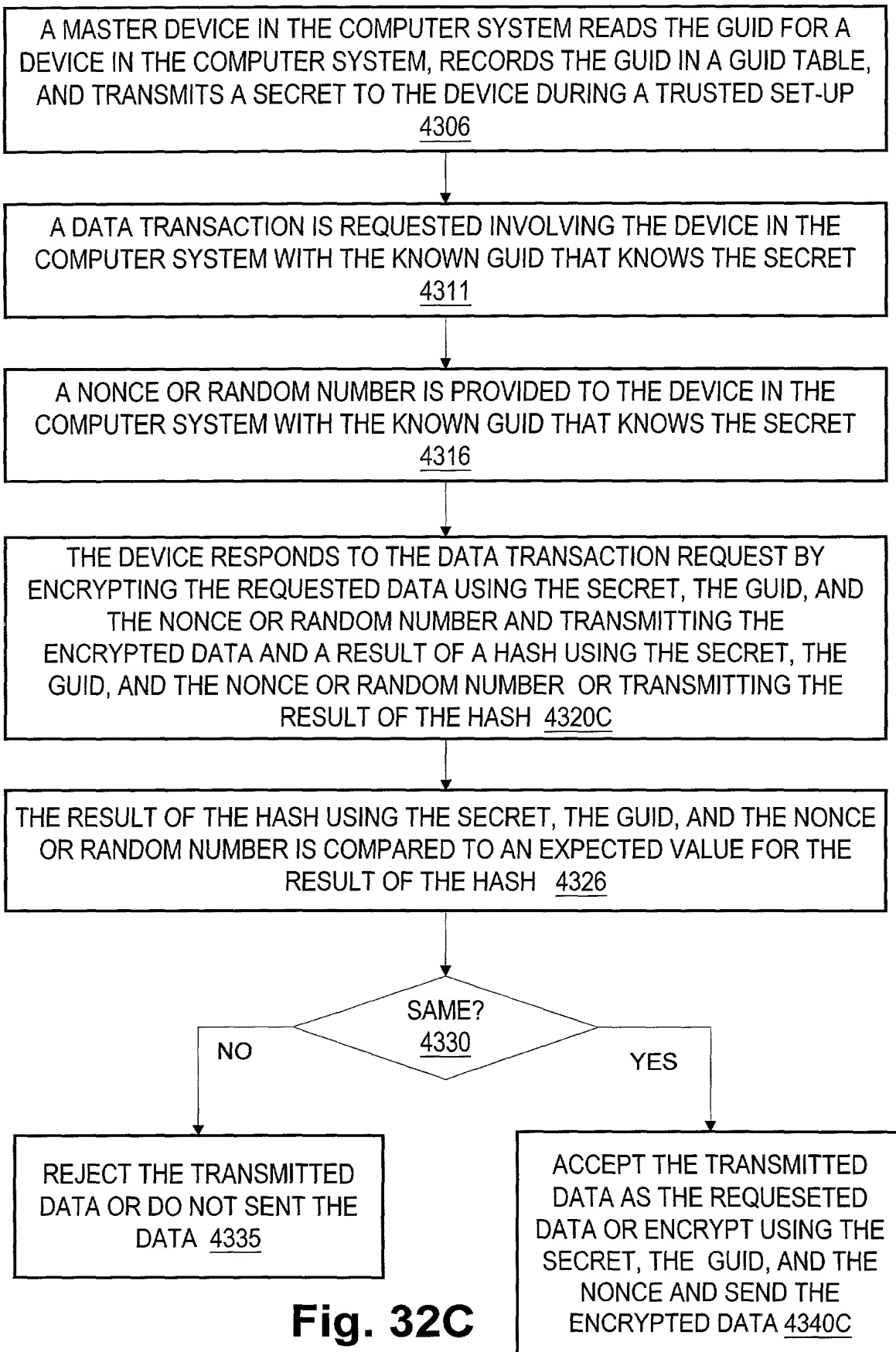


Fig. 32C

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4400

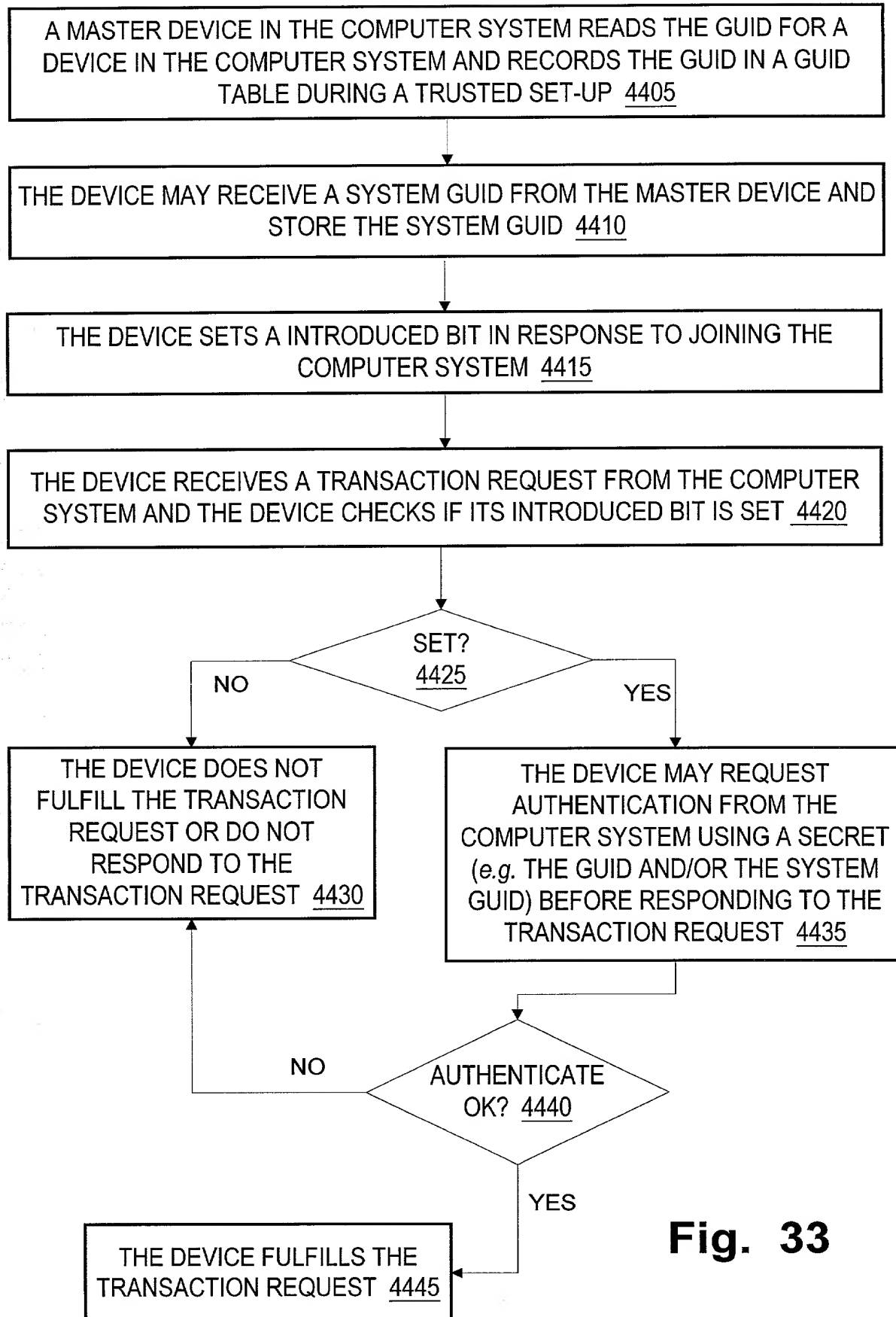
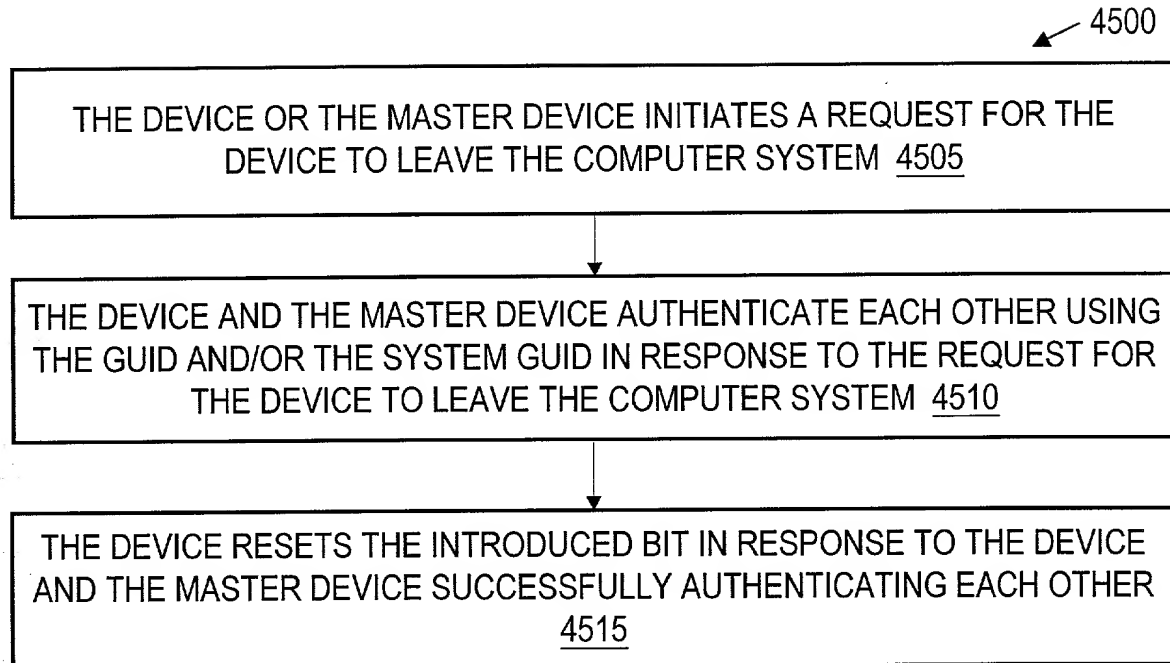
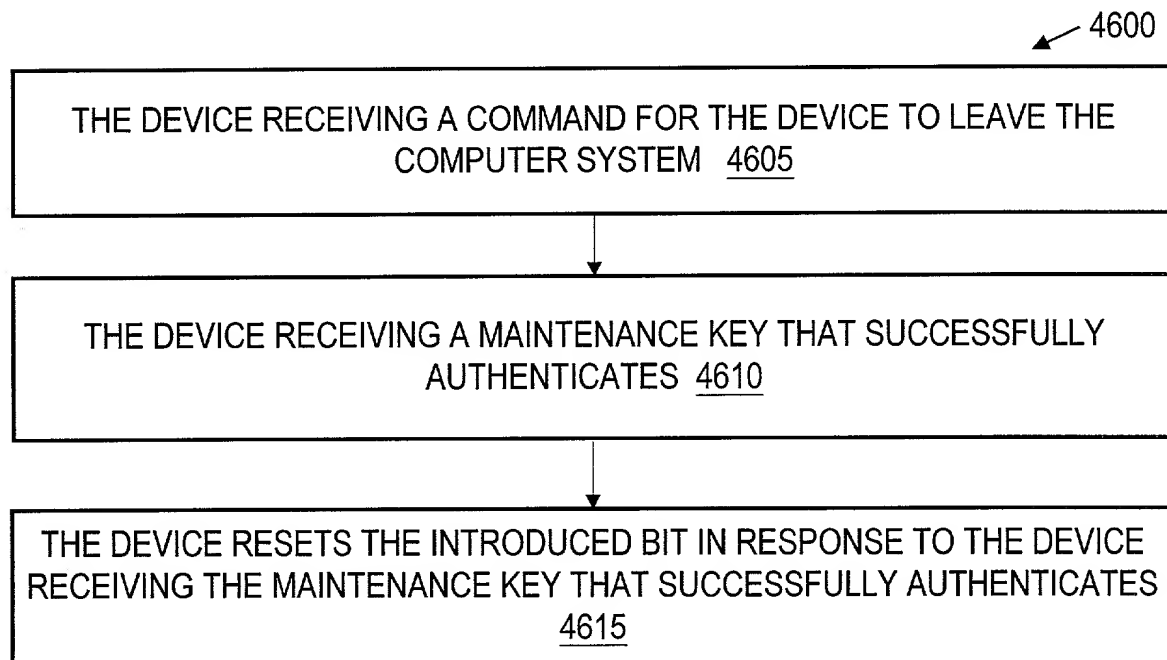


Fig. 33

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**Fig. 34****Fig. 35**

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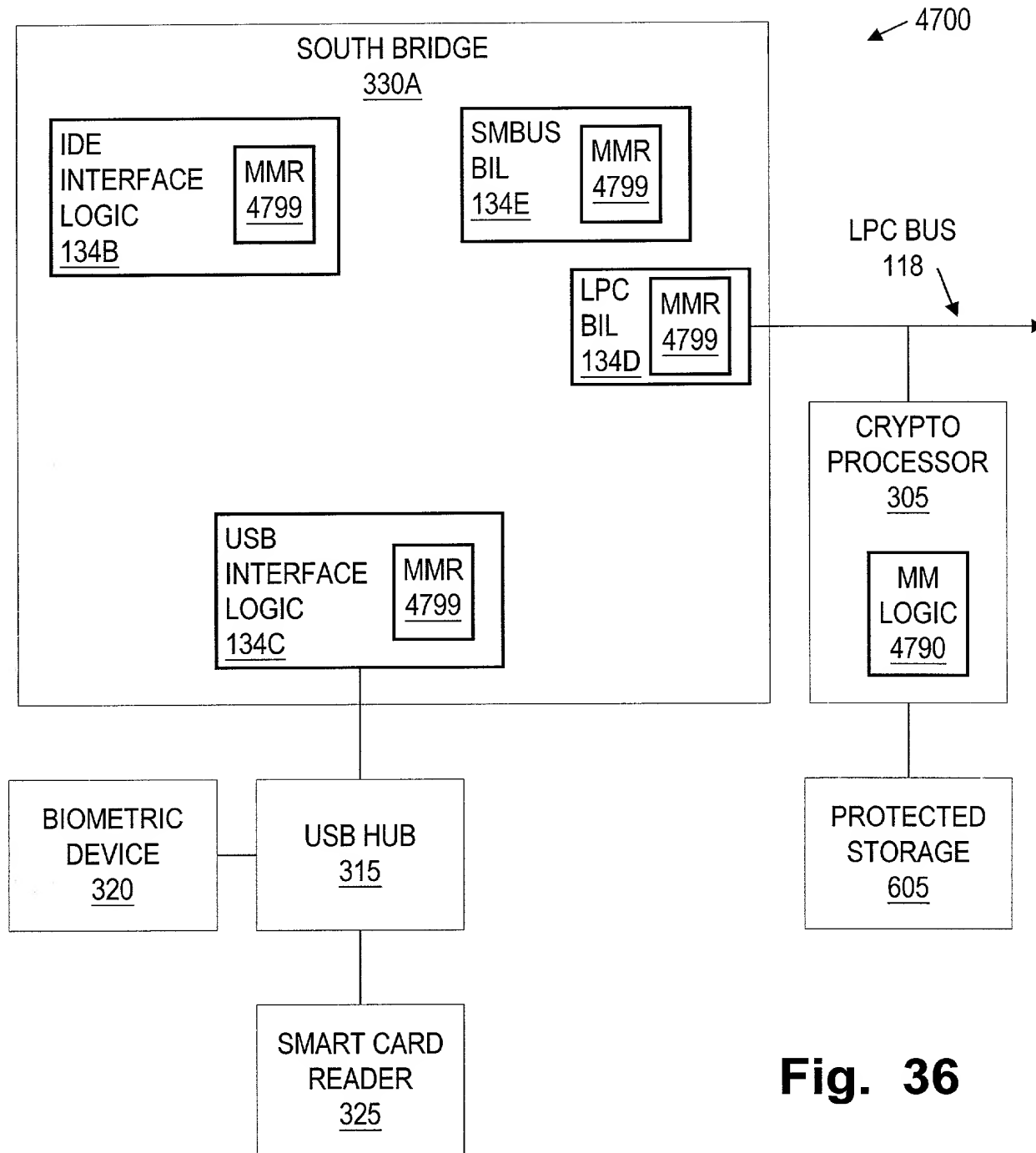


Fig. 36

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4800

TRANSMIT A MASTER MODE SIGNAL TO BUS INTERFACE LOGIC CONNECTED BETWEEN MASTER MODE LOGIC AND A DATA INPUT DEVICE, WHERE THE BUS INTERFACE LOGIC INCLUDES A MASTER MODE REGISTER

4805

SET A MASTER MODE BIT IN THE MASTER MODE REGISTER(S) TO ESTABLISH SECURE TRANSMISSION CHANNEL BETWEEN THE MASTER MODE LOGIC AND THE DATA INPUT DEVICE OUTSIDE THE OPERATING SYSTEM OF THE COMPUTER SYSTEM 4810

THE MASTER MODE LOGIC AND THE DATA INPUT DEVICE EXCHANGE DATA OUTSIDE THE OPERATING SYSTEM OF THE COMPUTER SYSTEM THROUGH THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER 4815

THE MASTER MODE LOGIC FLUSHES THE BUFFERS OF THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER AFTER CONCLUDING THE DATA TRANSMISSIONS 4820

THE MASTER MODE LOGIC SIGNALS THE BUS INTERFACE LOGIC(S) TO UNSET THE MASTER MODE BITS AFTER FLUSHING THE BUFFERS OF THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER 4825

Fig. 37

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4900A

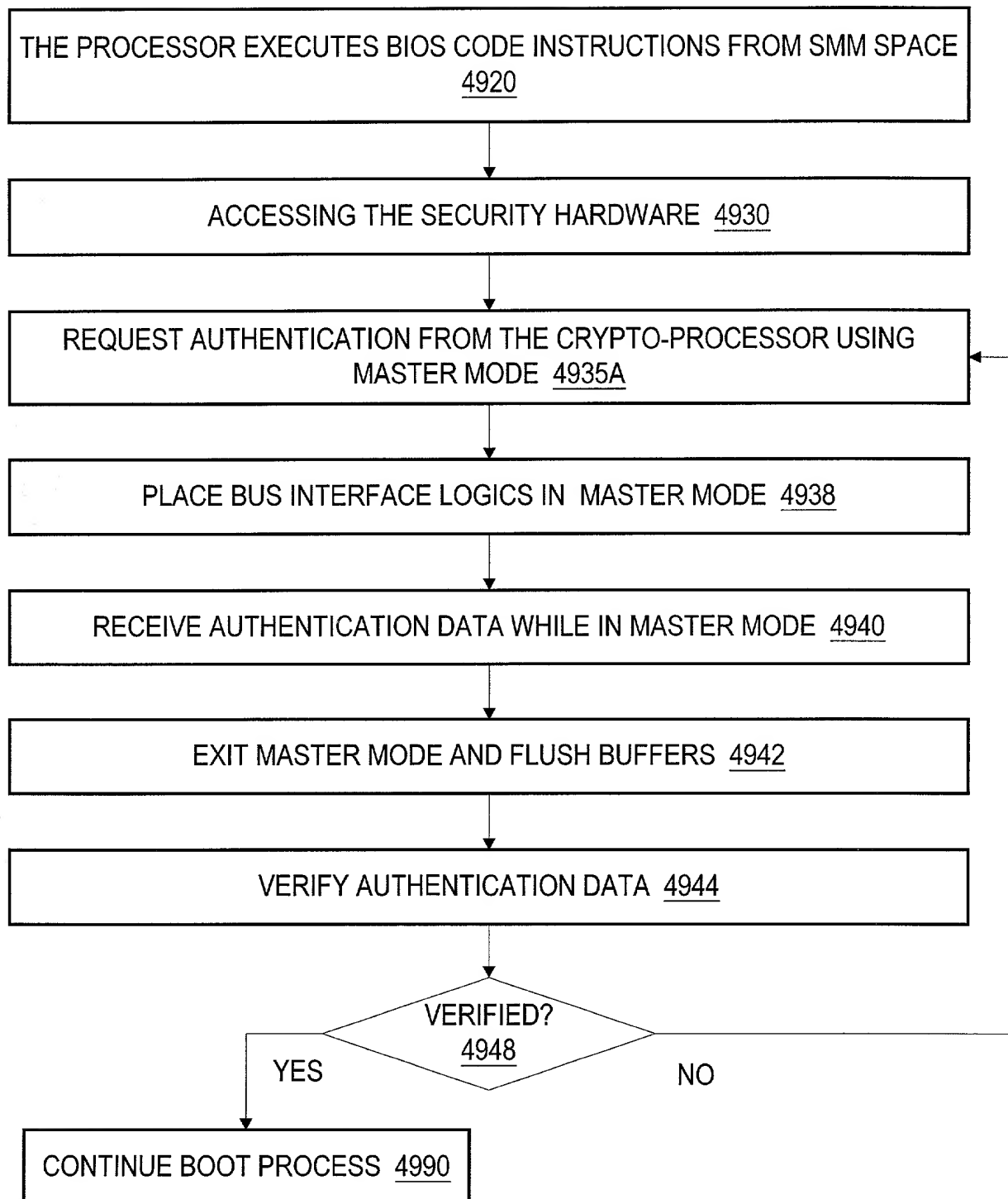


Fig. 38A

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4900B

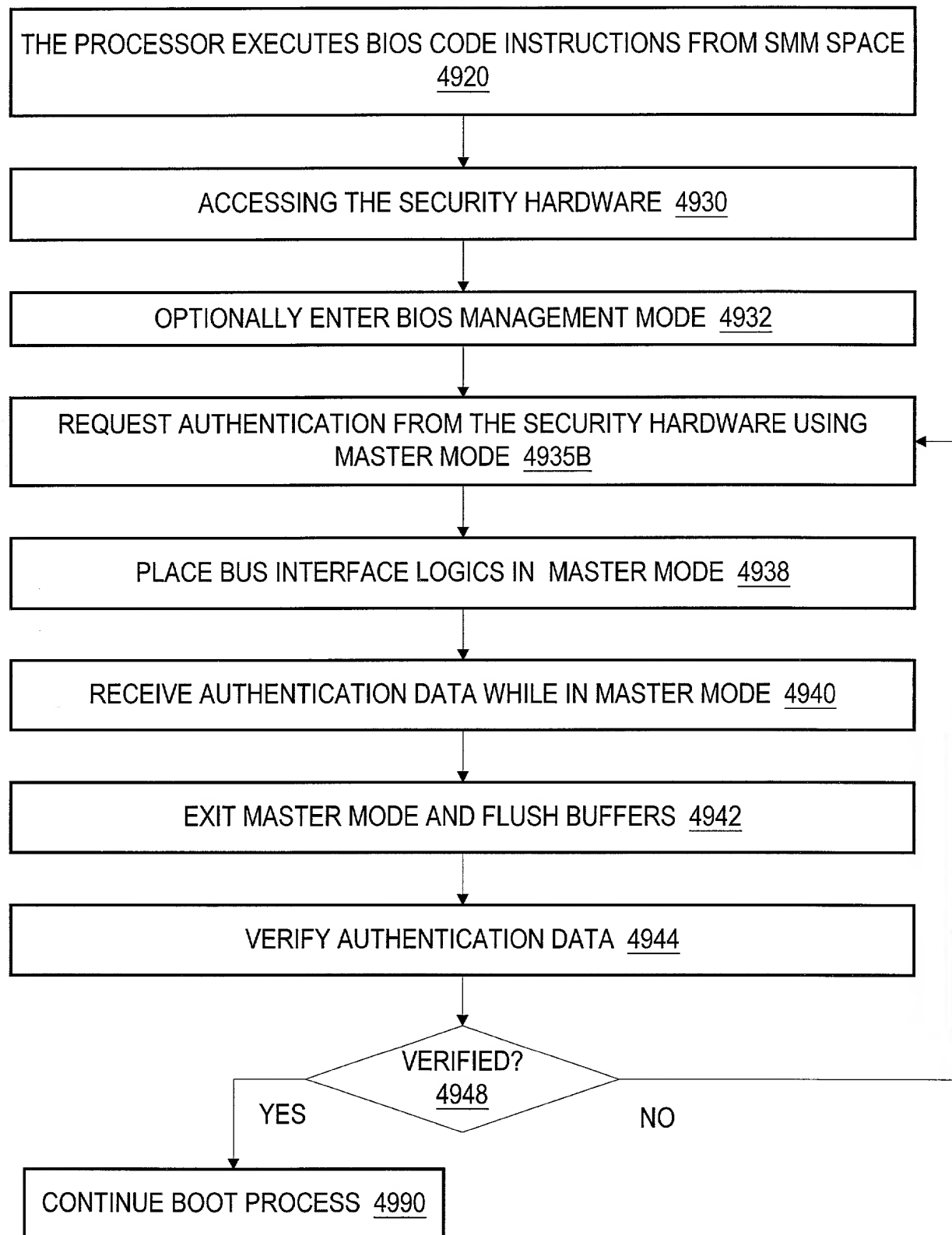


Fig. 38B

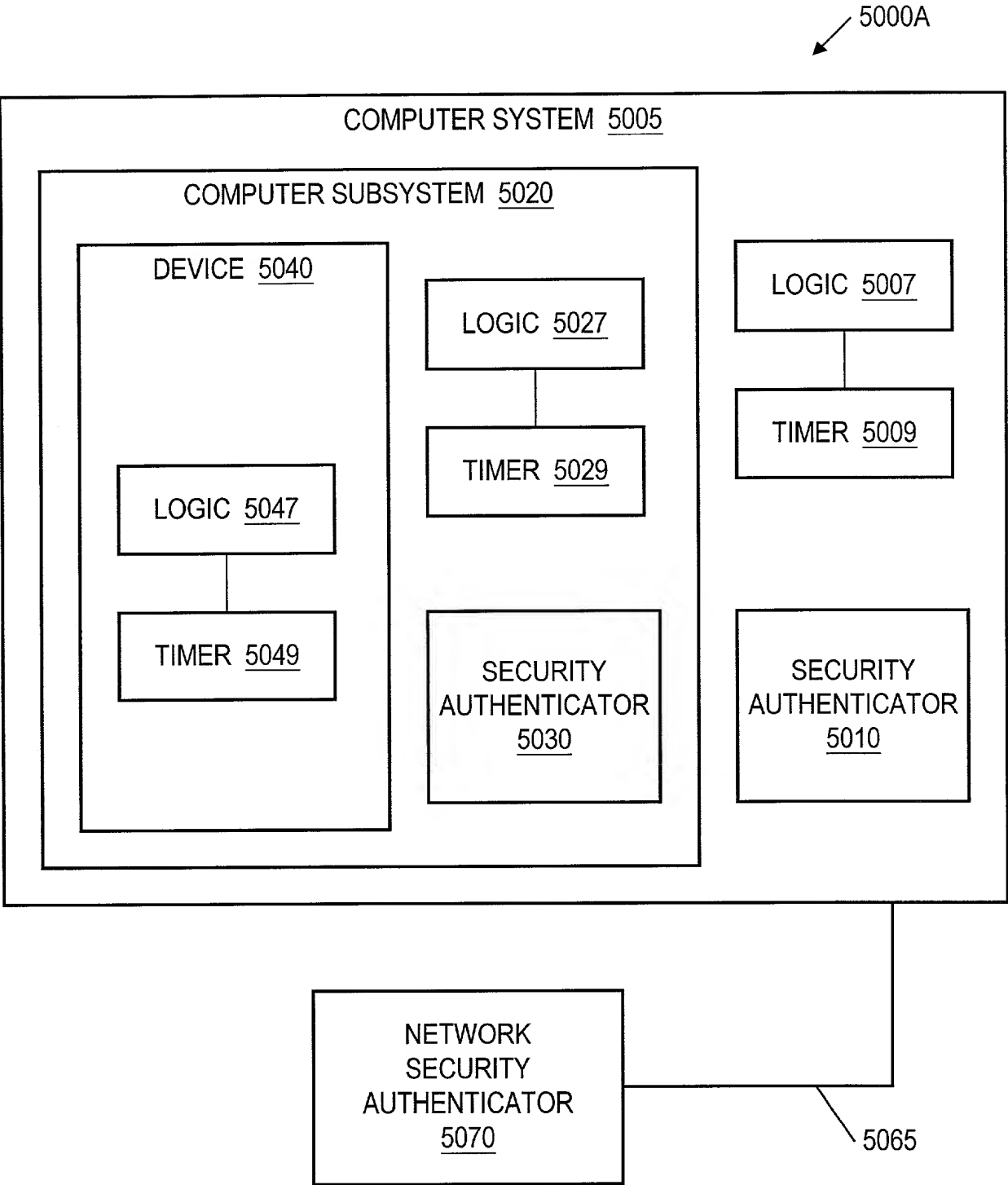


Fig. 39A

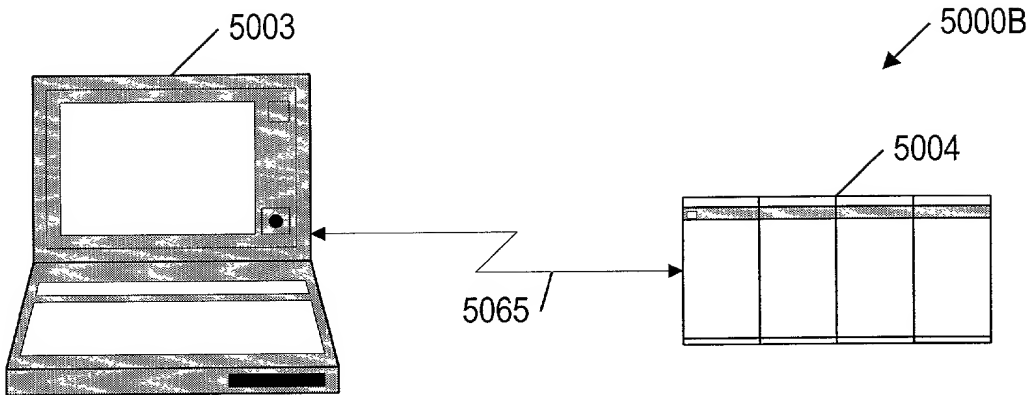


Fig. 39B

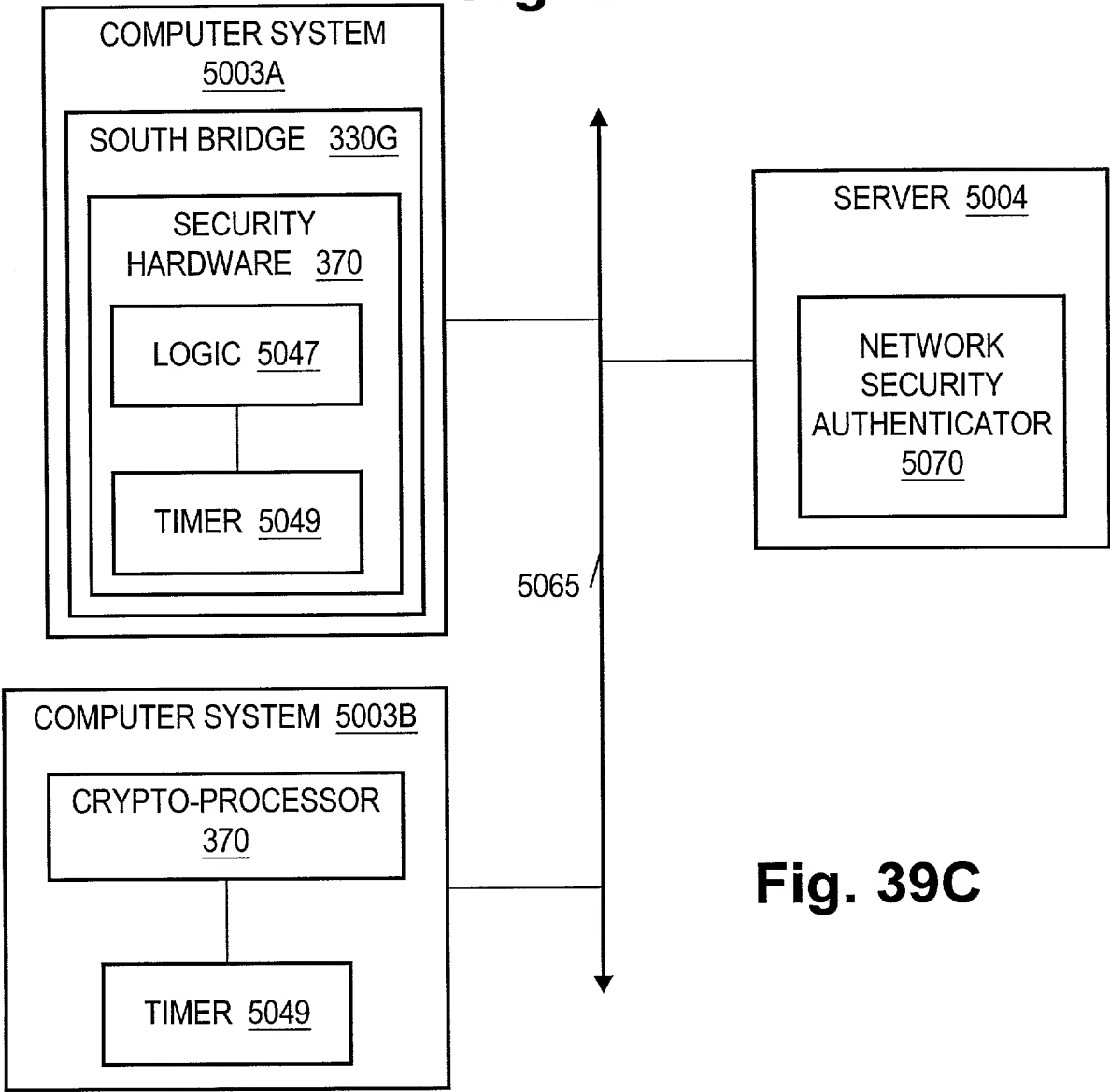


Fig. 39C

09330346

```

graph TD
    Start(( )) --> 5105[AUTHENTICATE A DEVICE, A COMPUTER SUBSYSTEM, OR A COMPUTER SYSTEM TO A COMPUTER SUBSYSTEM, A COMPUTER SYSTEM, OR A NETWORK SECURITY SYSTEM 5105]
    5105 --> A1((A))
    A1 --> 5110[SET A STARTING VALUE ON A TIMER IN RESPONSE TO SUCCESSFULLY AUTHENTICATING 5110]
    5110 --> 5115[UPDATE THE TIMER IN A PERIODIC FASHION 5115]
    5115 --> 5120{EXPIRED? 5120}
    5120 -- NO --> 5125[CONTINUE NORMAL OPERATION OF THE DEVICE, THE COMPUTER SUBSYSTEM, OR THE COMPUTER SYSTEM 5125]
    5125 --> 5115
    5120 -- YES --> 5130[RE-AUTHENTICATE THE DEVICE, THE COMPUTER SUBSYSTEM, OR THE COMPUTER SYSTEM TO THE COMPUTER SUBSYSTEM, THE COMPUTER SYSTEM, OR THE NETWORK SECURITY SYSTEM 5130]
    5130 --> 5135{SUCCESSFUL? 5135}
    5135 -- YES --> A2((A))
    5135 -- NO --> 5140[SHUT DOWN UNTIL RE-AUTHENTICATED 5140]
    5140 --> 5130
  
```

Fig. 40A

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5100B

ESTABLISH NETWORK CONNECTION TO A NETWORK SECURITY SYSTEM 5104

AUTHENTICATE A PORTABLE COMPUTER TO THE NETWORK SECURITY SYSTEM, SUCH AS DURING A BOOT PROCESS 5106

B

SET A STARTING VALUE ON A TIMER IN RESPONSE TO SUCCESSFULLY AUTHENTICATING 5110

UPDATE THE TIMER IN A PERIODIC FASHION 5115

EXPIRED?
5120

NO

CONTINUE NORMAL OPERATION OF THE PORTABLE COMPUTER 5126

YES

ATTEMPT TO ESTABLISH NETWORK CONNECTION TO THE NETWORK SECURITY SYSTEM 5129

RE-AUTHENTICATE THE PORTABLE COMPUTER TO THE NETWORK SECURITY SYSTEM 5131

SUCCESSFUL?
5135

NO

SHUT DOWN THE PORTABLE COMPUTER AND REQUIRE AUTHENTICATION DURING THE BOOT PROCESS 5141

YES

B

Fig. 40B

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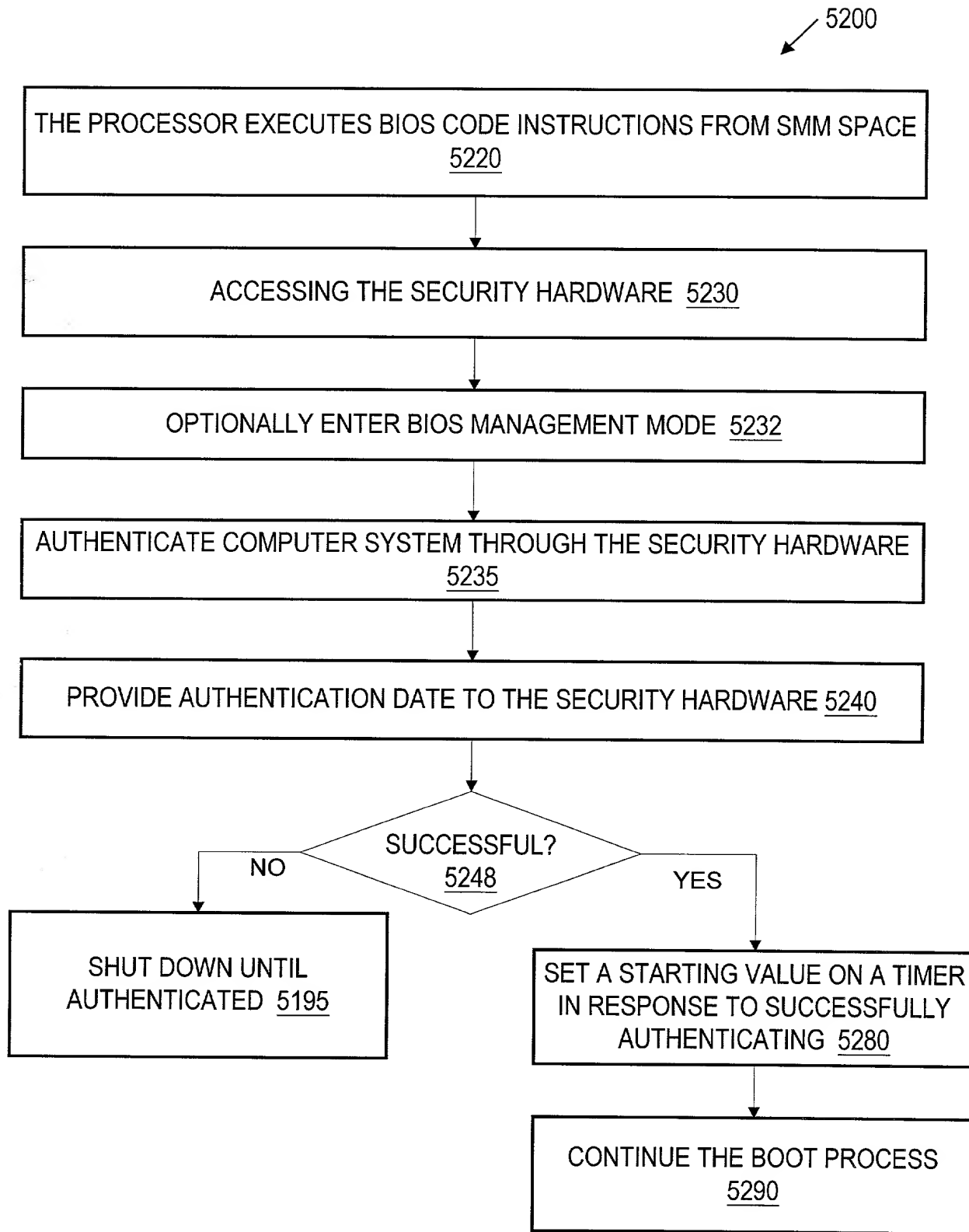


Fig. 41

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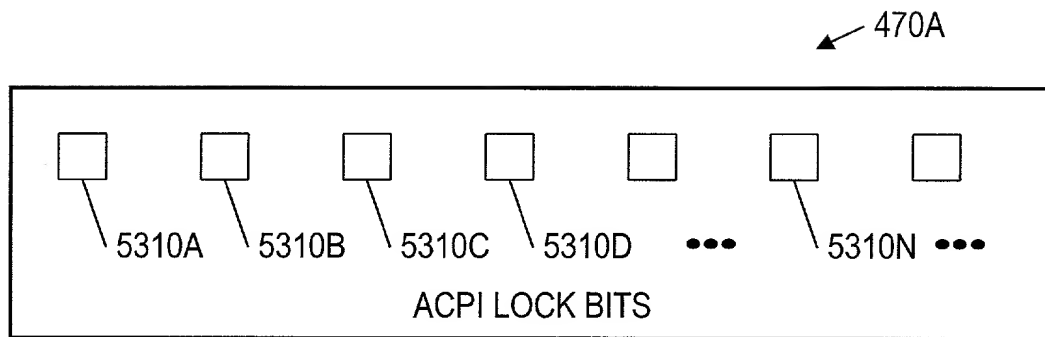


Fig. 42A

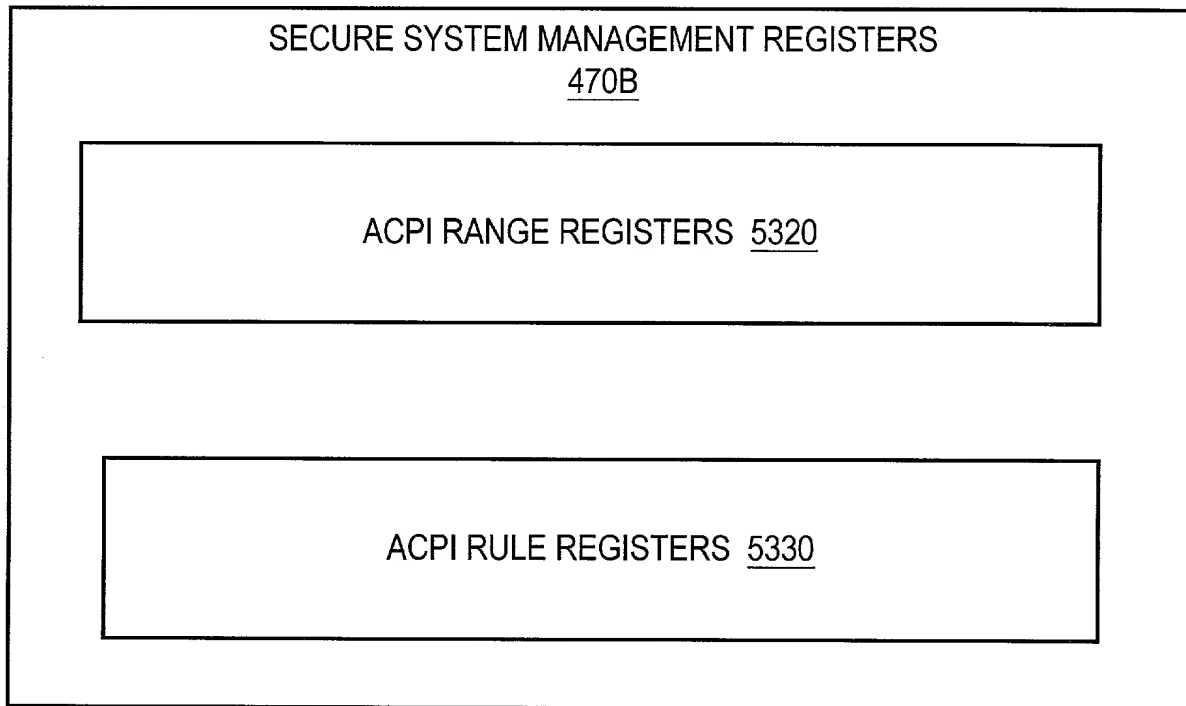


Fig. 42B